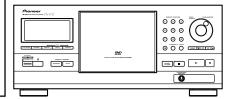


Service Manual



ORDER NO. RRV2236

DV-F07

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Type	Power Requirement Power Requirement		Power Poquirement	Region No.	Remarks
Туре			Region No.	Nemarks	
KU	0	_	AC120V	1	
KC	0	_	AC120V	1	
KU/RC	0	_	AC120V	3	
KU/CA	_	0	AC120V	1	

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1. SAFETY INFORMATION

This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

WARNING

This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to cause cancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 - Proposition 65

NOTICE

(FOR CANADIAN MODEL ONLY)

Fuse symbols — (fast operating fuse) and/or — (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE

(POUR MODÈLE CANADIEN SEULEMENT)

Les symboles de fusible — (fusible de type rapide) et/ou — (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

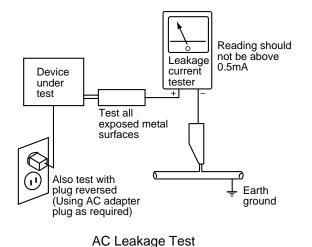
\bot (FOR USA MODEL ONLY) \bot

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (water pipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5mA.



ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a Δ on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

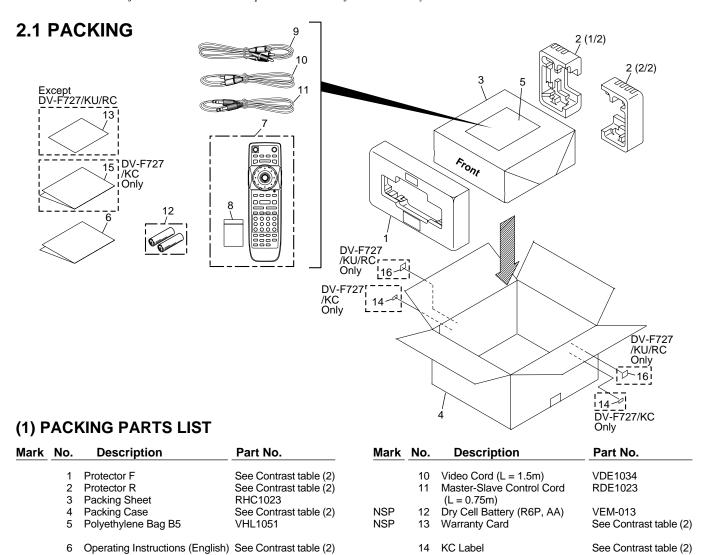
Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

2. EXPLODED VIEWS AND PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- The
 \(\Delta\) mark found on some component parts indicates the importance of the safety factor of the part.

 Therefore, when replacing, be sure to use parts of identical designation.
- Screws adjacent to ▼ mark on the product are used for disassembly.



(2) CONTRAST TABLE

Remote Control Unit

Audio Cord (L = 1.5m)

(CU-DV039)

Battery Cover

DV-F727/KU, KC, KU/RC and DV-F07/KU/CA are constructed the same except for the following :

VXX2629

VNK4423

VDE1033

Mark	No.	Symbol and Description	Part No.				
IVIAI'K	NO.	Symbol and Description	DV-F727 /KU	DV-F727 /KC	DV-F727 /KU/RC	DV-F07 /KU/CA	Remarks
	1	Protector F	PHA1325	PHA1325	PHA1325	PHA1336	
	2	Protector R	PHA1326	PHA1326	PHA1326	PHA1337	
	4	Packing Case	VHG1840	VHG1840	VHG1840	VHG1841	
	6	Operating Instructions (English)	VRB1237	VRB1237	VRB1237	VRB1238	
NSP	13	Warranty Card	ARY7023	ARY7024	Not used	ARY1026	
	14	KC Label	Not used	VRW1716	Not used	Not used	
	1	Operating Instructions (French)	Not used	VRC1107	Not used	Not used	
	16	Region Label	Not used	Not used	VRW1702	Not used	

Operating Instructions (French)

Region Label

15

See Contrast table (2)

See Contrast table (2)

2.2 EXTERIOR SECTION (1/2) 23 26 DV-F07 Only 28 DV-F727 Only DV-F07 Only 25 0 DV-F727/KU and DV-F07/KU/CA Only 28 DV-F727 Only DV-F07 Note *1 Part of Center Pole Cut Cut / 29 14 Only 29 DV-F727/KU/RC Refer to Only "2.3 EXTERIOR SECTION (2/2)". DV-F07 19 Only from PS2B Assy "2.5 FRONT PANEL SECTION".

Only

(1) EXTERIOR SECTION (1/2) PARTS LIST

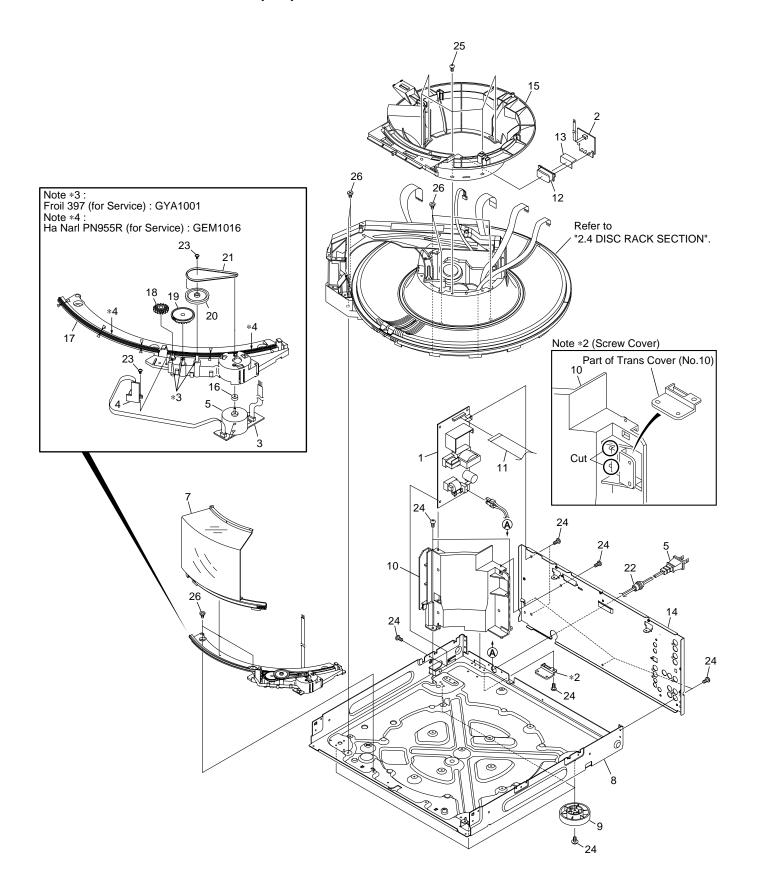
Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
	1	DVDM Assy	See Contrast table (2)		16	PCB Spacer	VEC2077
	2	232B Assy	See Contrast table (2)	NSP	17	Main Holder	VNE2215
	3	VQEB Assy	VWV1669		18	Side MoleR	See Contrast table (2)
	4	MSJB Assý	See Contrast table (2)		19	Side Mole L	See Contrast table (2)
	5	AVJB Assy	See Contrast table (2)	NSP	20	Spacer	See Contrast table (2)
	6	MDRB Assy	VWG2127		21	Caution Label 301	VRW1817
	7	Flexible Cable (12P)	VDA1779		22	65 Label	See Contrast table (2)
	8	Flexible Cable (7P)	See Contrast table (2)		23	Bonnet Case S	See Contrast table (2)
	9	Flexible Cable (11P)	VDA1781		24	Side Wood L	See Contrast table (2)
	10	Flexible Cable (12P)	VDA1778		25	Side Wood R	See Contrast table (2)
	11	Flexible Cable (7P)	VDA1782		26	Wood Collar	See Contrast table (2)
	12	Flexible Cable (14P)	VDA1707		27	Screw	See Contrast table (2)
	13	Flexible Cable (15P)	VDA1784		28	Screw	See Contrast table (2)
	14	Screw (#4-40/M2)	See Contrast table (2)		29	Screw	BBZ30P080FZK `´
	15	PCB Support Cushion	VEC2079		30	Screw	IPZ30P080FMC
					31	Region Label	See Contrast table (2)

(2) CONTRAST TABLE

DV-F727/KU, KC, KU/RC and DV-F07/KU/CA are constructed the same except for the following :

Morle	Na	Symbol and Description		Part	No.		Remarks
Mark	No.	Symbol and Description	DV-F727 /KU	DV-F727 /KC	DV-F727 /KU/RC	DV-F07 /KU/CA	Remarks
	1	DVDM Assy	VWS1386	VWS1386	VWS1386	VWS1396	
	2	232B Assy	Not used	Not used	Not used	VWG2129	
		MSJB Assy	VWG2131	VWG2131	VWG2131	VWG2128	
	5	AVJB Assy	VWV1719	VWV1719	VWV1719	VWV1720	
	8	Flexible Cable (7P)	Not used	Not used	Not used	VDA1777	
	14	Screw (#4-40/M2)	Not used	Not used	Not used	DBA1078	
	18	Side Mole R	Not used	Not used	Not used	PAN1374	
	19	Side Mole L	Not used	Not used	Not used	PAN1373	
NSP	20	Spacer	Not used	Not used	Not used	PNM1331	
	22	65 Label	ARW7050	Not used	Not used	ARW7050	
	23	Bonnet Case S	VXX2692	VXX2692	VXX2692	VXX2693	
	24	Side Wood L	Not used	Not used	Not used	PMM1043	
	25	Side Wood R	Not used	Not used	Not used	PMM1044	
	26	Wood Collar	Not used	Not used	Not used	PNW1238	
	27	Screw	Not used	Not used	Not used	PBA1103	
	28	Screw	FBT40P080FZK	FBT40P080FZK	FBT40P080FZK	Not used	
	31	Region Label	Not used	Not used	VRW1703	Not used	

2.3 EXTERIOR SECTION (2/2)



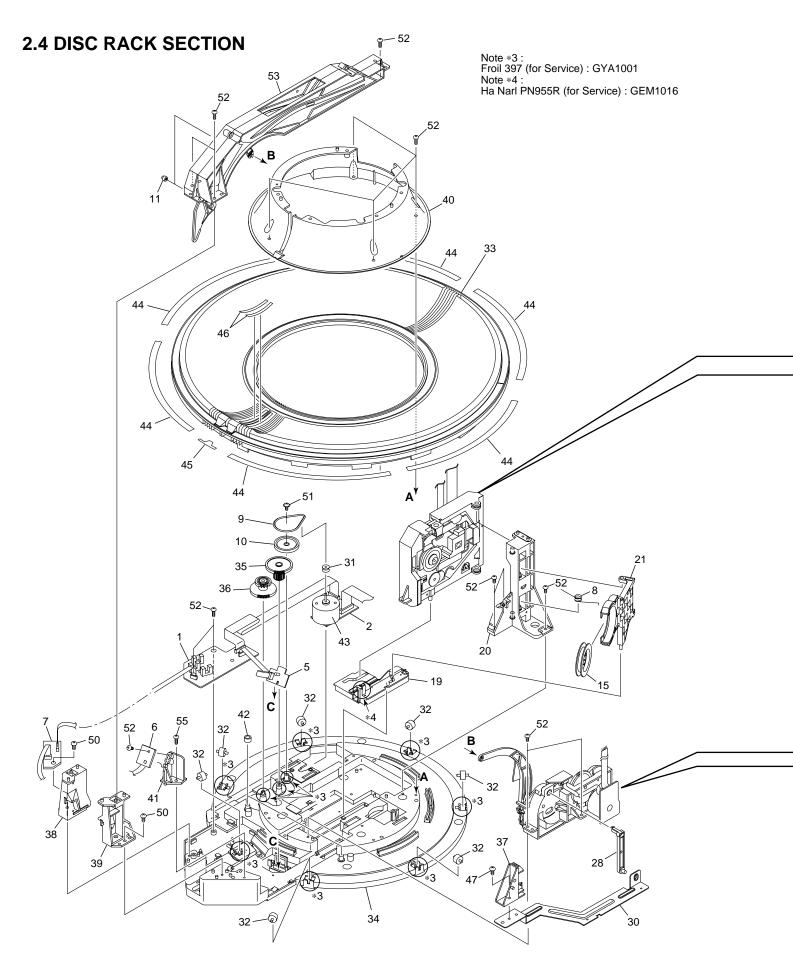
(1) EXTERIOR SECTION (2/2) PARTS LIST

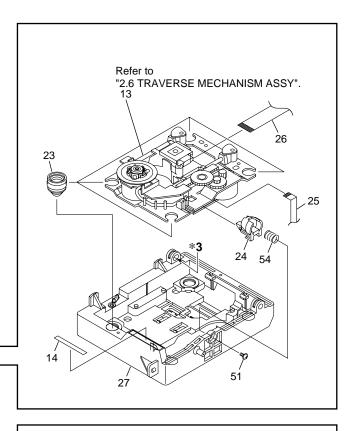
Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
\triangle	1	POWER SUPPLY Assy	VWR1317		16	Motor Pulley	PNW1634
NSP	2	LEDB Assy	VWG2124		17	Hood Base 301	PNW2791
NSP	3	DOMB Assy	VWG2121		18	Gear M1	PNW2800
NSP	4	DOSB Assy	VWG2122		19	Gear AW	PNW2906
Δ	5	AC Power Cord	ADG7024		20	Gear Pulley	VNL1662
	6	Carriage Motor (DOOR)	VXM1033		21	Belt	PEB1300
	7	Hood	See Contrast table (2)		22	Cord Stopper	CM-22C
NSP	8	Under Base DVD	VNA2125		23	Screw	IPZ20P080FMC
	9	Insulator	PNW2766		24	Screw	BBZ30P080FZK
	10	Trans Cover	VNK4542		25	Screw	IPZ30P080FMC
	11	Flexible Cable (26P)	VDA1776		26	Screw C	PBA1106
	12	CR Lens	PNW2816				
	13	Dispersion Sheet	VEC2113				
	14	Rear Base	See Contrast table (2)				
	15	Center Pole 301	PNW2792				

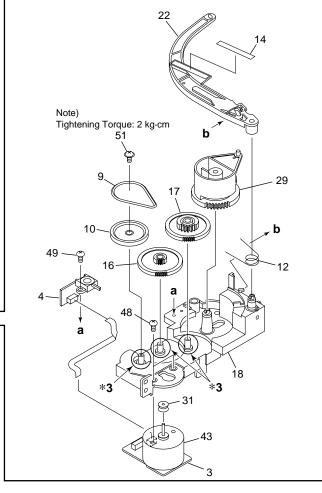
(2) CONTRAST TABLE

DV-F727/KU, KC, KU/RC and DV-F07/KU/CA are constructed the same except for the following :

Mark No.	Symbol and Description		Part	No.		Remarks	
IVIAIR	NO.	Symbol and Description	DV-F727 /KU	DV-F727 /KC	DV-F727 /KU/RC	DV-F07 /KU/CA	Remarks
		Hood Rear Base	VNK4531 VNA2126	VNK4531 VNA2126	VNK4531 VNA2126	VNK4532 VNA2127	

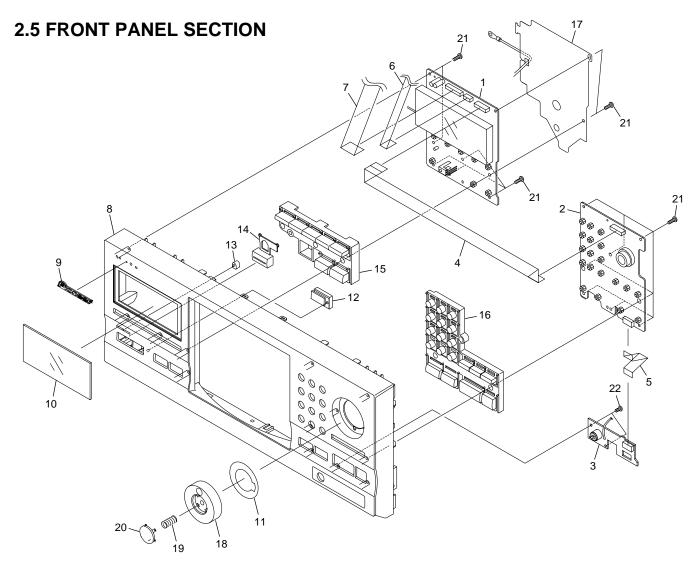






• DISC RACK SECTION PARTS LIST

● DI.	эс г	NACK SECTION PA	K I S LIS I
Mark	No.	Description	Part No.
NSP NSP NSP NSP NSP		SSRB Assy SEMB Assy LOMB Assy LOSB Assy RADB Assy	VWG2113 VWG2114 VWG2115 VWG2116 VWG2117
NSP NSP	6 7 8 9 10	PHOB Assy VOLB Assy Clamp Spring Loading Belt Gear Pulley (B)	VWG2118 VWG2123 VBH1318 AEB7029 ANW7062
NSP	11 12 13 14 15	-1 3	ANW7075 PBH1226 VWT1161 PED1028 VXA2382
	16 17 18 19 20		PNW2819 PNW2820 PNW2822 PNW2823 PNW2826
	21 22 23 24 25	Float Rubber A Balancer	PNW2827 PNW2829 AEB7063 VNL1842 VDA1785
	26 27 28 29 30	Flexible Cable (24P) Float Base Link L Drive Cam Lock Plate	VDA1780 VNL1841 PNW2844 PNW2873 PNA2438
	31 32 33 34 35	Motor Pulley Roller Disc Rack Rack Base ST Gear 0.6	PNW1634 PNW2647 PNW2790 PNW2835 PNW2836
	36 37 38 39 40	Guide Support L Guide Support R	PNW2837 PNW2838 PNW2839 PNW2840 PNW2841
	41 42 43 44	Sensor Stay Guide Roller Carriage Motor (SELECT, LOADING) Rack Label	PNW2842 PNW2843 VXM1033 PAM1770
	45 46 47 48 49	S Label +1 Label Screw Screw Screw	PAM1771 PRW1507 BBZ30P080FZK BMZ26P040FZK BPZ26P060FMC
	50 51 52 53 54	Screw Screw Screw Arm Assy Float Spring	BPZ30P100FCU IPZ20P080FMC PPZ30P080FMC PXA1615 VBH1319
	55	Screw	IPZ30P080FMC



(1) FRONT PANEL SECTION PARTS LIST

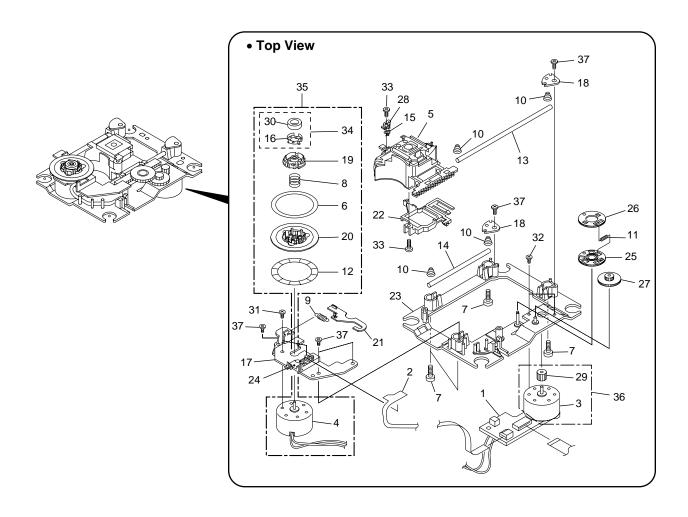
Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
	1	FLKY Assy	See Contrast table (2)		11	JOG Sheet	PEC1042
NSP	2	KEYB Assy	VWG2120		12	Sensor Lens	PNW2804
	3	PS2B Assy	VWG2125		13	LED Lens	PNW2019
	4	Flexible Cable (11P)	VDA1787		14	Power Button	VNK4527
	5	Flexible Cable (7P)	VDA1792		15	Mode Button DVD	VNK4525
	6	Flexible Cable (7P)	VDA1786		16	Play Button DVD	VNK4526
	7	Flexible Cable (15P)	VDA1775		17	PCB Cover	PNM1324
	8	Operation Panel	See Contrast table (2)		18	JOG Dial	PAC1882
	9	Pioneer Badge	See Contrast table (2)		19	Enter Spring	PBH1228
	10	Display Window	PAM1782		20	Enter Button	PAC1883
					21	Screw	PPZ30P100FMC
					22	Screw	PPZ30P050FMC

(2) CONTRAST TABLE

DV-F727/KU, KC, KU/RC and DV-F07/KU/CA are constructed the same except for the following :

Mark	No.	Symbol and Description		Remarks			
IVIAIK	NO.	Symbol and Description	DV-F727 /KU	DV-F727 /KC	DV-F727 /KU/RC	DV-F07 /KU/CA	Remarks
	1	FLKY Assy	VWG2126	VWG2126	VWG2191	VWG2119	
	8	Operation Panel	VNK4529	VNK4529	VNK4529	VNK4530	
	9	Pioneer Badge	PAM1776	PAM1776	PAM1776	PAN1376	

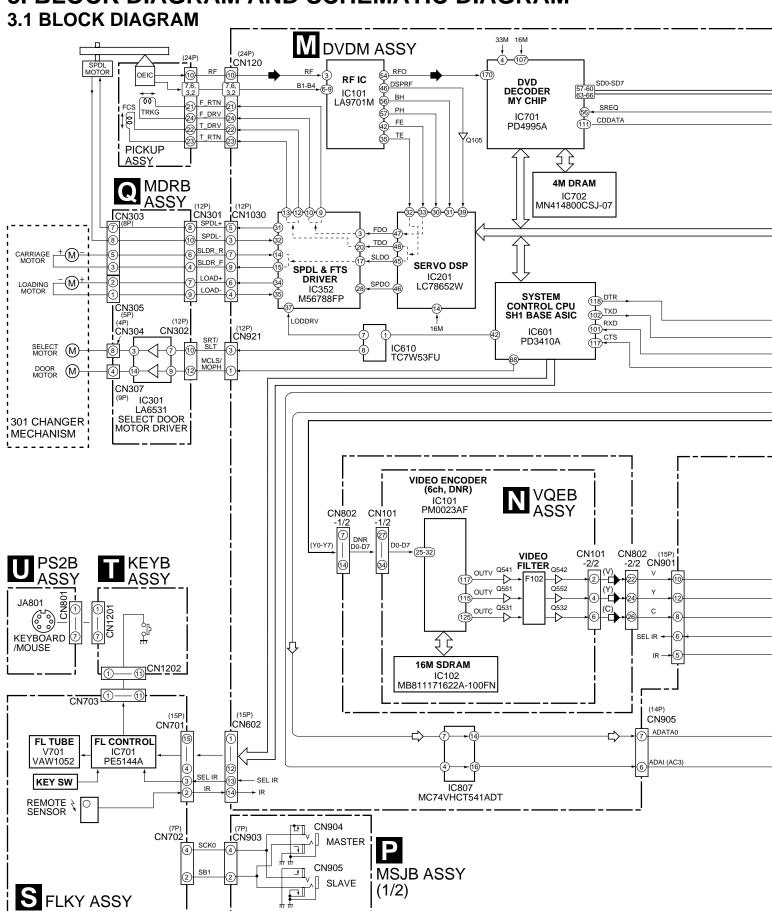
2.6 TRAVERSE MECHANISM ASSY

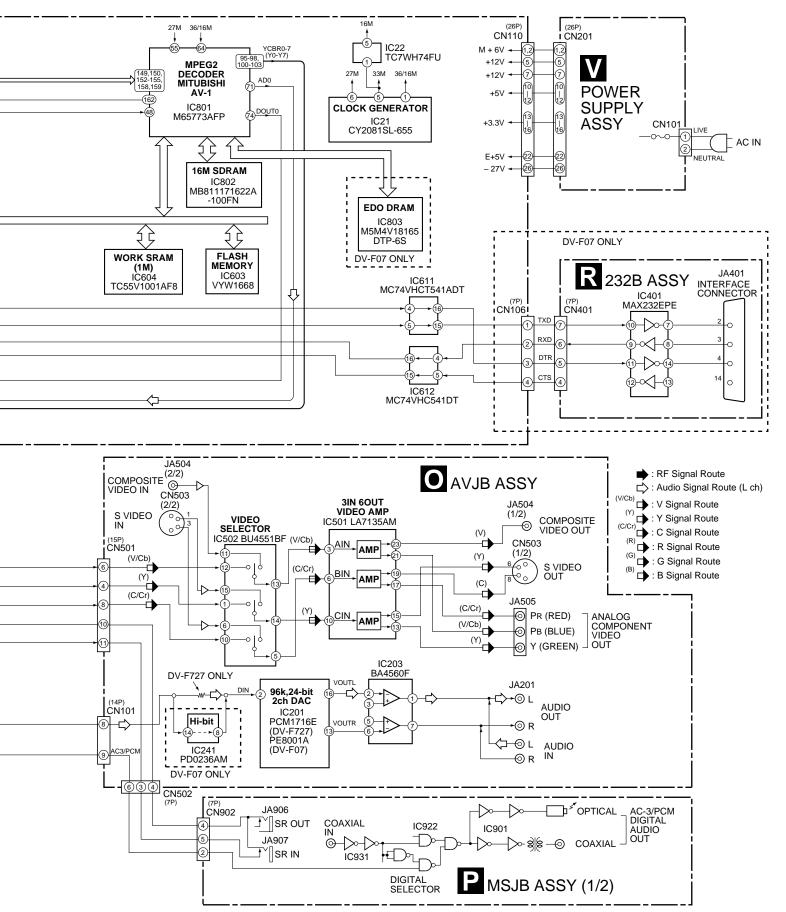


• TRAVERSE MECHANISM ASSY PARTS LIST

Mark 1	No.	Description	Part No.	<u>Mark</u>	No.	Description	Part No.
NSP	1	SMEB Assy	VWG2048		21	Hook	VNL1770
NSP	2	FGSB Assy	VWG2009		22	FFC Holder	VNL1802
	3	Motor	VXM1079		23	Mechanism Base	VNL1806
	4	Motor	VXM1078		24	FG Holder	VNL1807
⚠ NSP	5	Pickup Assy	VWY1055		25	Gear A	VNL1808
	6	Table Sheet	DEC2040		26	Gear B	VNL1809
	7	Screw	VBA1058		27	Gear C	VNL1810
	8	Centering Spring	VBH1278		28	Slider	VNL1811
	9	Hook Spring	VBH1317		29	Gear D	VNL1814
	10	Skew Spring	VBH1303	NSP	30	Magnet	VYM1024
	11	Gear Spring	VBH1308		31	Screw	JFZ17P025FZK
NSP	12	Reflected Sheet	VEC1959		32	Screw	JGZ17P028FMC
	13	Guide Bar	VLL1504		33	Screw	VBA1051
	14	Sub-guide Bar	VLL1505		34	Magnet Holder Assy	VXX2507
	15	Hold Spring	VNC1017		35	Spindle Motor Assy	VXX2649
NSP	16	Magnet Holder	VNE2070		36	Carriage Motor Assy	VXX2650
NSP	17	Motor Base	VNE2154		37	Screw	PBA1069
NSP	18	Cover	VNE2155				
	19	Centering Ring	VNL1746				
NSP	20	Disc Table	VNL1747				

3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM



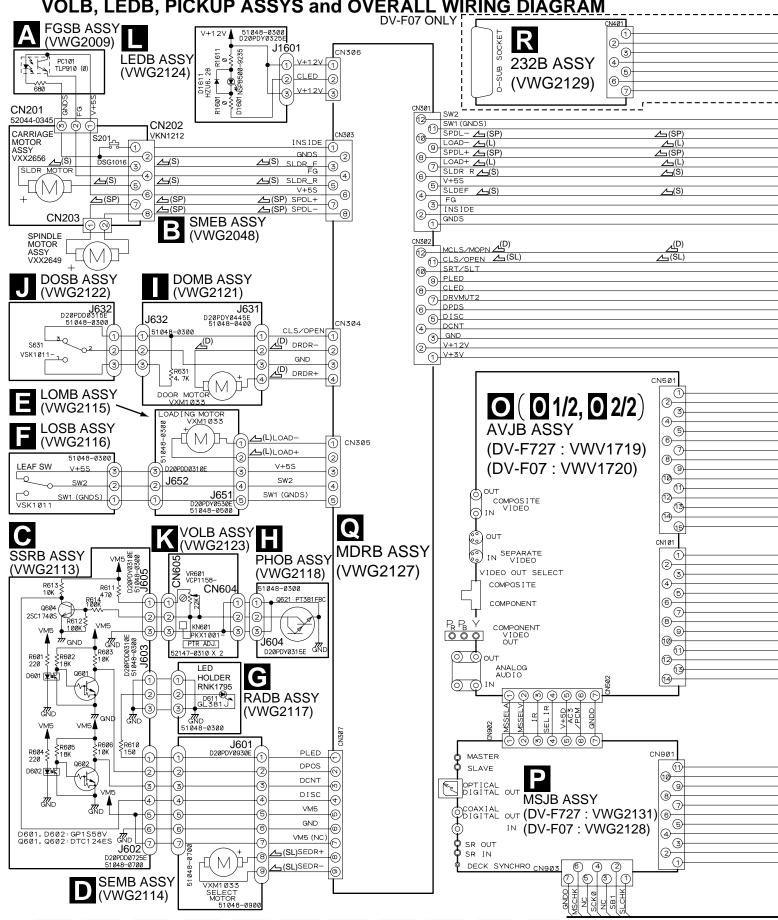


C

D

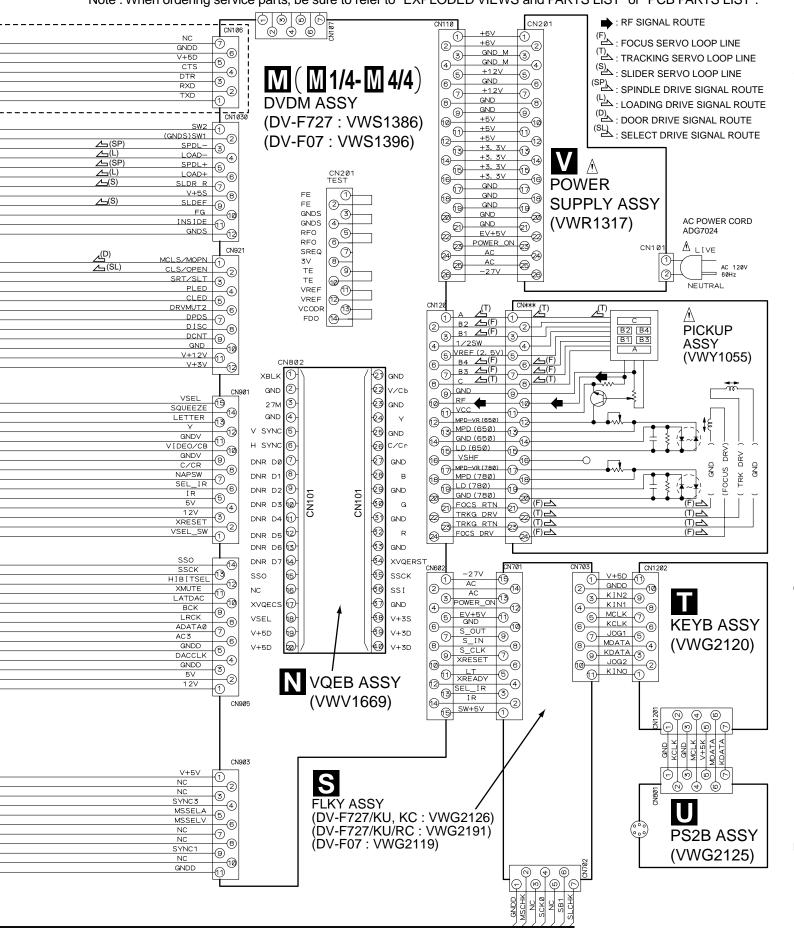
3.2 FGSB, SMEB, SSRB, SEMB, LOMB, LOSB, RADB, PHOB, DOMB, DOSB, VOLB, LEDB, PICKUP ASSYS and OVERALL WIRING DIAGRAM

3



14 A B C D E E G H II J K L

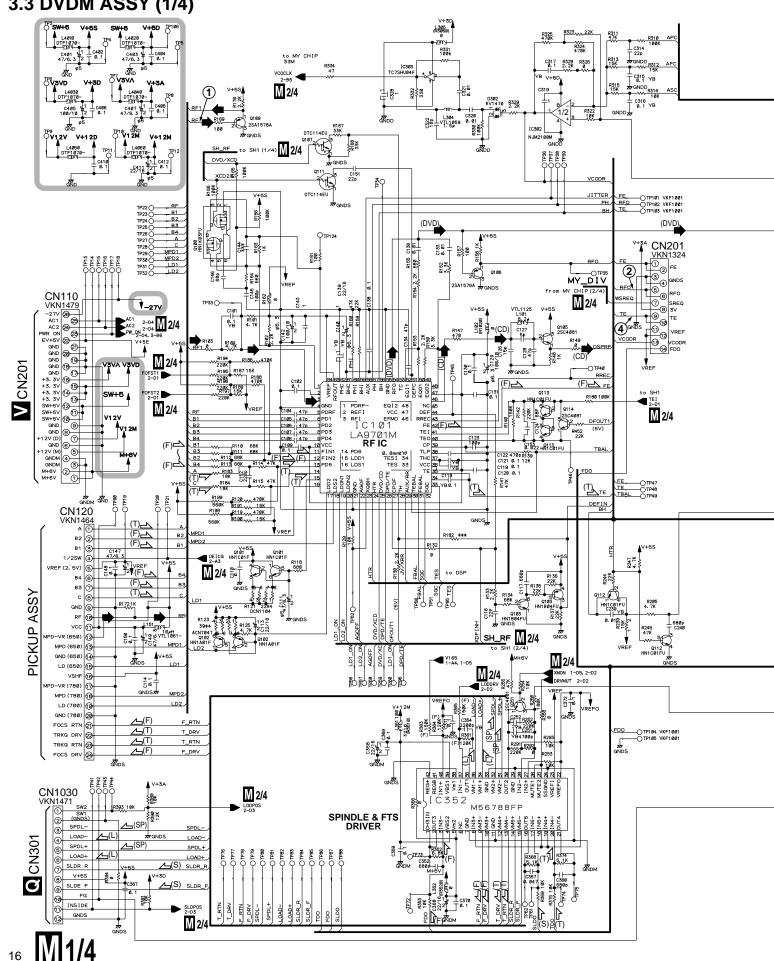
Note: When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".



С

D

3.3 DVDM ASSY (1/4)



3

4

2

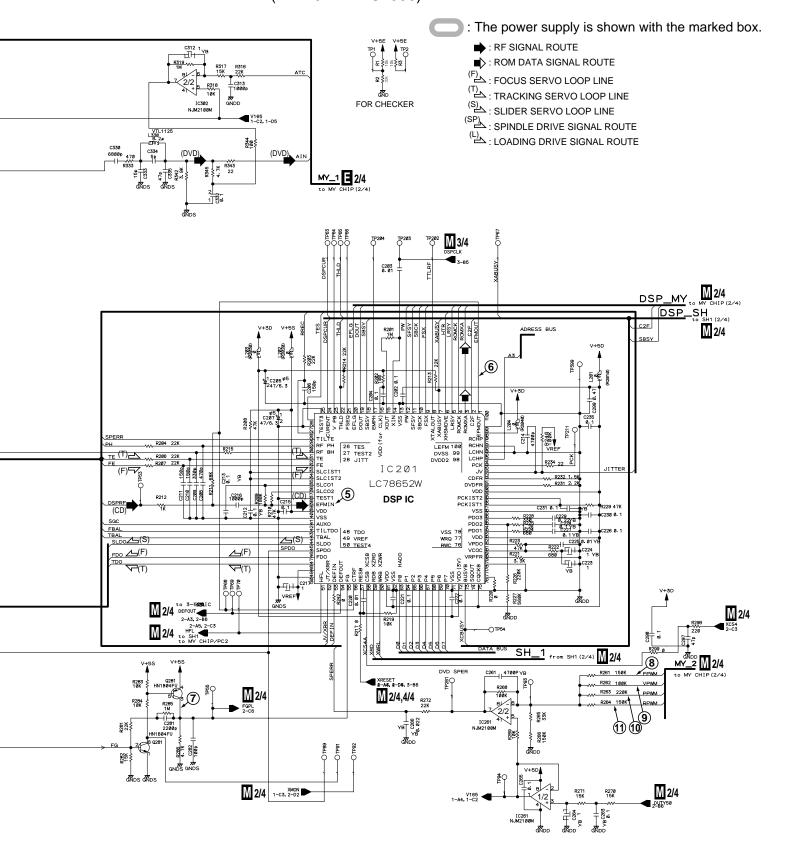
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1/4 DVDM ASSY (DV-F727 : VWS1386) (DV-F07 : VWS1396)

6

5

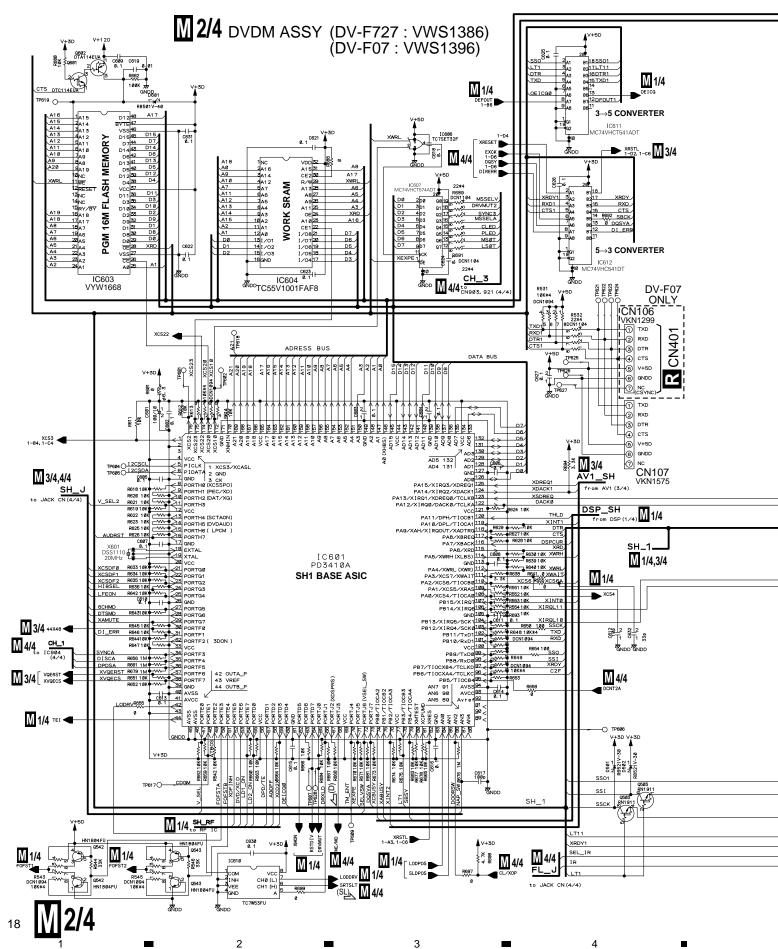
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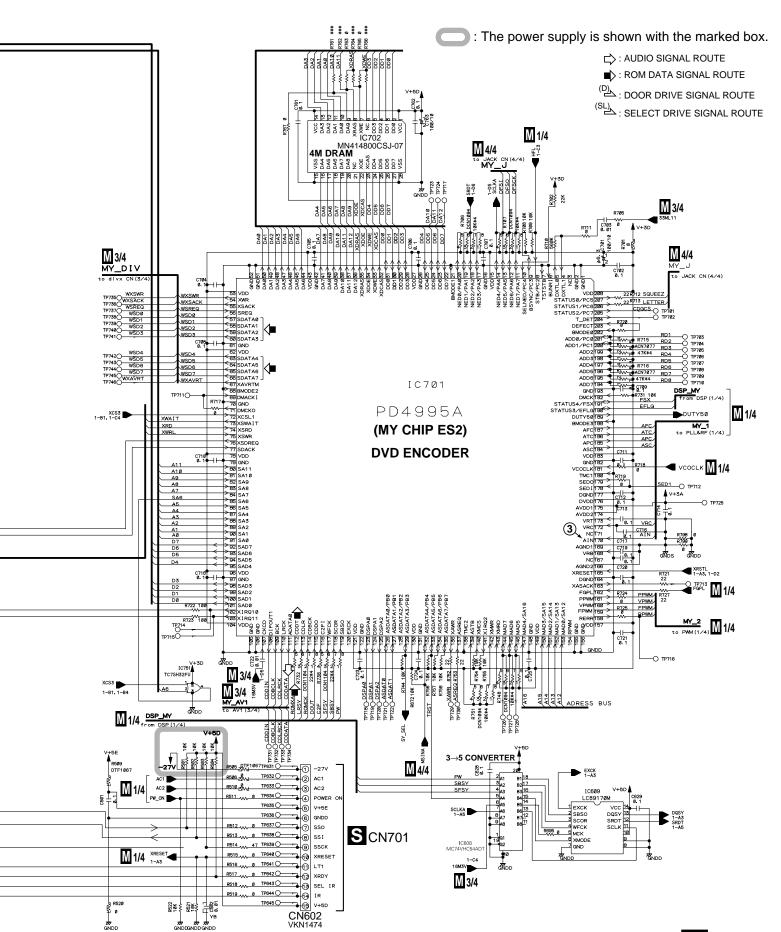
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D

3.4 DVDM ASSY (2/4)



D



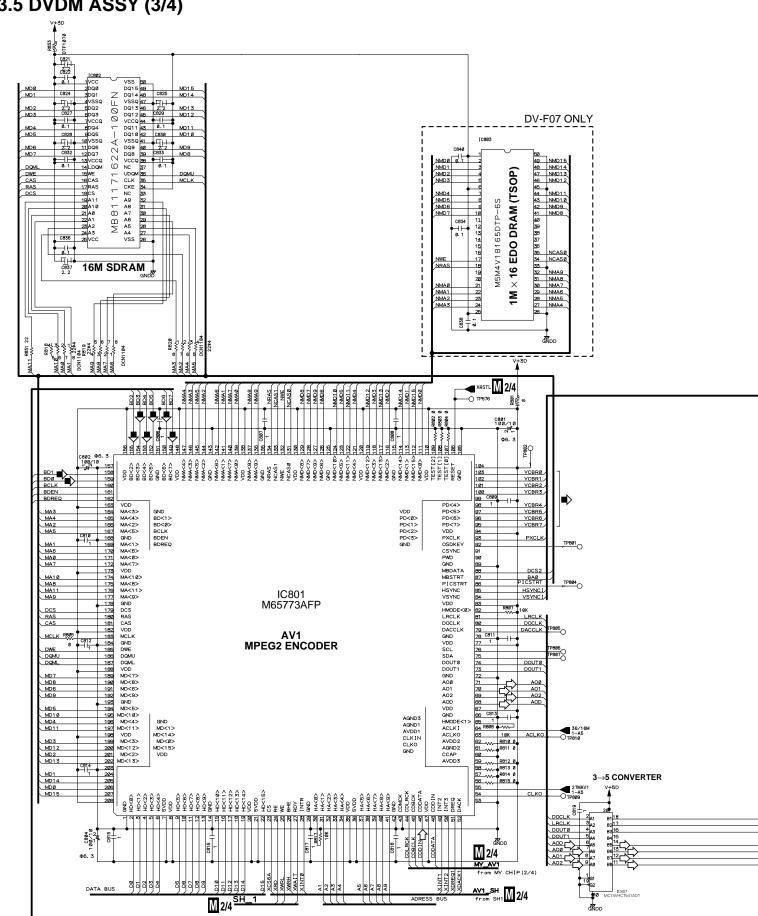
2/4

5

5

6

3.5 DVDM ASSY (3/4)



3

M3/4

D

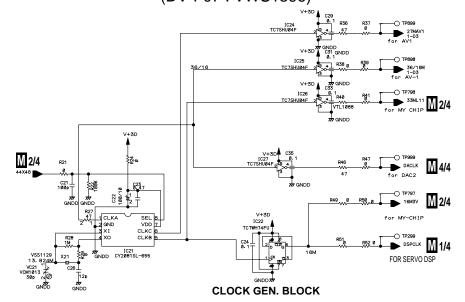
В

2

3

3/4 DVDM ASSY (DV-F727 : VWS1386) (DV-F07 : VWS1396)

5



6

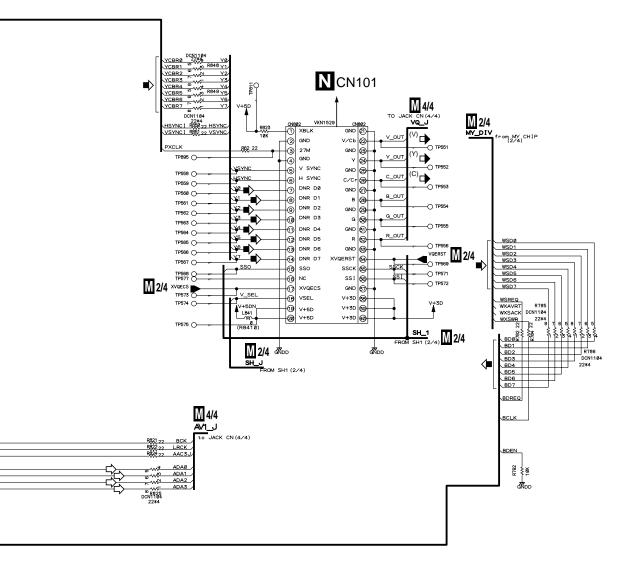
: ROM DATA SIGNAL ROUTE (V) ☐ : VIDEO SIGNAL ROUTE (Y) ☐ : Y SIGNAL ROUTE

В

С

D

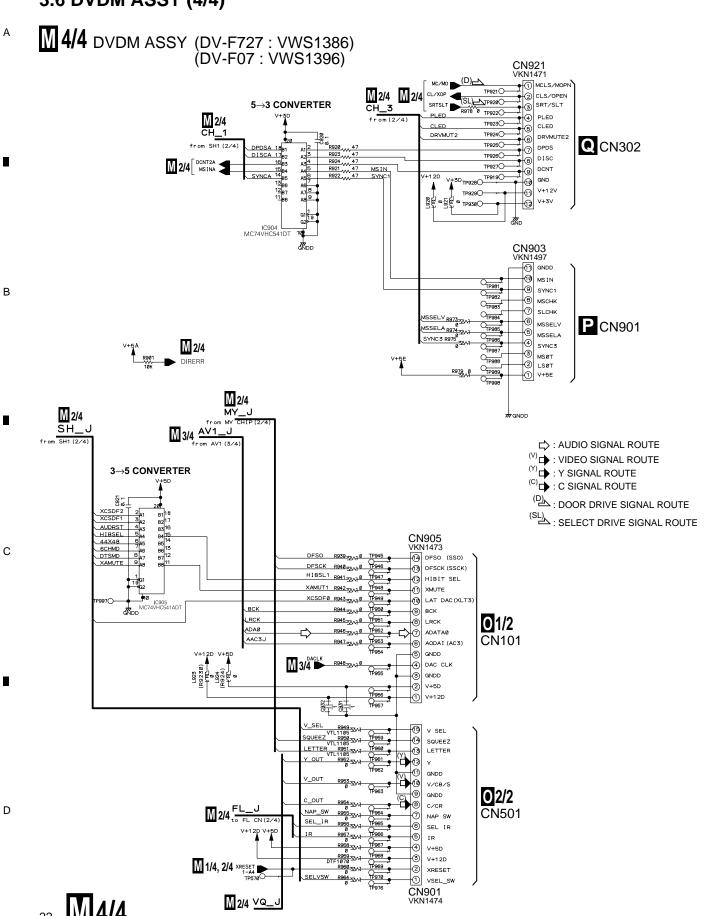
(C) : C SIGNAL ROUTE



5

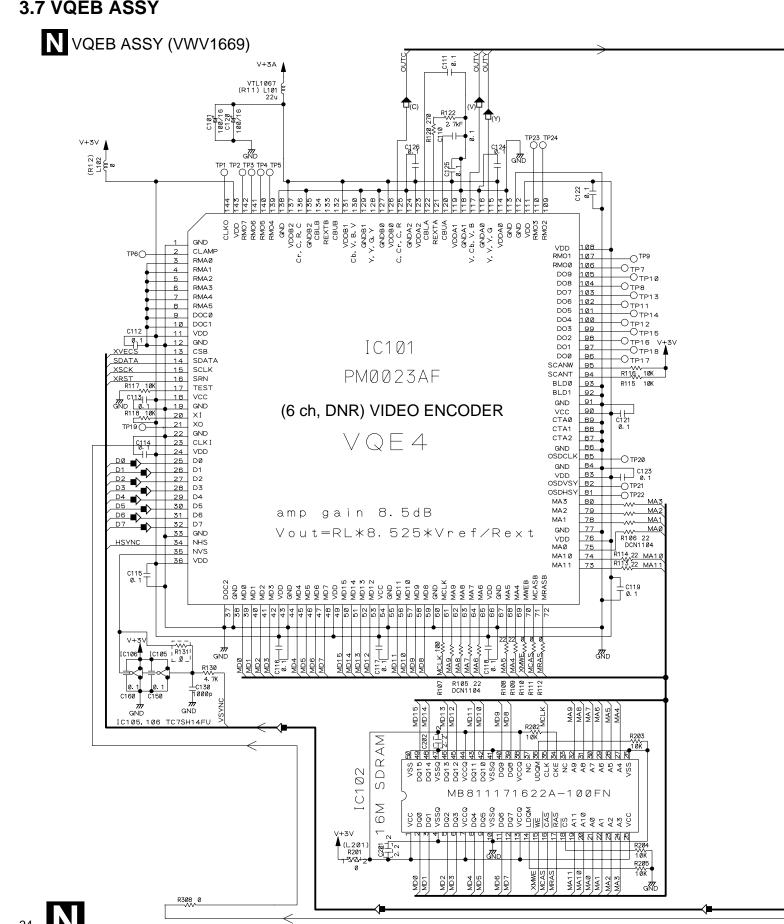
6

3.6 DVDM ASSY (4/4)



2

3.7 VQEB ASSY



3

2

D

7

: The power supply is shown with the marked box. : ROM DATA SIGNAL ROUTE (V) ☐ : VIDEO SIGNAL ROUTE (Y) ☐ : Y SIGNAL ROUTE (C) ☐ : C SIGNAL ROUTE M 3/4 V+5VE **↓** CN101 CN802 2 XBLK m GND GND dND OUT_ C532 ② GND V/Cb R401_{~~}22 23 27M GND 3 3,6 OUT_C OUT_Y GND 4 0531 VSYNC R402, 22 Ø v sync GND (5) HSYNC R403_W 22 OUT_C 29 H SYNC 6 C/Cr DNR DØ (7) GND OUT_E V+5VE ONR D1 8 В ONR D2 9 GND OUT_C M DNR D3 1 G ①,④ _{Q541} 3 DNR D4 GND 1 OUT_R 3 DNR D5 R/C 13 3 DNR D6 GND (3) from SH 37pin R416 M DNR D7 XVQERST Q542 SDATA R406 \$8542 }330° ⊕ sso SSCK from SH 38pin XVECS R407_{VV} 22 39 DNRCS **6** SSI ③ VECS GND 3 VSEL 13 V+3D V+5VE V+5V (R501) R409 **39** ∨+5 19 V+3D 2,5 F102 V+3D **∞** Q551 VKN1530 Q552 ďNo 40pin \$8552 330_F BtoB connector 杰 Q531, Q532, Q541, Q542, Q551, Q552 : 2PB709A (QR) F102 : VTF1155

6

6

5

5

2

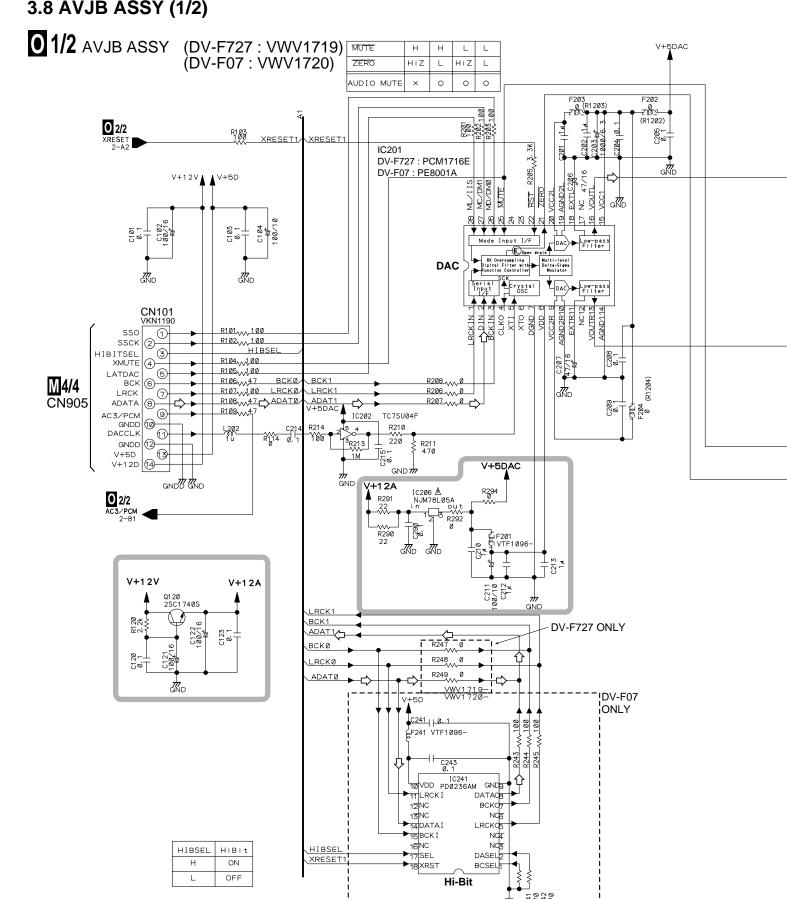
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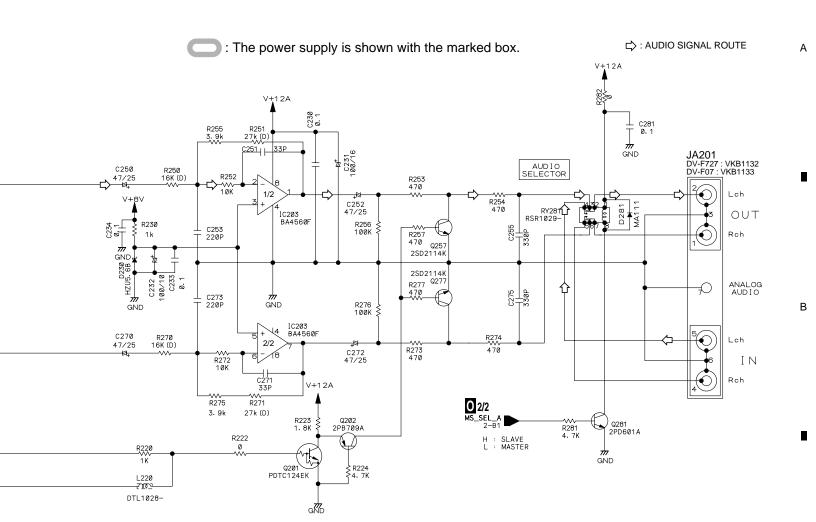
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3

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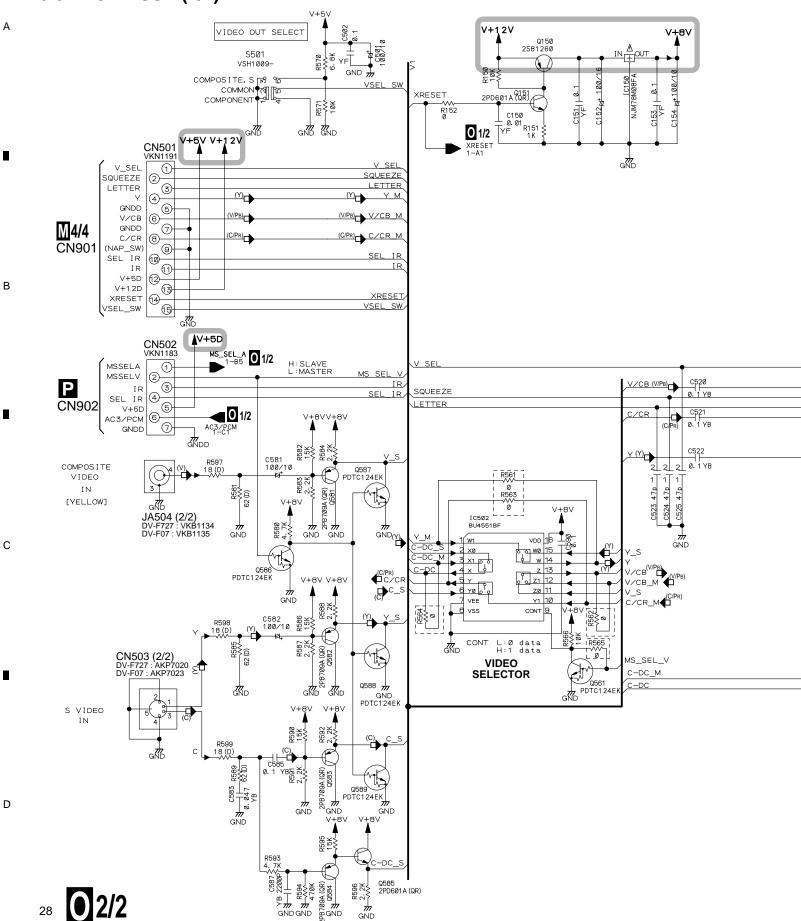
0 1/2 27

С

D

7

-



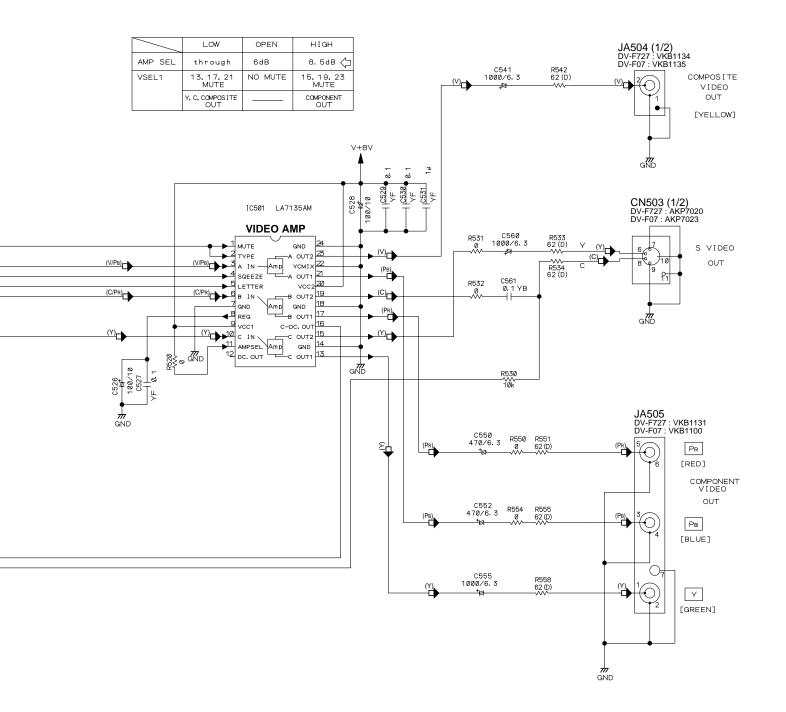
Q 2/2 AVJB ASSY (DV-F727 : VWV1719) (DV-F07 : VWV1720)

5

(VPB) : VIDEO/PB SIGNAL ROUTE
(Y) □ : Y SIGNAL ROUTE
(C/PR) : C/PR SIGNAL ROUTE

: The power supply is shown with the marked box.

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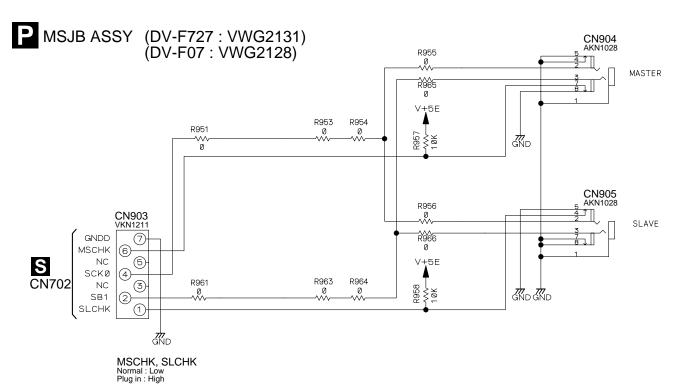
6

2

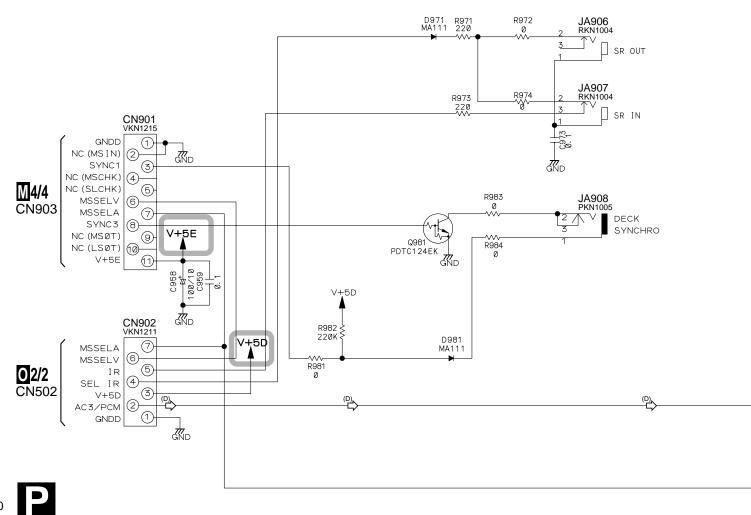
3.10 MSJB ASSY

С

D



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3

Α

В

С

D

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: The power supply is shown with the marked box. V+5D V+5D JA902 GP1F32T F901 AC3/PCM F912 VTF1096 OPTICAL OUT R911 C915 1 100/10 R908_{vw}1. 5K IC901 IC901 TC74HCU04AF 100/10 6 C916 GND GND IC901 IC901 IC901 R907 0 L902 RTF1167-JA901 DV-F727 : VKB1077 DV-F07 : VKB1074 C902 IC901 L901 PTL1003 R901 100/10 1k AC3/PCM COAXIAL [BLACK] C903 0.01 OUT R906 C905 C906 0.1 0.1 ĝψ. GND # GND GND V+5D DIGITAL AUDIO V+5D VTF1096 SELECTOR JA903 DV-F727 : VKB1077 DV-F07 : VKB1074 R932 IC922 R934 IC931 AC3/PCM 10 R921 IC922 COAXIAL [BLACK] IC931 TC74HCU04AF IC922 TC74HC00AF ΙN dN_D C936 弘D 弧D IC922 0.1 GND ďND ďND IC931 IC931 IC931 IC931 R920 ďND

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5

SLAVE MASTER

6

3

GND 6 7

GNDM

NC (VM5)

SEDR+ (8)

SEDR-(9)

В

С

D

2

3

R303

GNOM GNOM

R304

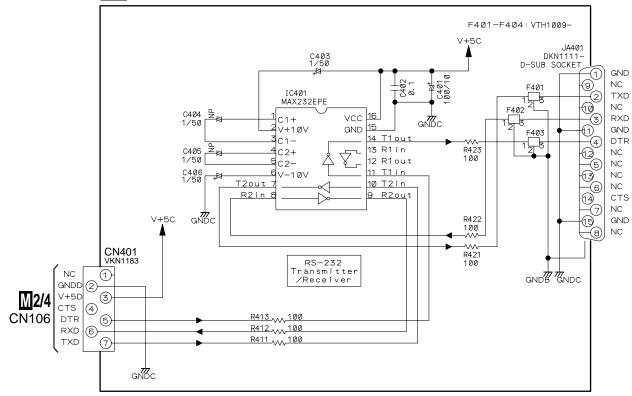
R305 R306 R307

3.12 232B ASSY (DV-F07 ONLY)

1

R 232B ASSY (VWG2129)

2



2

R

3

33

В

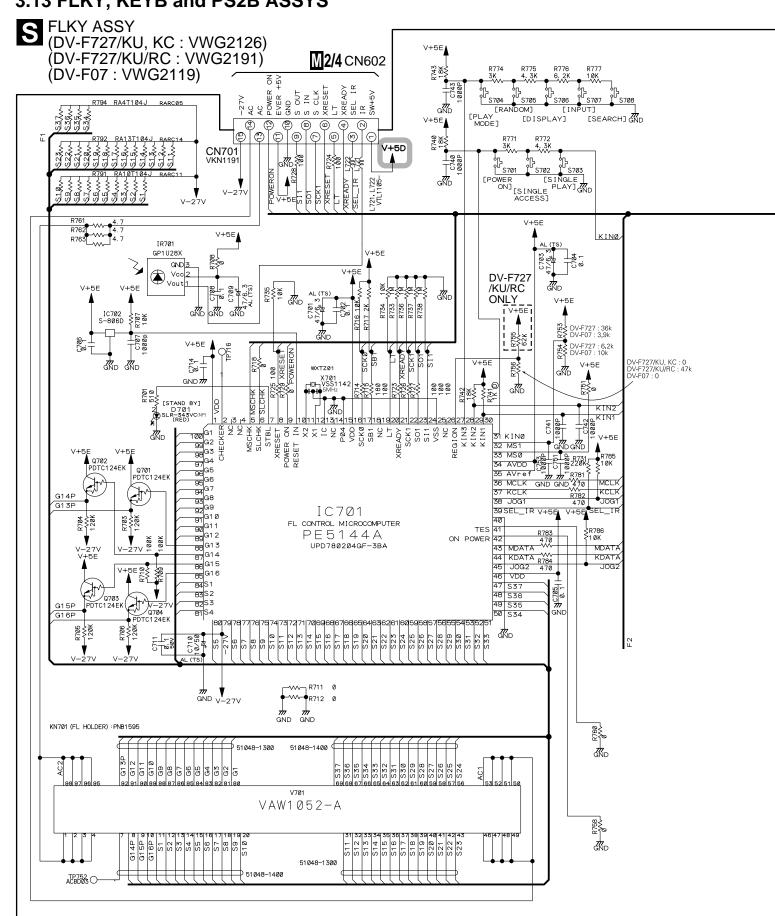
С

D

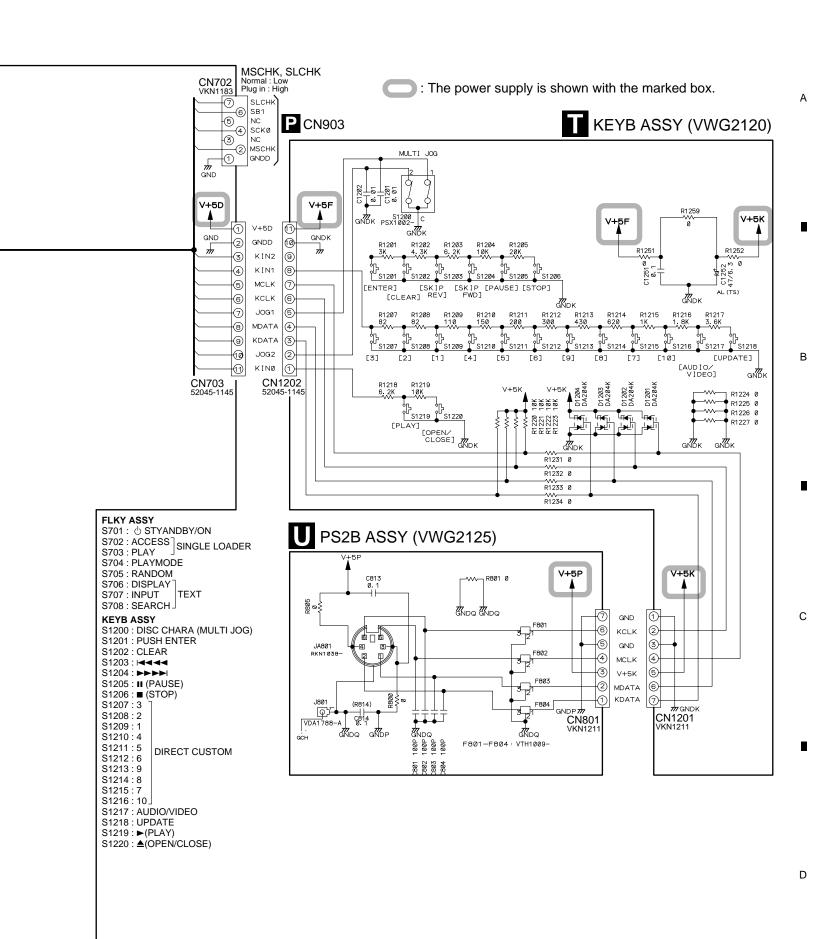
С

D

3.13 FLKY, KEYB and PS2B ASSYS

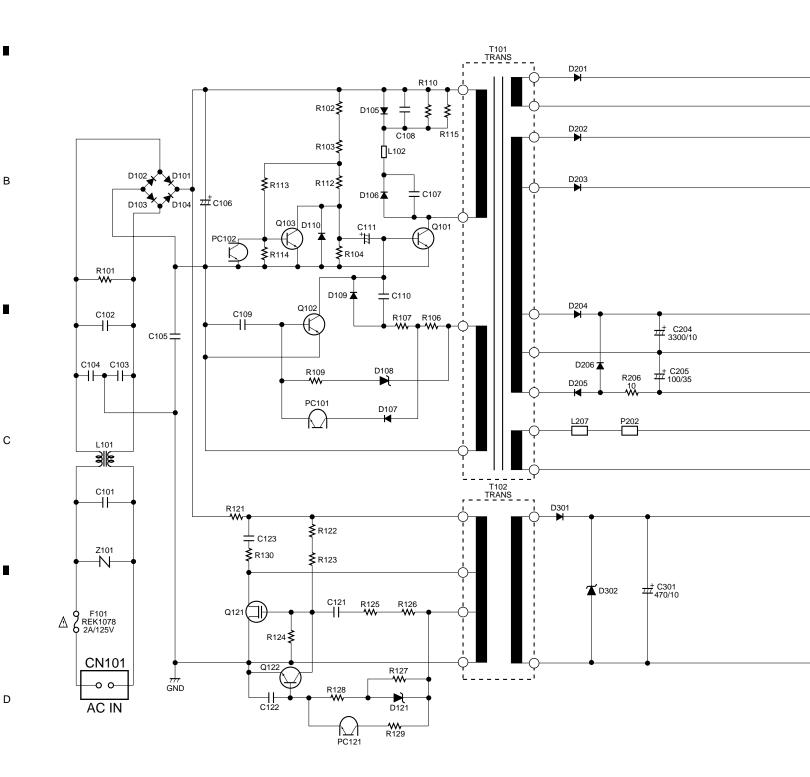


2



3.14 POWER SUPPLY ASSY

V POWER SUPPLY ASSY (VWR1317)



« NOTE OF SPARE PARTS IN POWER SUPPLY (SYPS) ASSY »

- In case of repairing, use the described parts only to prevent an accident.
- Please write the red \checkmark mark on the board when the primary section of POWER SUPPLY (SYPS) Assy is repaired.

7

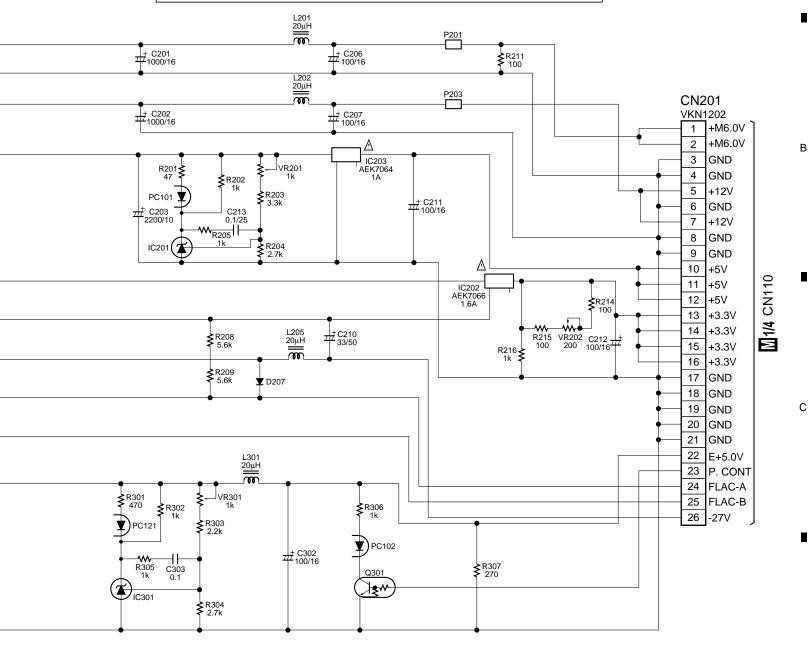
• Please take care to keep the space, not touching other parts when replacing the parts.

6

• NOTE FOR FUSE REPLACEMENT

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CAUTION -FOR CONTINUED PROTECTION AGAINST RISK OF FIRE. REPLACE WITH SAME TYPE AND RATINGS ONLY.



V

37

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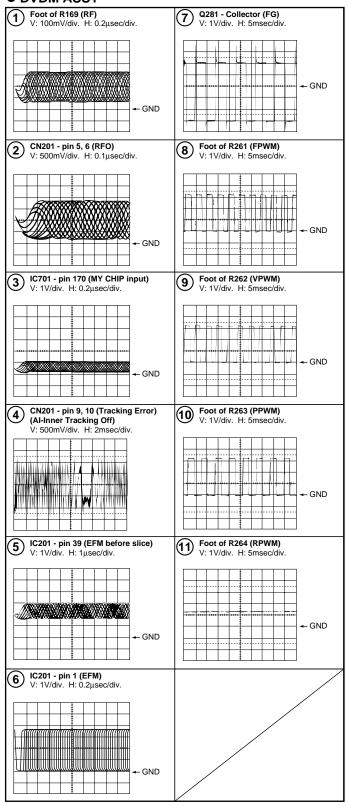
WAVEFORMS

Note: The encircled numbers denote measuring point in the schematic diagram.

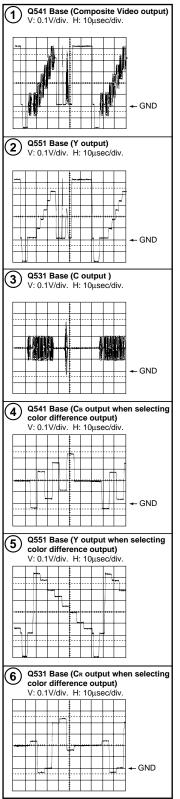
Measurement condition:

No. 1 to 4 and 6 to 11 : Disc MA1, Title 1-chp 1
No. 5 : CD, ABEX-784 Track 1
No. 12 to 14 : MJK1, Title 1-chp 4 or T2-1
No. 15 to 17 : MJK1, Title 1-chp 5 or T2-19
No. 18 to 20 : T2-19, Color-bar (WY and WV Types only)

DVDM ASSY



VQEB ASSY



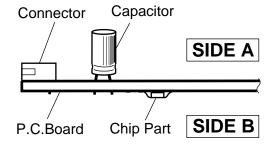
4. PCB CONNECTION DIAGRAM

NOTE FOR PCB DIAGRAMS:

- 1. Part numbers in PCB diagrams match those in the schematic diagrams.
- 2. A comparison between the main parts of PCB and schematic diagrams is shown below.

Symbol In PCB Diagrams	Symbol In Schematic Diagrams	Part Name
000 B C E	B C E B C E	Transistor
• <u>(0 0 0</u> B C E	B C E B C E	Transistor with resistor
000 D G S		Field effect transistor
@00\\000\d	******	Resistor array
000		3-terminal regulator

- 3. The parts mounted on this PCB include all necessary parts for several destinations.
 - For further information for respective destinations, be sure to check with the schematic diagram.
- 4. View point of PCB diagrams.

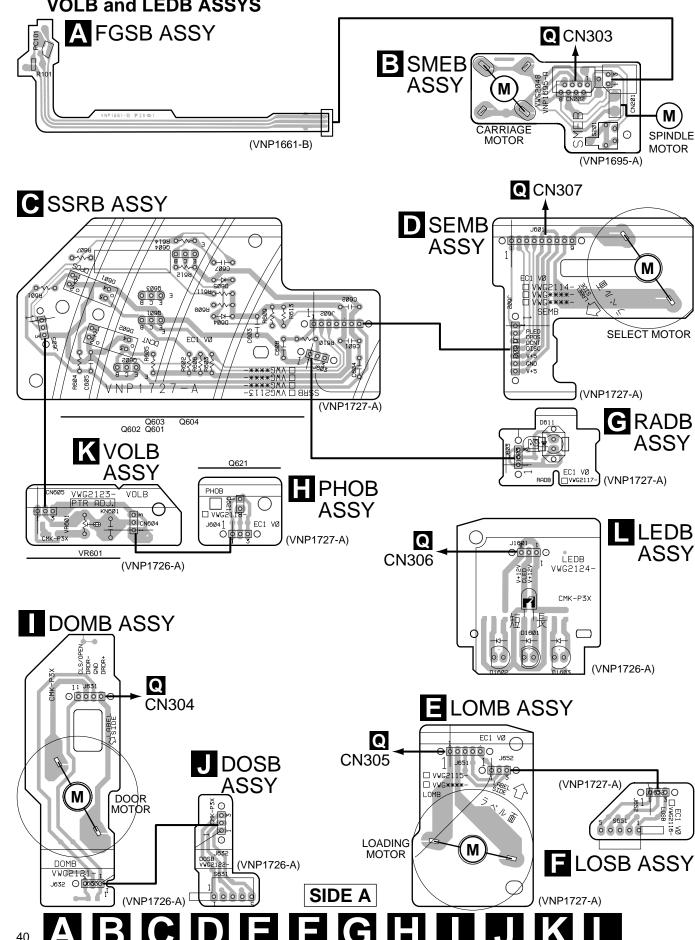


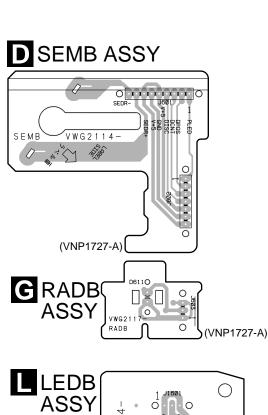
В

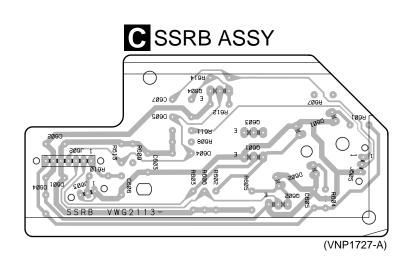
С

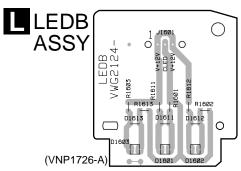
D

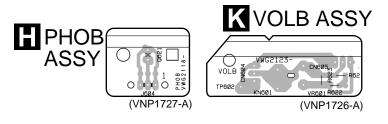
4.1 FGSB, SMEB, SSRB, SEMB, LOMB, LOSB, RADB, PHOB, DOMB, DOSB, VOLB and LEDB ASSYS

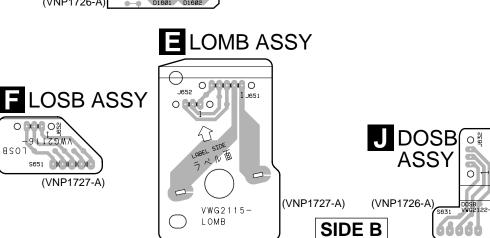




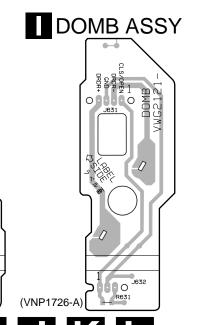








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D

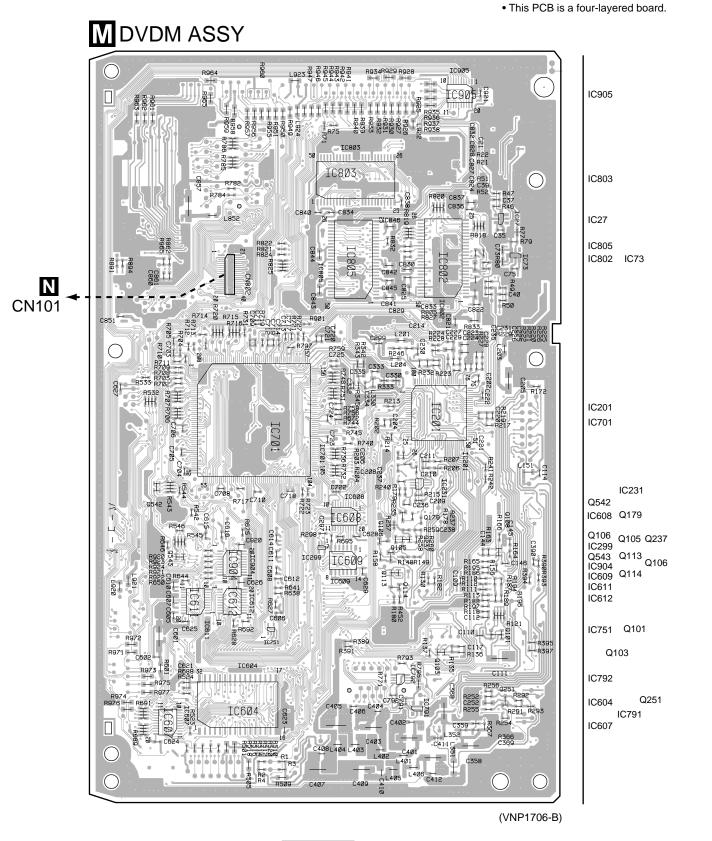
SIDE A

42

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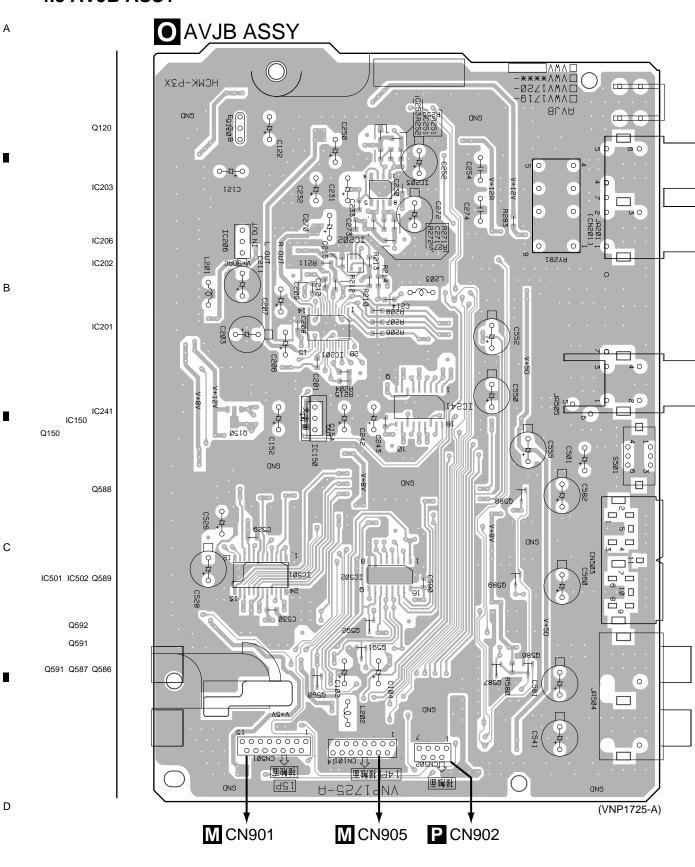
I

3

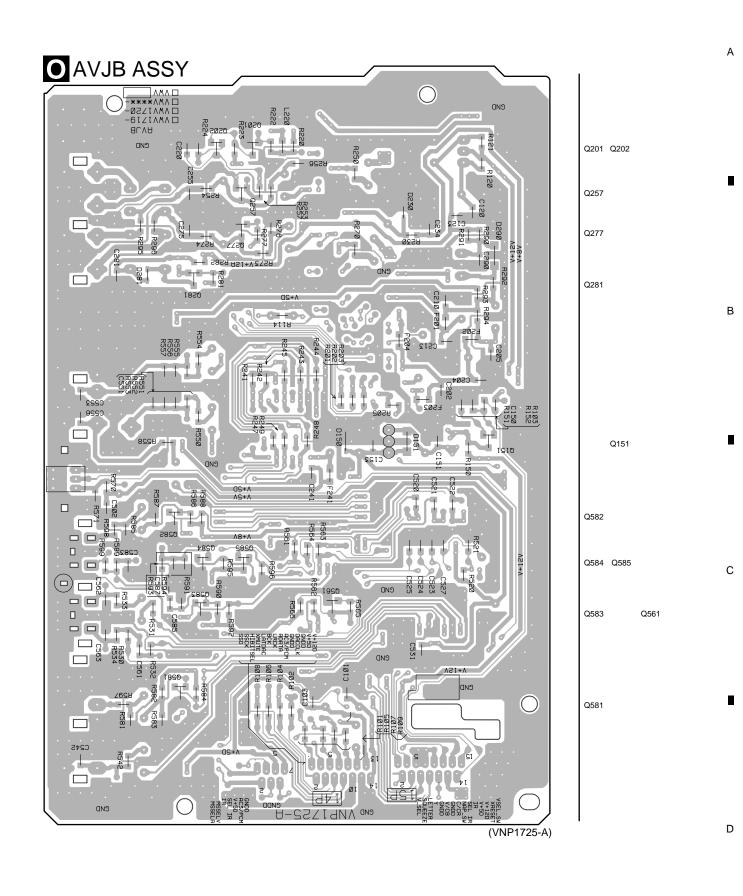


SIDE B

4.3 AVJB ASSY



SIDE A



SIDE B



4.4 VQEB ASSY

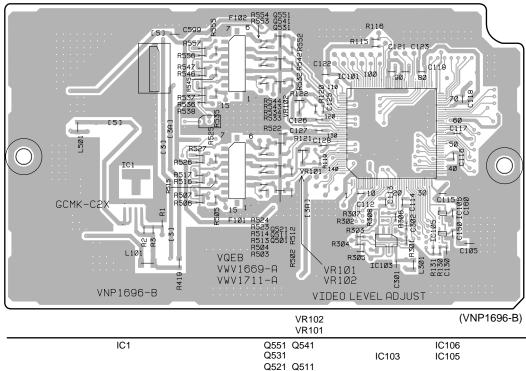
N VQEB ASSY

SIDE A

В

С

D



Q501

3

N VQEB ASSY

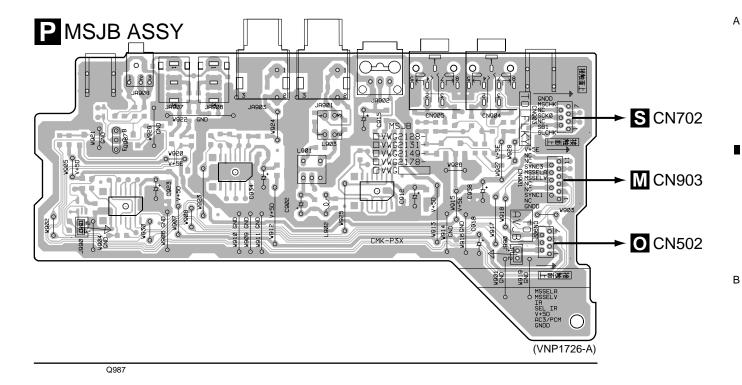
SIDE B

U-[[Z[AMA U-699[AMA 8-969I4NV cioi C102 C10t C20S c215 C25S R599 - C124 __ C225 SS RSØ1 C245 __ c225 [2] (VNP1696-B) M CN802 IC102

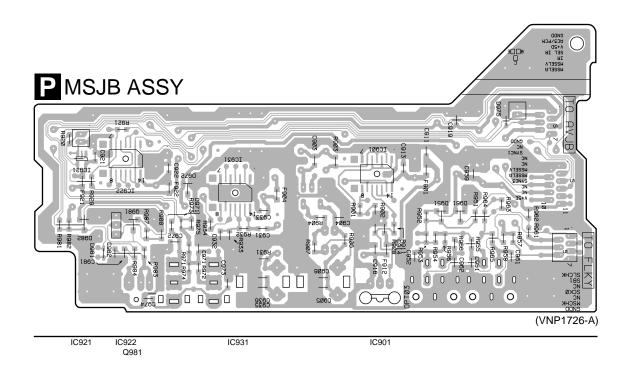
Q502 Q512 Q522 IC104 Q532 Q542 Q552

2

4.5 MSJB ASSY



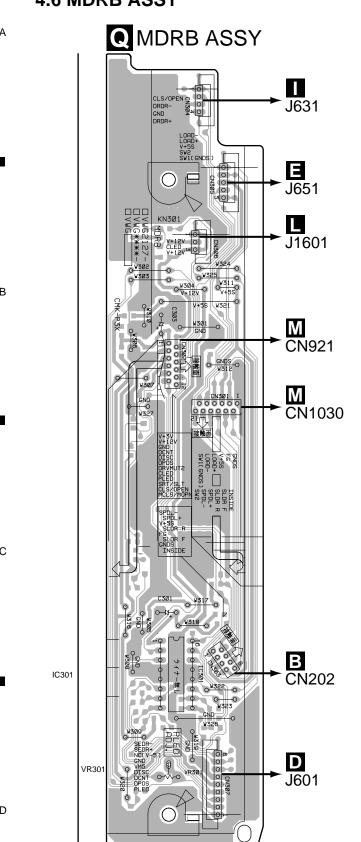
SIDE A

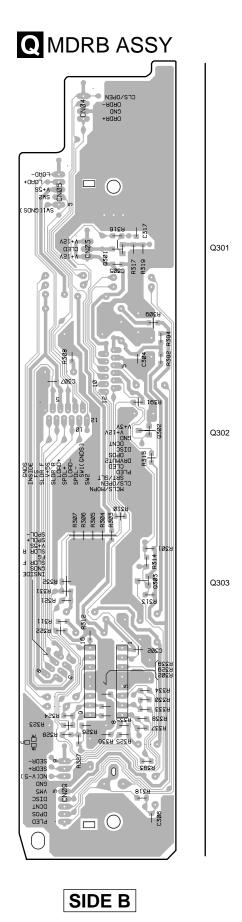


SIDE B

3

С



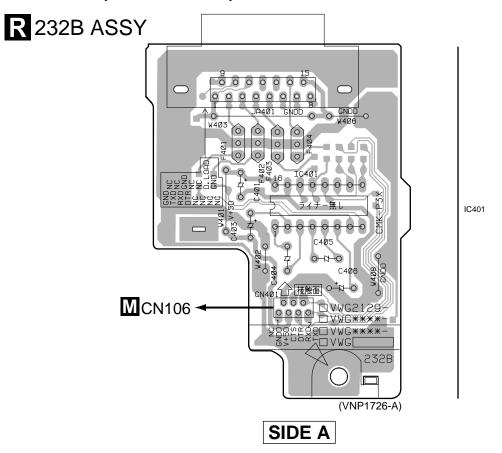


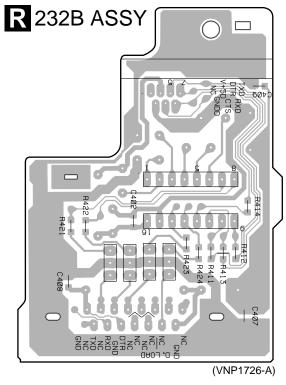
SIDE A

2

(VNP1726-A)

4.7 232B ASSY (DV-F07 ONLY)





SIDE B

3

R

49

В

С

4.8 FLKY, KEYB and PS2B ASSYS

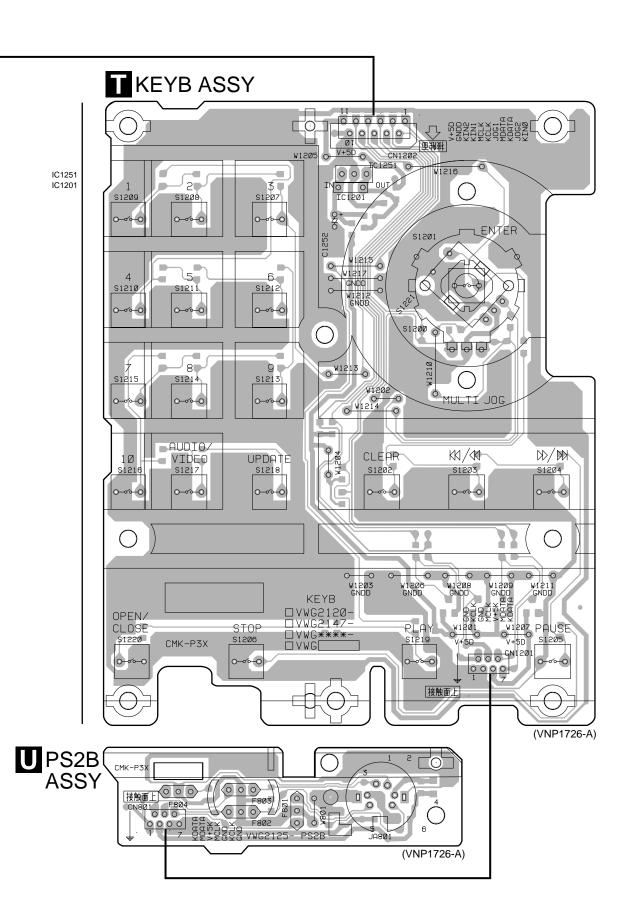
M CN602 P CN903 S FLKY ASSY Ø W747 **-**⊙ W746 0 0 0 W74Ø 0 J7Ø3 O W713 O W7Ø2 R791 PLAY MODE ODISPLAY RANDOM 0 S7Ø4 ○ W7Ø3 ○ W7Ø4 L7Ø1 FLKY DVWG2154-DVWG2155o√0 o≠0 WZØ1 POWER ON • _UVWG2119-S7Ø3 CMK-P3X O W761 UVWG2126-UVWG2146-□VWG<u>**</u>* □VWG[(VNP1726-A) IC701 IC702

SIDE A

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D



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7

В

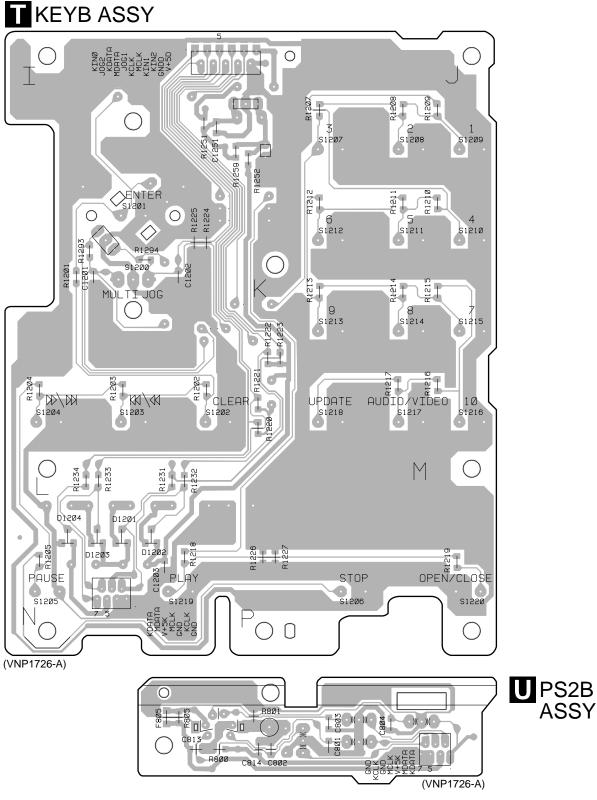
С

D

В

D



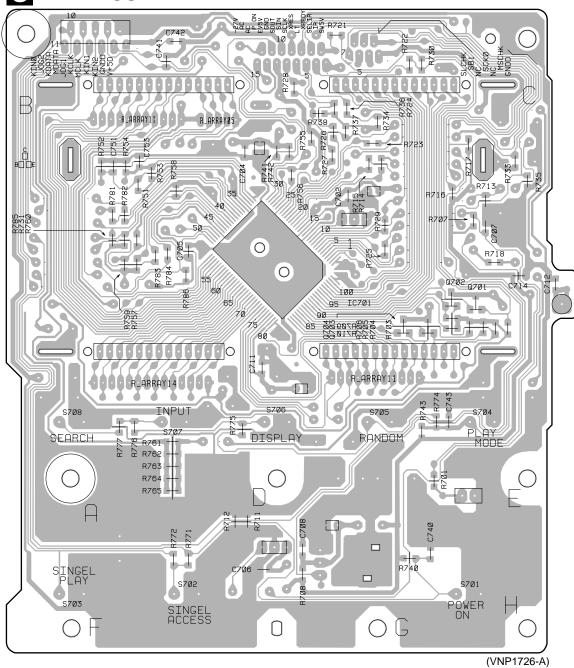


2

S FLKY ASSY

6

5



Q702 Q701

SIDE B

7

В

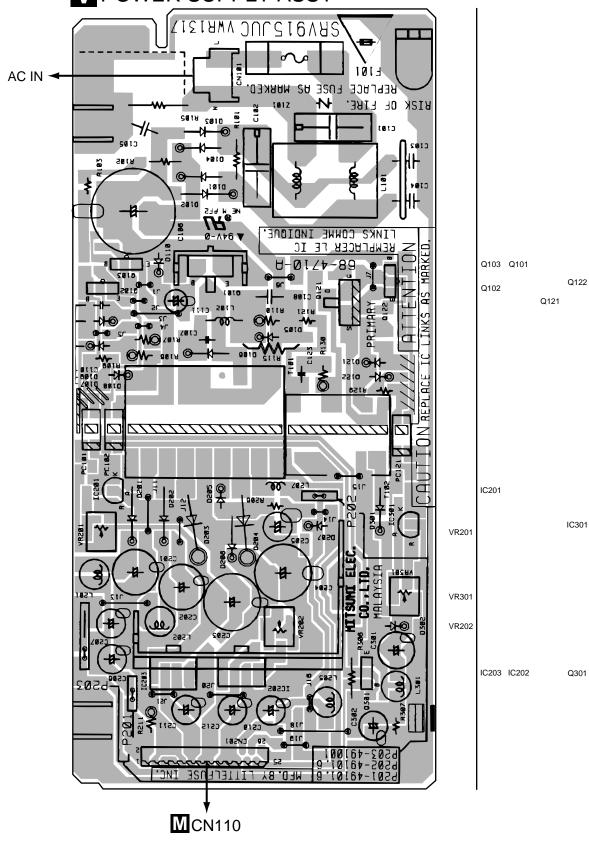
С

D

5

4.9 POWER SUPPLY ASSY

V POWER SUPPLY ASSY



3

SIDE A

В

2

5. PCB PARTS LIST

NOTES: • Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.

- ullet The $oldsymbol{\Delta}$ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- When ordering resistors, first convert resistance values into code form as shown in the following examples.

Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).

560 Ω $47k\Omega$ \rightarrow 0.5Ω

*Ex.*2 When there are 3 effective digits (such as in high precision metal film resistors).

■LIST OF WHOLE PCB ASSEMBLIES

Mark	Symbol and Description		DV-F727		DV-F07	Remarks
		KU	KU KC		KU/CA	
NSP	TRAVERSE MECHANISM ASSY	VWT1161	VWT1161	VWT1161	VWT1161	
NSP	FGSB ASSY	VWG2009	VWG2009	VWG2009	VWG2009	
NSP	─ SMEB ASSY	VWG2048	VWG2048	VWG2048	VWG2048	
NSP	└─ PICKUP ASSY	VWY1055	VWY1055	VWY1055	VWY1055	
NSP	MECB ASSY	VWM1957	VWM1957	VWM1957	VWM1957	
NSP	─ SSRB ASSY	VWG2113	VWG2113	VWG2113	VWG2113	
NSP	─ SEMB ASSY	VWG2114	VWG2114	VWG2114	VWG2114	
NSP	LOMB ASSY	VWG2115	VWG2115	VWG2115	VWG2115	
NSP	LOSB ASSY	VWG2116	VWG2116	VWG2116	VWG2116	
NSP	RADB ASSY	VWG2117	VWG2117	VWG2117	VWG2117	
NSP	└─ PHOB ASSY	VWG2118	VWG2118	VWG2118	VWG2118	
NSP	SUBB ASSY	VWM1958	VWM1958	VWM1999	VWM1959	
NSP	⊢ KEYB ASSY	VWG2120	VWG2120	VWG2120	VWG2120	
NSP	─ DOMB ASSY	VWG2121	VWG2121	VWG2121	VWG2121	
NSP	─ DOSB ASSY	VWG2122	VWG2122	VWG2122	VWG2122	
NSP	─ VOLB ASSY	VWG2123	VWG2123	VWG2123	VWG2123	
NSP	LEDB ASSY	VWG2124	VWG2124	VWG2124	VWG2124	
	─ PS2B ASSY	VWG2125	VWG2125	VWG2125	VWG2125	
	FLKY ASSY	VWG2126	VWG2126	VWG2191	VWG2119	
	─ MDRB ASSY	VWG2127	VWG2127	VWG2127	VWG2127	
	─ MSJB ASSY	VWG2131	VWG2131	VWG2131	VWG2128	
	└─ 232B ASSY	Not used	Not used	Not used	VWG2129	
	DVDM ASSY	VWS1386	VWS1386	VWS1386	VWS1396	
	VQEB ASSY	VWV1669	VWV1669	VWV1669	VWV1669	
	AVJB ASSY	VWV1719	VWV1719	VWV1719	VWV1720	
Δ	POWER SUPPLY ASSY	VWR1317	VWR1317	VWR1317	VWR1317	

■CONTRAST OF PCB ASSEMBLIES



VWS1386 and VWS1396 are constructed the same except for the following:

Mark	Symbol and Decoriation	Part	Domorko	
IVIAIK	Symbol and Description	VWS1386	VWS1396	Remarks
	IC803 C834, C838, C840 CN106 7P FFC CONNECTOR	Not used Not used Not used	M5M4V18165DTP-6S CKSRYF104Z16 VKN1299	

AVJB ASSY

VWV1719 and VWV1720 are constructed the same except for the following:

Mark	Cumbal and Decarintian	Par	Part No.			
IWIATK	Symbol and Description	VWV1719	VWV1720	Remarks		
	IC201 IC241 F241 CHIP SOLID INDUCTOR	PCM1716E Not used Not used	PE8001A PD0236AM VTF1096			
	C241, C243 R241, R242	Not used Not used Not used	CKSQYF104Z25 RS1/10S471J			
	R243-R245 R247-R249 CN503 DUAL 4P MINI DIN SOCKET JA201 4P PIN JACK JA504 2P PIN JACK	Not used RS1/10S0R0J AKP7020 VKB1132 VKB1134	RS1/10S101J Not used AKP7023 VKB1133 VKB1135			
	JA505 3P PIN JACK	VKB1131	VKB1100			

MSJB ASSY

VWG2131 and VWG2128 are constructed the same except for the following:

Mark		Symbol and Description	Part	Domorto	
Ι'	VIAIK	Symbol and Description	VWG2131	VWG2128	Remarks
Г		JA901, JA903 1P PIN JACK	VKB1077	VKB1074	

S FLKY ASSY

VWG2126 and VWG2119 are constructed the same except for the following:

Mark	Symbol and Description	Part No.					
IWIATK	Symbol and Description	VWG2126	VWG2191	VWG2119	Remarks		
	R753	RS1/10S363J	RS1/10S363J	RS1/10S392J			
	R754	RS1/10S622J	RS1/10S622J	RS1/10S103J			
	R755	Not used	RS1/10S623J	Not used			
	R756	RS1/10S0R0J	RS1/10S473J	RS1/10S0R0J			

■ PCB PARTS LIST FOR DV-F727/KU UNLESS OTHERWISE NOTED

Mark No. Description	Part No.	Mark No. Description	Part No.
A FGSB ASSY		RESISTORS	
FGSB ASST		All Resistors	RD1/4PU□□□J
SEMICONDUCTOR			
PC101	TLP910(O)	OTHERS	
		3P CABLE HOL	DER 51048-0300
RESISTORS		7P CABLE HOL	DER 51048-0700
	DC4/4000000	J603 JUMPER WIRE	3P D20PDD0310E
All Resistors	RS1/10S□□□J	J602 JUMPER WIRE	7P D20PDD0725E
		J605 JUMPER WIRE	3P D20PDY0310E

SMEB ASSY

SWITCH

DSG1016 S201

OTHERS

3P FFC CONNECTOR CN201 52044-0345 CN202 **8P FFC CONNECTOR** VKN1212 PC BOARD SMEB VNP1695

SSRB ASSY SEMICONDUCTORS

> Q604 2SC1740S Q601,Q602 DTC124ES D601,D602 GP1S58V

SEMB ASSY OTHERS

> 7P CABLE HOLDER 9P CABLE HOLDER

51048-0700 51048-0900 J601 JUMPER WIRE 9P D20PDY0930E

LOMB ASSY OTHERS

J652

J651

3P CABLE HOLDER 51048-0300 **5P CABLE HOLDER** 51048-0500 JUMPER WIRE 3P D20PDD0310E JUMPER WIRE 5P D20PDY0530E Mark No. Description Part No. Mark No. Description Part No. **LOSB ASSY LEDB ASSY SWITCH SEMICONDUCTORS** LEAF SWITCH VSK1011 D1611 HZU6.2B D1601 NSPB500-9235 **OTHERS RESISTORS** 3P CABLE HOLDER 51048-0300 All Resistors RS1/10S□□□J **OTHERS** C RADB ASSY 51048-0300 3P CABLE HOLDER J1601 JUMPER WIRE 3P D20PDY0325E SEMICONDUCTOR D611 GL381J **DVDM ASSY OTHERS SEMICONDUCTORS** 3P CABLE HOLDER 51048-0300 LED HOLDER RNK1795 IC21 CY2081SL-655 IC101 LA9701M IC201 LC78652W LC89170M IC609 **PHOB ASSY** M56788FP IC352 **SEMICONDUCTOR** IC801 M65773AFP Q621 PT381FBC IC802 MB811171622A-100FN IC612,IC904 MC74VHC541DT IC608,IC611,IC807,IC905 MC74VHCT541ADT **OTHERS** IC607 MC74VHCT574ADT 3P CABLE HOLDER 51048-0300 JUMPER WIRE 3P D20PDY0315E J604 IC702 MN414800CSJ-07 IC261,IC302 NJM2100M IC601 PD3410A **DOMB ASSY** IC701 PD4995A IC604 TC55V1001AF8 **RESISTORS** All Resistors RS1/10S□□□J IC606 TC7SET32F IC751 TC7SH32FU IC24-IC27,IC303 TC7SHU04F **OTHERS** IC610 TC7W53FU 3P CABLE HOLDER 51048-0300 IC22 TC7WH74FU **4P CABLE HOLDER** 51048-0400 JUMPER WIRE 4P D20PDY0445E J631 IC603 VYW1668 Q106,Q109 2SA1576A 2SC4081 Q105,Q114,Q251 **DOSB ASSY** Q602 DTA114EUA Q107,Q111,Q601 DTC114EUA **SWITCH** Q102 HN1A01F S631 LEAF SWITCH VSK1011 Q103,Q281,Q542,Q543 HN1B04FU Q101 HN1C01F **OTHERS** Q112,Q113 HN1C01FU 3P CABLE HOLDER 51048-0300 HN1K03FU Q108 J632 JUMPER WIRE 3P D20PDD0315E Q503 RN1911 D302 KV1470 RB501V-40 D601 **VOLB ASSY** D501,D502 RB521S-30 RESISTOR **COILS AND FILTERS** VR601 (22kΩ) VCP1158 F5050,F5090 CHIP BEAD DTF1067 F4010,F4020,F4030,F4040,F4050 DTF1070 **OTHERS** CHIP BEAD CN604, CN605 52147-0310 F4060,F8330,F9590 CHIP BEAD DTF1070 3P JUMPER CONNECTOR CHIP COIL (1.5µH) L304 VTL1059 KN601 JUMPER TERMINAL PKX1001 L151 CHIP COIL (10µH) VTL1061 L47 CHIP BEAD VTL1084 CHIP BEAD L1400 VTL1088 L9490,L9500,L9510 CHIP BEAD VTL1105

VTL1125

L101,L330 CHIP COIL (8.2µH)

Mark	No. Description	Part No.	Mark	No.		Description	Part No.
CAPA	CITORS		RESI	STO	RS	 }	
	C612	CCSRCH100D50				39Ω×4)	ACN7047
	C123,C145,C21,C282,C617	CCSRCH101J50				R716 (47Ω×4)	ACN7077
	C26	CCSRCH120J50				R543,R545,R613 (10kΩ×4)	DCN1094
	C126,C333	CCSRCH150J50				R649,R706,R707,R748	DCN1094
	C206,C210,C211	CCSRCH151J50		110-	7O,1 V	$(10k\Omega\times4)$	DOI\1004
	C322	CCSRCH180J50		R75	51 (1	10kΩ×4)	DCN1094
	C116,C151,C314	CCSRCH220J50				R532,R689,R691,R732	DCN1104
	C152	CCSRCH221J50		1112	, . ,	$(22\Omega\times4)$	BONTIO
	C632	CCSRCH330J50		R73	36 R	(2232/4) R785,R786,R818-R820	DCN1104
	C209	CCSRCH331J50			,,,,	(22Ω×4)	2011101
	C104-C108,C128,C134,C297	CCSRCH470J50		R82	25.R	R848.R849 (22Ω×4)	DCN1104
	C335	CCSRCH470J50		_	-,	,R162,R2010,R2020,R2030	RS1/10S0R0J
	C122,C208	CCSRCH471J50			,	,R3050,R3520,R506,R510	RS1/10S0R0J
	C127,C334	CCSRCH5R0C50				R601,R701,R801,R8410	RS1/10S0R0J
	C124,C146	CCSRCH680J50				,R9210,R9230,R9240	RS1/10S0R0J
	C117,C240,C352,C360	CCSRCH681J25		R93	39-R	R948,R952-R958,R960	RS1/10S0R0J
	C129,C142,C22,C405,C601	CEV101M10				R973-R975,R979	RS1/10S0R0J
	C701,C763,C801,C802,C804	CEV101M10				R364	RS1/16S1203F
	C113,C139,C358,C368,C411	CEV220M16				R365	RS1/16S1503F
	C111,C147,C149,C205,C207	CEV470M6R3		R16			RS1/16S5600F
	C401,C403,C407	CEV470M6R3		R35	510	(100Ω)	VCN1120
	C502	CKSQYB103K50		Oth	er F	Resistors	RS1/16S□□□J
	C140,C223,C224,C252,C264	CKSQYB105K10					
	C312	CKSQYB105K10	OTHE	ER S			
	C229	CKSQYB224K16	OIIIL			0.0000000000000000000000000000000000000	500
	C217	CKSOVE105716		X60)1	CHIP CERALOCK (20MHz)	DSS1110
	C217 C216,C313	CKSQYF105Z16 CKSRYB102K50				FLEXIBLE CABLE 7P	VDA1681
	C133,C136,C203,C220,C225	CKSRYB103K50		CN	201		
	C239,C320,C321,C619,C703	CKSRYB103K50		CN.			
	C722	CKSRYB103K50					
	C/22	CKSKTBTUSKSU		CN.	103	80,CN921	VKN1471
	C101,C102,C114,C118,C119	CKSRYB104K16				12P FFC CONNECTOR	
	C121,C130,C138,C204	CKSRYB104K16		CN:	905	14P FFC CONNECTOR	VKN1473
	C212,C213,C227,C228,C231	CKSRYB104K16		CN	602	2,CN901	VKN1474
	C24,C263,C315-C317,C332	CKSRYB104K16				15P FFC CONNECTOR	
	C281,C354	CKSRYB222K50					
	0201,0354	CNONTBEZZNOO		CN.	110	26P FFC CONNECTOR	VKN1479
	C153,C266	CKSRYB223K25		CN	903	11P FFC CONNECTOR	VKN1497
	C214,C251,C261	CKSRYB472K50		CN	802	<u> </u>	VKN1529
	C357	CKSRYB473K16				B TO B CONNECTOR 4	0P
	C330	CKSRYB682K50		CN.	107	7P FFC CONNECTOR	VKN1575
	C109,C110,C120,C131,C148	CKSRYF104Z16					
	0100,0110,0120,0101,0140	61(6)(11 104 <u>2</u> 10				BARCODE LABEL	VRW1773
	C150,C202,C215,C221,C222	CKSRYF104Z16		X21		CRYSTAL RESONATOR	R VSS1129
	C226,C230,C235,C265,C29	CKSRYF104Z16				(13.824MHz)	
	C31.C33.C35.C359.C367	CKSRYF104Z16					
	C369-C372,C402,C404,C406	CKSRYF104Z16					
	C408,C410,C412,C501	CKSRYF104Z16	.				
	C602 C614 C612 C646 C646	CKCDVE404740		VQI	ΕΒ	BASSY	
	C602-C611,C613-C616,C618	CKSRYF104Z16	CEM	~~\		LICTORS	
	C621-C631,C702,C704-C714	CKSRYF104Z16	SEMI	CON	NDU	UCTORS	
	C716-C721,C723-C725	CKSRYF104Z16		IC1			MB811171622A-100FN
	C761,C762,C822,C827,C829	CKSRYF104Z16		IC1			PM0023AF
	C832,C833,C836,C920,C921	CKSRYF104Z16				IC106	TC7SH14FU
	C143,C319,C806-C819	CKSRYF105Z10			,	Q532,Q541,Q542	2PB709A
	C328,C821,C824,C825,C828	VCG1030		Q55	51,C	Q552	2PB709A
	(2.2μF)		COU	A NIT	_	III TED	
	C830,C837 (2.2µF)	VCG1030	COIL			FILTER	
	C23,C299 (0.47µF)	VCG1032		F10		VIDEO FILTER	VTF1155
	\\C21 (20pE)	\/CM4043		L10)1	CHIP COIL	VTL1067
	VC21 (30pF)	VCM1013					

Mark	No.	Description	Part No.	Mark		Description	Part No.
CAP	ACITO	RS			C550,	C552	CEAT471M6R3
	C130		CCSRCH102J50		C150	C522,C561,C585	CKSQYB103K50 CKSQYB104K25
	C101,0	C120	CEV101M16		C520-	C322,C301,C363	CKSQYB222K50
	C110-0	C119,C121-C126,C150	CKSRYB104K16		C583		CKSQYB473K50
		C532,C542,C552	CKSRYB104K16		0000		ONOQ I D-I ONOO
	C201,0	C202 (2.2μF)	VCG1031		C101,	C103,C120,C123,C151	CKSQYF104Z25
					C153,	C204,C205,C208,C209	CKSQYF104Z25
RESI	STORS	3			C214,	C215,C230,C233,C234	CKSQYF104Z25
	R105.F	R106,R404,R405 (22Ω×4)	DCN1104			C290,C502,C527	CKSQYF104Z25
		201,R409,R418,R419	RS1/10S0R0J		C529,	C530,C590	CKSQYF104Z25
	R501		RS1/10S0R0J				01/00//=/0==
	R122		RS1/10S2701F			C202,C210,C212,C213	CKSQYF105Z16
	R532,F	R542,R552	RS1/16S3300F		C531		CKSQYF105Z16
	D5045	0544 D554	D04/4004700F	550	OTO D	•	
		R544,R554	RS1/16S4700F	RESI	STOR	S	
	Otner i	Resistors	RS1/16S□□□J		R597-		RN1/10SC18R0D
						R534,R542,R551,R555	RN1/10SC62R0D
OTH	ERS					R581,R585,R589	RN1/10SC62R0D
	CN101	B TO B CONNECTOR 40P	VKN1530		R250,		RN1/10SE1602D
					R251,	R271	RN1/10SE2702D
					Other	Resistors	RS1/10S□□□J
0	AVJB	ASSY		ОТНЕ	RS		
SEMI	COND	UCTORS		-	CN50:	3 DUAL 4P MINI DIN SOCKE	T AKP7020
SLIVII		OCTORS	DA 45005		JA505		VKB1131
	IC203		BA4560F		JA201		VKB1132
	IC502		BU4551BF		JA504		VKB1134
٨	IC501 IC206		LA7135AM NJM78L05A		CN50		VKN1183
$\stackrel{\Delta}{\mathbb{A}}$	IC200		NJM78M08FA				
<i>ح</i> ے	10100		NOIWI OWIOOI A		CN10	1 14P FFC CONNECTOR	VKN1190
	IC201		PCM1716E		CN50		
	IC202		TC7SU04F			SCREW TERMINAL	VNE1948
	Q202,0	Q581-Q584	2PB709A				
	Q151,0	Q281,Q585	2PD601A				
	Q150		2SB1260		MSJE	3 ASSY	
	Q120		2SC1740S	CEMI	COND	LICTORS	
	Q257,0	7077	2SD2114K	SEIVII		UCTORS	
		Q561,Q586-Q589	PDTC124EK		IC922		TC74HC00AF
	D230	2301,Q360-Q369	HZU5.6B			,IC931	TC74HCU04AF
	D230 D281		MA111		Q981		PDTC124EK
	D201		MATT		D971,	D981	MA111
COIL	S AND	FILTER		COIL	S AND	FILTERS	
	L220	CHIP INPEDER	DTL1028		L901	PULSE TRANS.	PTL1003
	L202		LAU1R0J-TA		L902	NOISE FILTER	RTF1167
	F201	CHIP SOLID INDUCTOR	VTF1096			F904,F912,F922	VTF1096
					. 00.,	CHIP SOLID INDUCTOR	
SWIT		ID RELAY					
	S501		VSH1009	CAPA	CITO	RS	
	RY281		RSR1029		C932		CCSQCH101J50
					C902,	C912,C915,C958	CEAT101M10
CAP	ACITO	RS			C903,		CKSQYF103Z50
		2272	CCSQCH221J50			C906,C913,C916,C922	CKSQYF104Z25
		<i>J</i> 213			C933,	C936,C959,C973	CKSQYF104Z25
	C253,0 C251,0		CCSQCH330J50				
	C253,0	C271	CCSQCH330J50 CCSQCH470J50				
	C253,0 C251,0	C271 C525		RESI	STOR	S	
	C253,0 C251,0 C523-0 C255,0	C271 C525	CCSQCH470J50	RESI			RS1/10S□□□.I
	C253,0 C251,0 C523-0 C255,0 C104,0	C271 C525 C275 C154,C211,C232,C501	CCSQCH470J50 CCSQSL331J50 CEAT101M10	RESI		S sistors	RS1/10S□□□J
	C253,0 C251,0 C523-0 C255,0 C104,0	C271 C525 C275 C154,C211,C232,C501 C528,C581,C582	CCSQCH470J50 CCSQSL331J50 CEAT101M10 CEAT101M10	_	All Re		RS1/10S□□□J
	C253,0 C251,0 C523-0 C255,0 C104,0 C526,0 C102,0	C271 C525 C275 C154,C211,C232,C501 C528,C581,C582 C121,C122,C152,C231	CCSQCH470J50 CCSQSL331J50 CEAT101M10 CEAT101M10 CEAT101M16	RESI:	All Re	sistors	
	C253,0 C251,0 C523-0 C255,0 C104,0 C526,0 C102,0 C203,0	C271 C525 C275 C154,C211,C232,C501 C528,C581,C582 C121,C122,C152,C231 C541,C555,C560	CCSQCH470J50 CCSQSL331J50 CEAT101M10 CEAT101M10 CEAT101M16 CEAT102M6R3	_	All ReERS	sistors 4,CN905 MINI JACK	AKN1028
	C253,0 C251,0 C523-0 C255,0 C104,0 C526,0 C102,0 C203,0 C206,0	C271 C525 C275 C154,C211,C232,C501 C528,C581,C582 C121,C122,C152,C231 C541,C555,C560	CCSQCH470J50 CCSQSL331J50 CEAT101M10 CEAT101M10 CEAT101M16 CEAT102M6R3 CEAT470M16	_	All Re ERS CN904 JA902	4,CN905 MINI JACK	AKN1028 GP1F32T
	C253,0 C251,0 C523-0 C255,0 C104,0 C526,0 C102,0 C203,0 C206,0	C271 C525 C275 C154,C211,C232,C501 C528,C581,C582 C121,C122,C152,C231 C541,C555,C560	CCSQCH470J50 CCSQSL331J50 CEAT101M10 CEAT101M10 CEAT101M16 CEAT102M6R3	_	All Re ERS CN904 JA902 JA908	4,CN905 MINI JACK OPTICAL LINK OUT MINI JACK	AKN1028 GP1F32T PKN1005
	C253,0 C251,0 C523-0 C255,0 C104,0 C526,0 C102,0 C203,0 C206,0	C271 C525 C275 C154,C211,C232,C501 C528,C581,C582 C121,C122,C152,C231 C541,C555,C560	CCSQCH470J50 CCSQSL331J50 CEAT101M10 CEAT101M10 CEAT101M16 CEAT102M6R3 CEAT470M16	_	All Re ERS CN904 JA902 JA908	4,CN905 MINI JACK	AKN1028 GP1F32T PKN1005 RKN1004

Mark		Description	Part No.	Mark No.	De	scription	Part No.
	JA901, CN902	,CN903	VKB1077 VKN1211	S FLK	/ AS	SSY	
	CN901	7P FFC CONNECTOR 11P FFC CONNECTOR	VKN1215	SEMICONI	DUCT	ORS	
	0.100	SCREW TERMINAL	VNE1948	IC70 ² IC70 ²	 <u> </u> -Q704		PE5144A S-806D PDTC124EK SLR-343VC(NP)
Q	MDRE	B ASSY					,
SEMI	COND	UCTORS		COILS	. =	0.00	\
0	IC301	5010K5	LA6531	L721,	L/22	CHIP BEAD	VTL1105
	Q303 Q301 Q302		2SA1037K 2SC2412K PDTC124EK	SWITCHES S701	S -S708		ASG7013
CAPA	CITOR	RS		CAPACITO	RS		
OAI P	C306 C303 C301	C304,C305,C307	CCSQCH101J50 CEAT101M10 CEAT470M16 CKSQYF104Z25	C710 C701 C740 C707	,C703, -C743,	C709 C751,C753 C706,C708,C714	CEAL100M50 CEAL470M6R3 CKSQYB102K50 CKSQYF102Z50 CKSQYF104Z25
RESI	STORS	3		C711			CKSQYF104Z50
	•	R312 R336 R323,R326,R327	RS1/10S1503F RS1/10S1803F RS1/10S2202F RS1/10S3003F RS1/10S3302F	RESISTOR R791 R792 R794 R741	S		RA10T104J RA13T104J RA4T104J RN1/10SE1001D
	R324,F R334,F Other F		RS1/10S3902F RS1/10S3903F RS1/10S□□□J	Other	Resis	tors	RS1/10S
ОТИБ	De			OTHERS		40D CADI E LIOI DED	54040 4000
OTHE	CN306 CN304 CN305 CN307 CN303	4P JUMPER CONNECTOR 5P JUMPER CONNECTOR 9P JUMPER CONNECTOR	52147-0410 52147-0510	CN70 IR70 ² V701 CN70 CN70 X701)3)2)1	13P CABLE HOLDER 11P FFC CONNECTOR REMOTE RECEIVER UNIT FL TUBE 7P FFC CONNECTOR 15P FFC CONNECTOR CERAMIC RESONATOR (5MHz)	GP1U28X VAW1052 VKN1183 VKN1191
R	232B	ASSY (DV-F07/KU/	CA Only)	T KEY	B AS	SSY	
		UCTOR	• •	SEMICONE	DUCT	ORS	
OLIVII	IC401	5010K	MAX232EPE		1-D12		DA204K
FILTE	De			SWITCHES			
FILIE		403 EMI FILTER	VTH1009		1-S122	20	ASG7013 PSX1002
CAPA	CITO	RS		CADACITO	De		
	C404,0 C401 C403 C402 C406	C405	CEANP1R0M50 CEAT101M10 CEAT1R0M50 CKSQYF104Z25 CEAL1R0M50	C120	2 1,C12(3,C12(CEAL470M6R3 CKSQYF103Z50 CKSQYF104Z25
DEGI	STORS	1		RESISTOR			D91/1090000
ヘロンド	All Res		RS1/10S□□□J	All Re	esistors		RS1/10S□□□J
	, I 100			OTHERS			
OTHE	RS			CN12		11P FFC CONNECTOR	

CN1201 7P FFC CONNECTOR VKN1211

JA401 CN401 15P D-SUB SOCKET DKN1111 7P FFC CONNECTOR VKN1183 Mark No. Description Part No.

U PS2B ASSY

FILTERS

F801-F804 EMI FILTER VTH1009

CAPACITORS

C801-C804 CCSQCH101J50 C813,C814 CKSQYF104Z25

RESISTORS

All Resistors RS1/10S

OTHERS

JA801 MINI DIN 6P SOCKET RKN1038 CN801 7P FFC CONNECTOR VKN1211

V POWER SUPPLY ASSY

SEMICONDUCTORS

△ IC203 (1A) AEK7064 △ IC201,IC202 (1.6A) AEK7066

VARISTOR VZF1092

OTHERS

△ FU101 FUSE (2A) REK1078 CN201 26P FFC CONNECTOR VKN1202

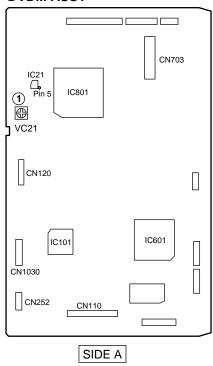
6. ADJUSTMENT

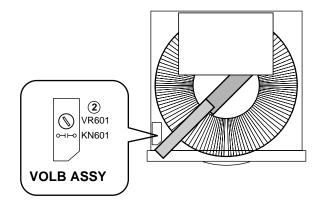
6.1 ADJUSTMENT ITEMS AND LOCATION

Note : When the Traverse mechanism adjustment is not properly adjusted, jitter, error rate and play ability are defective. The noise may come out by the case.

■ Adjustment Points (PCB Part)

DVDM ASSY



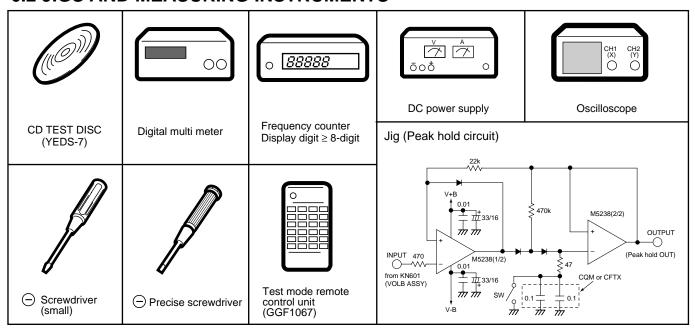


■ Adjustment Items

[Electrical Part]

- 1 Master Clock Adjustment
- 2 Disc-select Rotation Adjustment

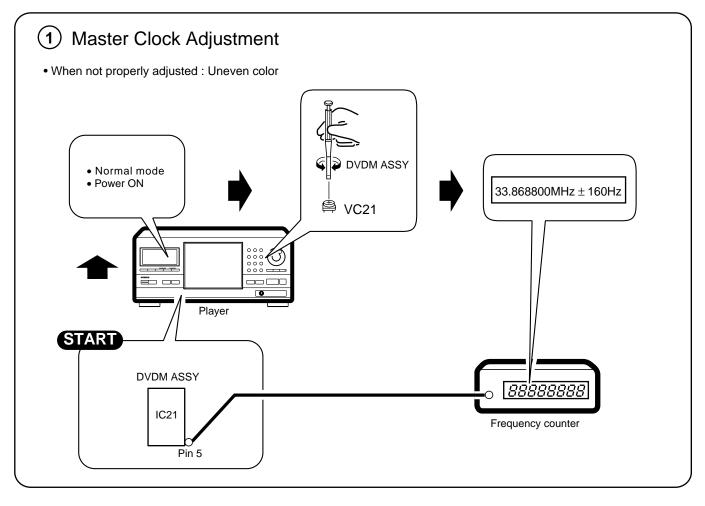
6.2 JIGS AND MEASURING INSTRUMENTS



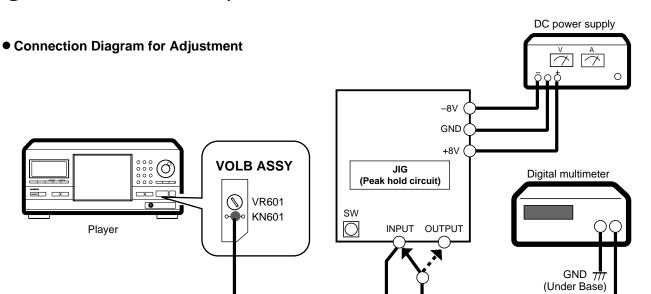
6.3 NECESSARY ADJUSTMENT POINTS

When		Adjustment Points
EXCHANGE PCB ASSY		
Exchange board VOLB ASSY, SSRB ASSY,		Mechanical point
RADB ASSY, PHOB ASSY	7	Electric 2
Exchange board DVDM ASSY		Mechanicalpoint
	7	Electric point Note: ① is adjusted already.

6.4 ELECTRICAL ADJUSTMENT



2 Disc-select Rotation Adjustment



Adjustment Procedure

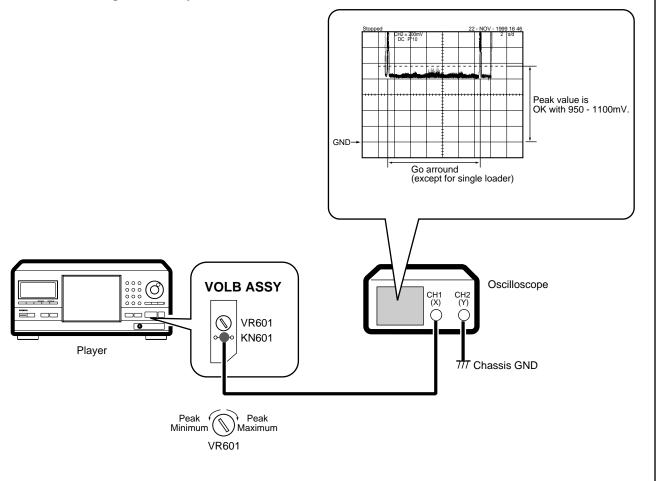
- 1. Connect all equipment as shown in the diagram.
- 2. Turn on the power (Normal mode) and put the test disc in the No. 1 disc slot.
- 3. Enter the Test mode by pressing the "ESC" → "TEST" button of the test mode remote control unit.
- 4. Press the "DIG/ANA" button of the test mode remote control unit. (Disc 1 is clamped.)
- 5. Adjust VR601 on the VOLB Assy so that the voltage becomes 830 ± 5 mV.
- 6. Switch the connection of Digital multimeter from INPUT to OUTPUT of the Jig.
- 7. Press the "DIG/ANA" button of the test mode remote control unit. (Starts the disc detection and peak hold.)
- Confirm the voltage during the disc detection.
 If voltage is between 920 to 1170mV, go to step 13. If not, go to step 9.
- 9. Switch the connection of Digital multimeter form OUTPUT to INPUT of the Jig.
- 10. Press the "DIG/ANA" button of the test mode remote control unit. (Disc 1 is clamped.)
- 11. Adjust VR601 to become the value for addition (or subtraction) that to have an adjustment voltage. (Refer to the following table.)
- 12. Perform steps 6 to 8 again and confirm the voltage during the disc detection. If voltage is between 920 to 1170mV, go to step 13. If not, repeat steps 9 to 12.
- 13. Confirm that Disc No. display doesn't become "1" others when you turn the Jog dial.
- 14. Release the Test mode by pressing the "ESC" button or turn off the power.

Adjustment voltage value

Peak hold voltage (mV)	Adjustment voltage (mV)
to 859	+20
859 to 879	+10
879 to 920	+ 5
920 to 1170	OK
1170 to 1309	- 5
1309 to 1520	-10
1520 to 1840	-20
1840 to 2220	-30
2220 to	-40

Simple Adjustment of Disc Detection

Connection Diagram for Adjustment



• Adjustment Procedure

- 1. Connect an oscilloscope.
- 2. Turn the POWER SW to ON.
- 3. Open the Food.
- 4. Press the "SINGLE LOADER PLAY" button of the player without putting a disc. Start the disc detection if loading is come and there is no disc.
- 5. If the peak value is 950 1100mV while a disc detection goes arround the disc, is OK.
- 6. When peak value is except for OK range, adjust VR601 and repeat steps 3 5.

7. GENERAL INFORMATION

7.1 DIAGNOSIS

7.1.1 TEST MODE SCREEN DISPLAY

Consecutive double-OSD display is supported during test mode. The screen is composed 10 lines with a maximum of 32 characters per line. It can't be used with the debugging display mode together.

Screen Composition

	Character in bold : Item name ☐: Information display			Remote control code Key code
Address —	0000000 R-000	K-000		Mechanism position value and slider position
Background color → Tracking status →	C-R G B B C TRKG- C C C C C C C C C	M-□		Output video system and Skirt terminal output
Spindle status and AFB status → AGC setting →		AV:□,□/□□□□ ← FL:□,□ REG:□ ←		 AV1 chip version FL controller version and region setting for the player
FTS servo IC information →	кs-[0000] 0000	M D L : □□□□/□□□ ←	-	FL controller destination setting
C1 error value of CD and DVD Internal operation mode of the mechanism control	ER	V: 0. 000 FLSH0	-	Port No. of Flash ROM and system controller Flash ROM version and Flash ROM size
Disc judgment and	DSC-UU BM-UU	S:	←	- System controller revision
CD 1/3 beam switch Equalizer value and	E J 4	M: G:	<u></u>	DVD mechanism controller revision (Control and part No. of GUI-ROM)
iitter value				

First Screen Display

Caution:

The first screen and second screen switch by pressing [DISPLAY] key of the remote control unit.

It is only a version display part on the lower right of the screen those contents of display change.

ATB : ON/OFF information display and AGC manual setting display deleted with the second generation.

The displays of Tilt error value, Tilt servo status and pickup DVD/CLD display deleted with the third generation becomes LD part is deleted.

Description of Each Item on the Display

(1) Address indication

The address being traced is displayed in number.

DVD : ID indication (hexadecimal number, 8 digits)

[*******]
CD : A-TIME (min. sec.) [0 0 0 0 ****]

(Note: For DVDs, decimal-number indication is possible.)

(2) Code indication of the remote control unit

The code for the key pressed on the remote control unit, which is received by the FL controller, is displayed while the key is pressed. In the case of the double code, the second code will be displayed.

(3) Key code indication for the main unit [K - * *]

The code for the key pressed on the main unit, which is received by the system controller, is displayed while the key is pressed. At keyboard code input

K-KBD ** ** : At mouse code input

K-MS ** **

(4) Background color indication [C - R* * G* * B* *]

(5) Tracking status [TRKG – ***]

Tracking on [ON]
Tracking off [OFF]

(6) 1 Spindle status [SPDL - * * *]

Spindle accelerator and brake, free-runnimg
FG servo
[FG]
Rough, velocity phase servo
[SRV]
Offset addition, rough, velocity phase servo
[O_S]

AFB status [AFB - * *]

ON [ON] OFF [OFF]

(7) Mechanism position value [M - *]

Position code [1] to [3]

(8) Slider position [S - * * * *]

CD TOC area [IN]
CD active area [CD]

(9) AGC setting [AGC - * *]

AGC on [AGC-ON] AGC off [AGC-OFF]

(10) Output video system [V - * * * *]

NTSC system	[NTSC]
PAL system	[PAL]
Auto-setting	[AUTO]

Skirt terminal output [SK - * *]

VIDEO		[00]
S-VIDEO		[01]
RGB		[02]

*: Display only the model which can do the output setting of skirt

(11) FTS servo IC information

DSP coefficient indication [KS - [****]****]Displays the address (four digits) of the specified coefficient and the setting value (four digits) with [TEST] and [9] keys.

(12) Error rate indication

① C1 error value of CD	[ER - C1 * * * *]		
2 C1 error value of DVD	[ER - * * * * * * * * 1		

(13) Internal operation mode of mechanism controller [MM - * * : * *]

Internal mechanism mode (2 digits) and internal mechanism step (2 digits) of the mechanism controller

(14) 1 Disk sensing [DSC - * * *]

The type of discs loaded is displayed. [DVD], [CD], [VCD], []

(2) CD 1/3 beam switch [BM - * *]

(15) (1) Equalizer value [E - * *]

2 Jitter value [J - * *]

nake the jitter four times, and renew it in every one second.

CD is effective only in the jitter value.

(16) Version of the AV-1 chip [AV : * . * / * * * *]

(17) (1) Version of the FL controller

[FL:****]

2 Region setting of the player [REG: *] [1] to [6]

Setting value

(18) Destination setting of the FL controller

For charactors in front represent the type of model: There characters that follow represent the destination code. J:/J, K:/KU,/KC,/KU/KC, R:/RAM,/RL,/RD,/LB, WY:/WY

(19) The part number of the flash ROM and system

controller [* * * * * * / * * * * * * * 1

- 1) Part number of the flash ROM <Front> (Example) VYW1536-A → W1536A (Example) PD6256A9 → 6256A9 2 Part number of the system controller <Rear>
- (Example) PD3381T1 → 3381T1

(20) (1) Version of the flash ROM [V: *. * * *]

2 Flash ROM size [FLSH **]

(21) Revision of the system controller

- 1 Revision number of the external ROM part (flash ROM) of the system controller <Front>
- 2 Revision of the internal ROM part of the system controller <Rear>

(22) Revision of the DVD mechanism controller

[M:*.***]

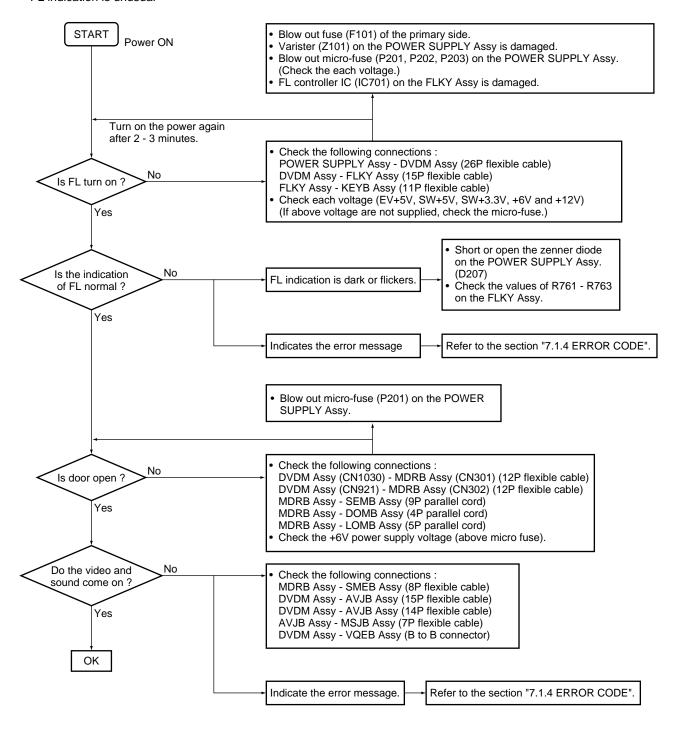
Revision number of the external ROM part (flash ROM) of the DVD mechanism controller

(23) Control and part numbers of the GUI-ROM

No GUI model displays as "——/——". OEM model displays the part number of GUI-ROM [G:****]

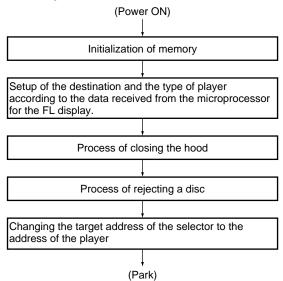
7.1.2 TROUBLE SHOOTING

- No Power ON
- FL is not turned ON
- FL indication is unusual

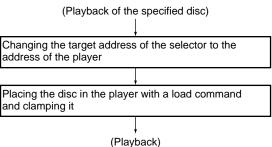


7.1.3 OPERATION FLOWCHART

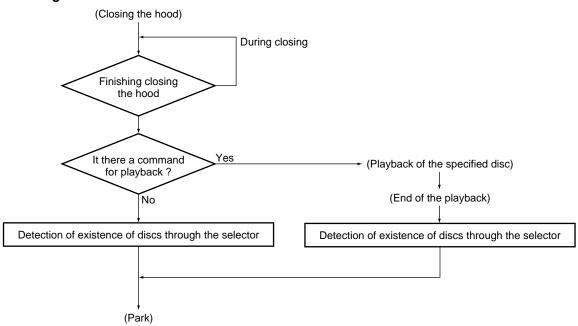
• From power-on till the end of initialization



• Until start of playback of the specified disc



Closing the hood



7.1.4 ERROR CODE

Error codes that are displayed on the FL display without using the remote control unit

FL Display	Possible causes	Operation of the unit
AV1 VER	AV-1 chip is not a match with the program of system controller	The sound may not out with the specific audio.
CPU AERR	CPU address error (Hardware is unusual.)	No operation
DMA AERR	DMA address error (Hardware is unusual.)	No operation
FLASH ID	Difference in versions of the internal ROM of the system controller and of the flash ROM, or bus line failure or reverse installation	No operation
FLASH WRP	Write protect error of the flash ROM	No operation
FLASH SIG	Difference in part number of the flash ROM (When the ROM which could't be used was used.)	No operation
FLASH SUM	Check sum error of the flash ROM (It exceeds the regular size.) or reverse installation (Hardware is unusual.)	No operation
FLASH SIZE	Size error of the flash ROM (Use 4 or 8 M-bit.)	No operation
ILLGAL	The system controller fetched a code other than an operation code (Hardware is unusual.)	No operation
RESERVE	Undefined interrupt (Hardware is unusual.)	No operation
SLOT	Inappropriate slot command issued (Hardware is unusual.)	No operation

Error codes that are displayed on the FL display by using the remote control unit (Mechanism controller error)

To display: ESC + DISPLAY + DISPLAY; Location of the display: At the two digits of center of the FL display To display the error history: ESC + DISPLAY + One shot; Location of the display: TV screen

100	To display the error history: ESC + DISPLAY + One shot; Location of the display: TV screen					
FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit		
11	Search timeout	Search could not be complete within 7 seconds.	Search could not be complete within 7 seconds, and it could not enter the target area within 7 seconds by VCD scan.	CD : Stops, DVD: Continues operation		
12	Search retry error	A search could not be completed after 3 retries, search backup was executed 4 times, or in a case of timeout (6 seconds) while the unit was tracing 11 tracks or more beyond the target while the search operation was converging.	Backup against slider skip was executed 4 times during a search, or slider skip twice resulted in starting from the readin point.	CD: Stops, DVD: Continues operation		
19	Tracing timeout while converging	Timeout (10.5 seconds) while tracing at the stage of convergence of a search.		Stop		
1B	Index 0 search error		During Track (Index) Search, the search for the beginning of a program could not be completed within 3 seconds (20 seconds in the case of Index Search) after positioning based on the TOC data was completed.	Stop		
22	Timeout of slider inner circumference	Inside switch could not ON within 3 seconds.		Stop		
23	Timeout of slider outer circumference	Inside switch could not OFF within 2 seconds.		Stop		
33	No FOK pulse during playback CLVA	When the focus was deviated continuously 20 times.		Adjusts focus at the innermost circumference and tries to return to its position where the error was generated (for 3 times),then opens. If the same error persists after one retry, the tray opens. (No FOK pulse)		
38	Disc-type-sensi- ng error	If normal starting was impossible in the following three cases, disc-type sensing will be retried if other errors occure excepting C5 error. However, when the focus error "33" was occured continuously 3 times, it is finished as "38 error" at the moment: (1) startup with the first disc-type-sensing result, (2) forced startup with another disc by designating the disc type, (3) forced startup with the original disc by designating the disc type.		Open		

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
39	SGC converge timeout	SGC could not converge during detects the peak		Open
41	Spindle timeout	The unit did not enter Stop mode within 10 seconds of	issuance of a Stop command.	Stop
48	Spindle FG transition timeout	The spindle could not converge into within \pm 12% of the target FG rotation speed within 10 seconds after spindle kick. The first time after startup (the first time after disc distinction), it doesn't become the number of the target rotation within five seconds. The first time after startup, detects the abnormal rotation number of high-speed continuously 3 loops. DVD: 5 to 9 mS , CD: 40 to 60 mS		Stops. (FG timeout)
49	Spindle PLL transition timeout	After the second times after startup, it doesn't become rotation within five seconds. Detects the abnormal high-speed or low-speed rotation DVD: 5 to 9 mS , CD: 40 to 60 mS	_	Stops. ("73" is displayed during starting process.)
4A	Spindle lock timeout	Spindle could not lock more than 1.5 seconds before st	art the AFB.	Stops. ("73" is displayed during starting process.)
51	Auto sequence timeout of peak detection	ABUSY did not return within 1 second after the DDTCT (peak detection) command was sent.		Stop
52	Auto sequence timeout of focus jump down	ABUSY did not return within 30 mS after the FJMPD (Focus jump 1 to 0) command was sent.		Stop
53	Auto sequence timeout of focus jump up	ABUSY did not return within 30 mS after the FJMPU (Focus jump 0 to 1) command was sent.		Stop
54	Auto sequence timeout of play AGC	ABUSY did not return within 50 mS after the GSUMON (play-AGC-measuring) command was sent.		Stop
55	Auto sequence timeout of disc-typesensing	ABUSY did not return within 2 seconds after the DJSRT (disc-sensing) command was sent.		Stop
56	Auto sequence timeout of ATB2	ABUSY did not return within 1 second after the TBLOFS (Internal ATB after the completion of external ATB) command was sent.		Stop
57	Auto sequence timeout of tracking servo ON	ABUSY did not return within 500 mS after the TSON (tracking servo ON) command was sent.		Stop
58	Auto sequence timeout of ATB1	ABUSY did not return within 200 mS after the TBL (external ATB) command was sent.		Stop
59	Auto sequence timeout of focus gain adjustment	ABUSY did not return within 2 seconds after the FGN (focus gain adjustment) command was sent.		Stop
5A	Auto sequence timeout of tracking gain adjustment	ABUSY did not return within 2 seconds after TGN (tracking gain adjustment) command was sent.		Stop
5B	Auto sequence timeout of offset adjustment	ABUSY did not return within 1 second after the CMDAVE (offset adjustment) command was sent.		Stop
5C	Auto sequence timeout of modulation factor measurement	ABUSY did not return within 200 mS after the ADJMIR (modulation factor measurement) command was sent.		Stop
5D	Auto sequence timeout of auto focus bias	ABUSY did not return within 2 seconds after the AFB (auto focus bias) command was sent.		Stop
5F	Auto sequence already busy	A command could not be sent because ABUSY was low. ABUSY did not return within 200 mS after TLV command was sent.		Stop
62	Pause retry error	Pause mode could not be restored within three retries after it had been released.		Continues operation

DV-F727, DV-F07

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
71	ID can not read during tracing	An ID could not be read for 1 second or more.		Stop
72	Subcode check failure during playback		No frame could be read for 3 seconds or more.	Stop
73	ID can not read at the startup	An ID could not be read within 1 second after the AFB adjustment had been finished.		Opens (ID readout failure)
74	Subcode check failure during startup		No subcode could be read within 3 seconds after AFB adjustment had been finished.	Opens (Subcode readout failure).
81	Timeout for reading TOC of the mechanism controller		TOC readout took 30 seconds or more.	Stop
82	Timeout for reading TOC of the system controller		Reading TOC of the system controller took 30 seconds or more.	Stop
A1	Communication timeout of DSP command	A command could not be issued to DSP because Command Busy (XCBUSY) was in force (XCBUSY = L) for a specified time (about 200 µS).		No operation
A2	Communication timeout for reading DSP coefficient	Command Busy (XCBUSY) was in force for a specified time (about 200 μ S) before and after a coefficient read command was issued to DSP, or the address echo-back after command issuance did not match the setup address.		No operation
А3	Communication timeout for writing DSP coefficient	Command Busy (XCBUSY) was in force for a specified time (about 1024 mS) before and after the coefficient write command was issued to DSP.		No operation
A4	Communication timeout for continuously writing DSP coefficient	Command Busy (XCBUSY) was in force for 200 µS during continuous coefficient writing, or before and after a continuous write command was issued to DSP.		No operation
B1	Timeout error for backup	In the tracing state during the backup sequence, codes could not be read for 1 second or more. In the backup sequence, tracking ON sequence of the servo DSP could not be completed even if more than 500 mS after the tracking ON command was issued.		Stops
B2	Retry error for backup	Tracing impossible after retring the tracking ON for 3 times in the backup sequence.		Stops
ВЗ	Retry error for trace	During tracing, runaway was detected after three iterations of backup operations for detecting runaway.		Stops
СЗ	Detection of tracking overcurrent			Stops (the mechanical controller operates independently).
(C5)	Short-circuit test corresponding error	While the power was on, the overcurrent detection port was at L for 40 ms or more continuously.		Turns off the power instantly (No indication on the FL display and no writing to flash memory)
E3	Violation against digital copy guard			Stops
F5	Tray being pushed	The tray switch that had been Open mode was forcibly changed to a mode other than Open by an external force.		Closes
F8	Loading timeout	Loading, unloading or clamping could not be completed within a specified time (about 5 seconds).		Reverses the loading direction. It timeout is repeated upon retry, the unit stops.
FC	Focus	The following error occured eight times. (1) Focus ON sequence could not be completed of the focus ON command (to the servo DSP) was (2) Focus IN sequence was finished, actually focus	Stops wherever possible then opens (stops in the case of side B).	

E00, E04-E06, E11, E16, E17, E90-E92, E99 : Refer to page 76.

Error codes that are displayed on the FL display by using the remote control unit (Device error) To display: ESC + DISPLAY + DISPLAY; Location of the display: At the two digits of left of the FL display

FL	Description of Error	Causes if with a DVD	Causes if with a CD	Operation of the Unit
bit3=1 08 etc.	AV1 access error (read, write NG)			No operation or it becomes debugging
bit2=1 04 etc.	MY CHIP access error			indication if the power is able to ON.
bit1=1 01 etc.	SRAM access error			

7.1.5 INTERFACE CONNECTOR

Communication control

The protocol system is based on a system in which the computer gives commands and the player returns statuses. The communication protocol used conforms to that used by industrial and educational equipment (changers, drives, LD players for educational use, etc.) of our company, and supports Communication Mode 7 (CM 7).

CM 7 uses a method of proceeding a program by issuing each execution command and confirming the execution status with a request command.

First, the controller sends an execution command, and the player returns the status of having received the command. Then, the controller sends a request command, and the player returns the corresponding status data. The controller repeats this operation until it receives the status data required.

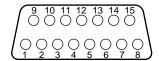
In CM 7, neither Completion of Execution Status nor error messages are returned. Statuses returned in response to execution commands are the End of Receive message <R> and Communication Errors <E 00>. To confirm Completion of Execution or occurrence of errors, use the request command "?J." However, note that some commands have restrictions or conditions, and that some commands cannot be executed or may not be executed as they should be.

Format

Serial control interface in conformance with the RS-232C Standards

Connector

15-pin, D-sub connector



Signal Line

No.	Name	I/O	Function
1	GND	-	Ground
2	TxD	0	Transmission output
3	RxD	I	Receiving input
4	DTR	0	Transmission permission
5	Reserved	-	
6	Reserved	-	
7	Reserved	-	
8	Reserved	-	
9	Reserved	-	
10	Reserved	-	
11	GND	-	Ground
12	Reserved	-	
13	Reserved	-	
14	Reserved	-	
15	GND	_	Ground

Specifications of Interface

Signal level : RS-232C level

Data format

Data length: 8 bits Stop bit: 1 bit Parity: none

Transmission speed (baud rate):

Selectable between 9600 and 19200 bps, by exclusive command of the player. Every time the power is turned on, the baud rate is reset to 9600 bps.

Connection:

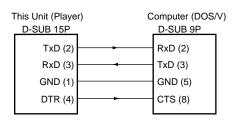
The computer and this unit are connected through three lines, as shown below:



The player can receive commands any time as long as the power is on. So, even when the RS-232C port of the computer is used, connection of any control lines other than TxD and RxD is NOT necessary.

However, some computers need physical CTS input for operation. In this case, the DTR output, which always outputs positive electric potential as long as transmission is possible, can be used. Connect the DTR output to the CTS input of the computer as shown below:

Example of connection (connection with a DOS/V computer)



List of Commands

Mnemonic	Command Name	Argument	Job					
OP	OPEN	_	Opens the hood.					
UL	UNLOAD	0 - 300	Moves the specified disc to the front and opens the hood.					
СО	CLOSE -		Closes the hood and detects the existence of discs.					
ZS	DISC SELECT 0 - 300 (600		(600) Executes playback of the specified disc.					
RJ	REJECT	_ ` ` `	1st time: Stops playback. 2nd time: Returns the disc to the rack.					
ZR	DISC RETURN	_	Returns disc being played to the rack.					
PL	PLAY	_	Executes playback.					
KP	1 TRACK PLAY	_	During one-track playback, the status becomes busy.					
PA	PAUSE	_	Pauses playback temporarily and displays a still picture.					
ST	STILL	_	Pauses the playback temporarily.					
SF	STEP FORWARD	_	Plays back picture forward frame by frame.					
SR	STEP REVERSE	_	Plays back picture in reverse frame by frame.					
NF	SCAN FORWARD	_	Scans picture forward (scanning mode is locked).					
NR	SCAN REVERSE	_	Scans picture in reverse (scanning mode is locked).					
NS	SCAN STOP	_	Stops scanning and returns to the normal playback mode.					
SE	SEARCH	See below.	Executes search. (Arguments differ from mode to mode.)					
ТМ	TIME MODE SET	MMMSS	Sets search by time (M: minute, S: second).					
СН	CHAPTER MODE SET	1 - 99	Sets search by chapter.					
TI	TITLE MODE SET	1 - 99	Sets search by title.					
TR	TRACK MODE SET	1 - 99	Sets search by track.					
SU	SELECT SUBTITLE	0 - 32, None	Switches to the specified subtitle. (0 or none: OFF)					
AU	SELECT AUDIO 1 - 8		Switches to the specified audio.					
AG	SELECT ANGLE 0 - 9		Switches to the specified angle.					
AP	SELECT ASPECT 1 - 3		Switches aspects either to 1 (Pan & Scan), 2 (Letter Box), or 3 (wide).					
RP	REPEAT MODE SET 0 - 3		Execute Repeat mode of the following: 0 (OFF), 1 (TRACK), 2 (DISC), or 3 (FUNCTION).					
DS	DISPLAY CONTROL	0 -5, None	Displays the specified OSD.					
		, , , , , , , ,	(0: OSD OFF, none: equivalent to key input from the remote control unit)					
CL	CLEAR	_	Cancels the function input, Repeat mode, and Function mode in this order.					
PM	PLAY MODE SET	0 - 1	Sets the playback mode to 0 (All Discs) or 1 (Single Disc).					
CP	CUSTOM PLAY SET	1 - 20	1-10: Audio 1-10 mode, 11-20: Video 1-10 mode					
UD	AUTO UPDATE	1 - 2	Executes the following: 0 (Additional Update) or 1 (All Update)					
ВС	BAUD RATE CHANGE	0 -1	Switches the transmission speed to 0 (9600 bps) or 1 (19200 bps).					
ID	DISC-ID OUTPUT	1 - 300 (600)	Outputs distinction data for the specified disc.					
ко	DISC TYPE OUTPUT	1 - 300 (600)	Outputs disc type data for the specified disc.					
NI	DISC TEXT INPUT	1 - 300 (600)	Inputs title data for the specified disc.					
Al	ARTIST TEXT INPUT	1 - 300 (600)	Inputs artist's name data for the specified disc.					
NO NO	DISC TEXT OUTPUT	1 - 300 (600)	Outputs title data for the specified disc.					
AO	ARTIST TEXT OUTPUT	1 - 300 (600)	Outputs artist's name data for the specified disc.					
?J	, ,		Returns the operational status of the execution command (R: finished, B: busy, EXX: error).					
?J ?P	JOB STATUS REQUEST PLAYER AVTIVE STATUS	RECHEST	Returns the operational status of the player (PXX: XX = operational status).					
?Z			Returns the number of the disc being played back.					
?Z ?E	DISC NUMBER REQUEST		Returns the latest error code (EXX: XX = Error code).					
?E ?T	ERROR REQUEST		Returns the time from the beginning of the TITLE/DISC to the current point					
'1	?T TIME CODE REQUEST		(MMMSS: M = minute, S = second).					
?R	 TITLE/TRACK NUMBER R	EOHEST	Returns the TITLE/TRACK No. being played back.					
?K ?C	CHAPTER REQUEST	LQULUI	Returns the chapter No. being played back.					
?C ?A	PLAY TIME REQUEST		Returns the current playback time (TTCCMMSSS: T = title, C = chapter).					
?A ?K	DISC TYPE REQUEST		Returns the disc type (00: unknown, 01: none, 02: CD, 10: DVD, 40: VCD)					
?K ?H	PLAYER MODE REQUES	г	Returns the player mode (XX: second digit = Play mode, first digit = Repeat mode).					
?H ?M	COMMUNICATION MODE		Returns the communication mode (CM 7 : always Mode 7).					
?IVI ?X	PLAYER MODEL NAME R		Returns the player model distinction number (P1560XX: XX = version)					
۲۸	I LATER WODEL NAME R	LQUEST	Trotaino tilo piayor modor distinction number (1 1000//. // = version)					

Argument: Argument of a command. The values in parentheses are those when the slave player(s) is(are) connected, and "0" stands for a single loader.

• Error List

Error Code	Error Name	Meaning	Circumstance when an error occurs
E00	Communication Error	A communication error was generated between the player and the host computer.	A string of commands exceeded the defined buffer size.
E04	Command Error	An inappropriate command string was detected.	An undefined command was issued or a command was issued at inappropriate timing.
E05	Request Error	A request command was detected at inappropriate timing.	A request command was issued in circumstances where a status signal could not be returned.
E06	Argument Error	Inappropriate argument, or argument missing	The argument was out of range or missing
E11	No Disc Error	No disc	No disc in the specified location, or a command was issued without a disc present.
E16	Key Input Error	There was key input at inappropriate timing.	Because of a key input through the remote control unit, for example, during execution of a command, the execution of the command was interrupted.
E17	GUI Menu Error	The GUI menu is displayed, or the unit entered GUI Menu mode during execution of a command.	The normal command was issued while the GUI Menu was already on-screen, or the GUI Menu was being output to the screen during execution of a command.
E90	Connect Error	Connection error of the master and slave players	Connections are not correct.
E91	Slave Player Error	A direct command from the host computer to a slave player was detected.	The host computer is connected to the slave player.
E92	Master-Slave Error	Communication error between the master player and the slave player(s)	Whether a command to the slave player(s) was issued correctly or not could not be confirmed.
E99	Panic Error	An error was generated, and playback could not be continued.	An error in the mechanical control system or the servo system was generated, and continuation of playback became impossible.

Notes

Normal operation

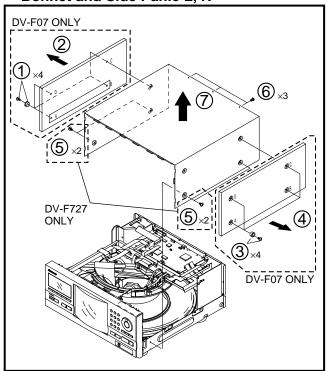
- Some operations in Search mode are prohibited for some discs. In such a case, an error is not generated when the mode is specified, but an error is generated when execution of the SE command is attempted.
- Always use the ZS command at the beginning of a command string.
- To use the ZS command during setup (PLAYER ACTIVE STATUS = P03,) always use the ZR command before the ZS command.
- If a disc does not exist at the location specified by the ZS command, playback of the disc in the next location in numerical sequence is executed if it is before the location of no-disc detection. If it is after the location of no-disc detection, a "No Disc Error" (E11) is generated.
- If an RJ command is issued during startup of the disc, the disc is returned to the rack, as with the ZR command.
- During GUI Menu mode (during Setup or Sub-Set-up mode,) normal commands, the NI command, or AI command cannot be accepted. If GUI
 Menu mode is entered during execution of a command, only normal commands are canceled. Other commands can be accepted during GUI
 Menu mode.
- Repeat mode is canceled if the SE command is executed.
- If the KO command is issued before location of no-disc detection, an error is generated, and the error code is displayed.
- The setting of Repeat mode is disabled in playback mode (Status is P1X with ?P).
- If an impossible OSD display item is specified (for example, if time display is specified for a DVD for which time data are not available) with the DS command, an error is generated, and the error code is displayed.
- The NI and AI commands are effective only when the disc type has been detected (except for NO DISC and UNKNOWN). disc type can be checked using the KO command.

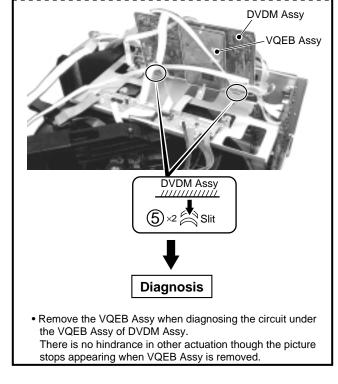
Operation with master-slave communication

- If the disc is being returned to the rack with the ZS command, etc., while a slave player is operating, the master player will assume success of the
 operation.
- The "End of Receive" Status (R) is generated by the master player. If a communication error is generated between the master and a slave player, only the error data item "E92" is stored, and the error code "E00" will not be returned to the host computer.
- The OP, UL, and CO commands apply to only the master player even during Slave Player Operation mode. That is, opening/closing of the hood of a slave player cannot be controlled with RS-232C commands.
- Any part of a command string after the ZR and RJ commands sent to a slave player is ignored.
- A request command to the slave player(s) takes 0.5 seconds at maximum. The ?T and ?A commands are exceptions, and return status data immediately.
- The ?T and ?A commands during Stop mode, etc., will generate errors if issued to the master player. But this is not the case for the slave player(s), and time data are always transmitted.
- If any connection error is detected between the master and slave players, the player returns a "E92" code once in response to any command. Afterwards, the players will not accept any commands.
- If the host computer is connected to the slave player, the player returns a "E92" code once in response to any command. Afterwards, the players will not accept any commands.
- Allow at least 0.5 second (1 second recommended) between commands to the slave player(s), except for the ?A, ?T, NI, AI, NO, AO, KO, and ID commands.
- No command can be accepted during the first approximately 3 seconds after the power is turned on.

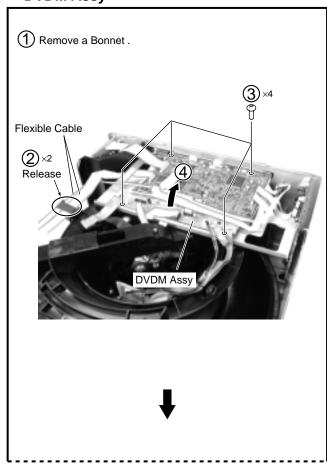
7.1.6 DISASSEMBLY

■ Bonnet and Side Panle L, R

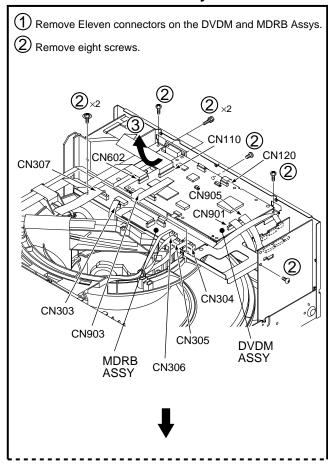


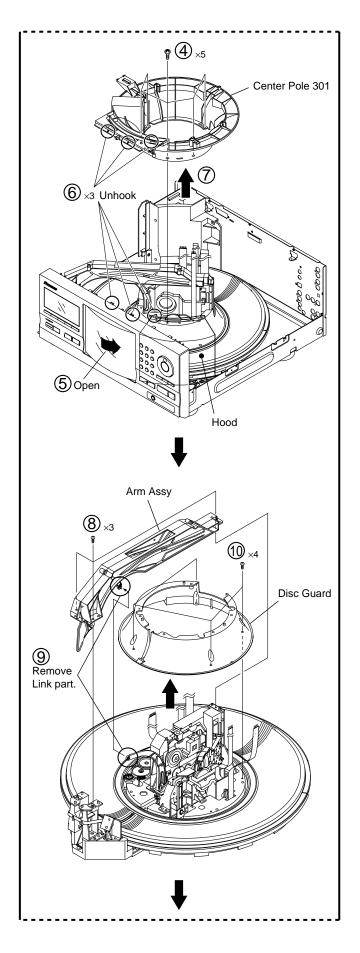


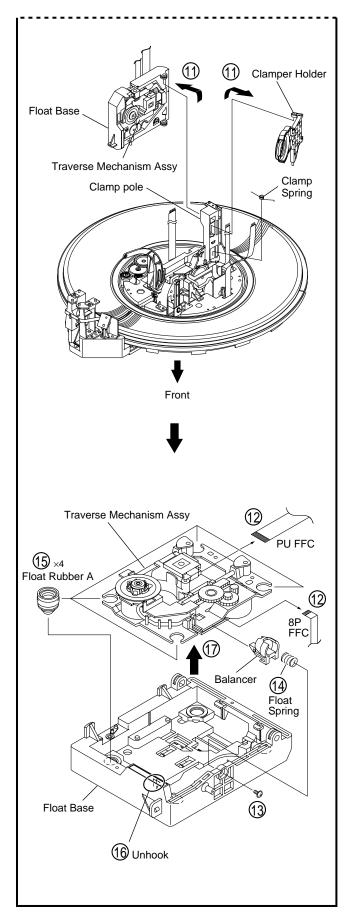
■ DVDM Assy



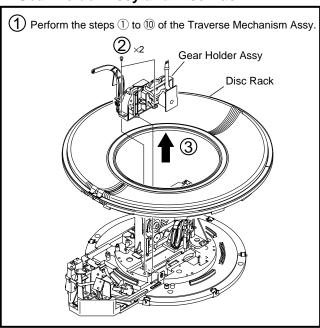
■ Traverse Mechanism Assy



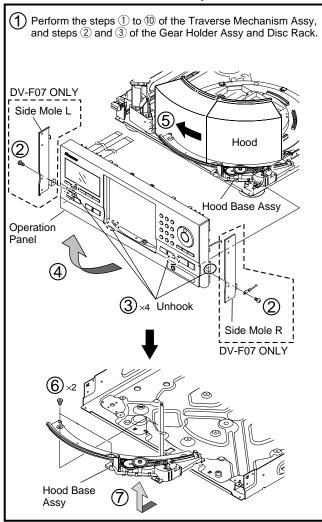




■ Gear Holder Assy and Disc Rack



■ Hood and Hood Base Assy



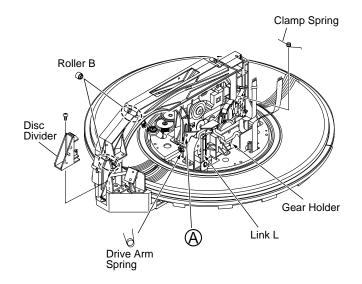
7.1.7 ABOUT SERVICE IN THE MECHANISM FAILURE

- Draw two discs of CD
- \bullet Pin of $\begin{tabular}{l} \end{tabular}$ portion in the figure deviates from the groove of cam
- Arm comes off

When a symptom of the above (mechanism failure, etc.) was occured, perform the check of following items in the Check Table with repair of failure section simultaneously.

Check Table

	Item							
1	Does Roller B installs it justly?							
2	Does Disc Divider installs it justly?							
3	Does Clamp Spring installs it justly or hang it?							
4	Does Drive Arm Spring installs it justly or hang it?							
5	Does hook of Link L installs to the Gear Holder justly?							



DV-727, DV-F07

7.2 PARTS

7.2.1 IC

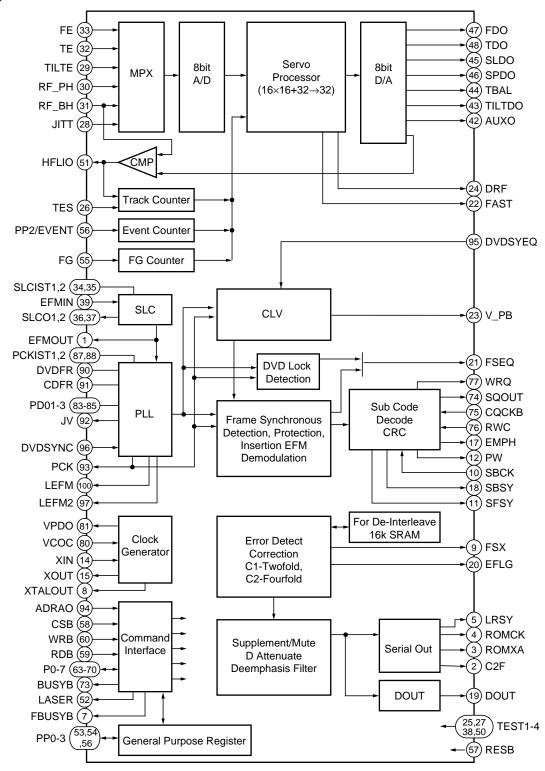
• The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

List of IC

LC78652W, PD3410A, VYW1668, M65773AFP, PM0023AF, CY2081SL-655

■ LC78652W (DVDM ASSY : IC201)

- DSP IC
- Block Diagram



September Comment Co	No.	Pin Name	I/O	Function						
3 ROMAK	1	EFMOUT	0	Output the state that was binary-stated value EFM						
ROMCK O Shift clock output for CD-ROM data output	2	C2F	0	C2 flag output						
Early Content Conten	3	ROMXA	0	CD-ROM data output						
February February	4	ROMCK	0	Shift clock output for CD-ROM data output						
7 FBUSYB 0 Busy signal output of DSP process operation N ch-OD output 8 XTALOUT 0 External system clock output 9 FSX 0 CD 1 frame sync. Signal output 10 SBCK 1 Subcode reading out clock input 11 SFSY 0 Frame sync. Signal output of subcode 12 PW 0 Subcode P, Q, R, S, T, U, V and W output 13 VSS - GND pin 14 XIN 1 Concert a crystal resonator 15 XOUT 0 Connect a crystal resonator 16 DVDD1 - 3.3V power supply of the oscillation circuit 17 EMPH 0 Monitor output of the escillation circuit 18 SSSY 0 Sync. signal output of the subcode block 19 DOUT 0 Audio EIAJ data output 20 EFLG 0 Error correction state monitor of the error correction C1 and C2 21 FSEQ 0 Detection monitor of the CD/DVD frame sync. signal 22 FAST <td>5</td> <td>LRSY</td> <td>0</td> <td>L/R clock output for CD-ROM data output</td>	5	LRSY	0	L/R clock output for CD-ROM data output						
8 XTALOUT 0 External system clock output 9 FSX 0 CD 1 frame sync. signal output 10 SBCK 1 Subcode reading out clock input 11 SFSY 0 Frame sync. signal output of subcode 12 PW 0 Subcode P, Q, R, S, T, U, V and W output 13 VSS – GND pin 14 XIN 1 Connect a crystal resonator (16.9344MHz) 15 XOUT 0 Connect a crystal resonator 16 DVDD1 - 3.3V power supply of the oscillation circuit 17 EMPH 0 Monitor pin of the deemphasis 18 SBSY 0 Sync, signal output of the subcode block 19 DOUT 0 Audio EIAJ data output 21 FERG 0 Derror correction state monitor of the error correction C1 and C2 21 FSET 0 Detection monitor of the CD/DVD frame sync. signal 22 FAST 0 Playback speed monitor N ch-OD output 23 FERG	6	PP3	I/O	General-purpose port input/output / DVD sync. signal input N ch-OD output						
9 FSX O CD 1 frame sync. signal output 10 SBCK I Subcode reading out clock input 11 SFSY O Frame sync. Signal output of subcode 12 PW O Subcode P, Q, R, S, T, U, V and W output 13 VSS - GND pin 14 XIN I Connect a crystal resonator 15 XOUT O Connect a crystal resonator 16 DVDD1 - 3.3V power supply of the oscillation circuit 17 EMPH O Monitor pin of the deemphasis 18 SSSY O Sync., signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Ernor correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V.PB O Monitor output of the rough servo/CLV control 24	7	FBUSYB	0	Busy signal output of DSP process operation N ch-OD output						
SBCK	8	XTALOUT	0	External system clock output						
11	9	FSX	0	CD 1 frame sync. signal output						
12	10	SBCK	I	Subcode reading out clock input						
13	11	SFSY	0	Frame sync. signal output of subcode						
14 XIN I Connect a crystal resonator 15 XOUT O Connect a crystal resonator 16 DVDDI - 3.34 yower supply of the oscillation circuit 17 EMPH O Monitor pin of the deemphasis 18 SBSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the eID/DVD frame sync. signal 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Delayback speed monitor N choD output 24 DRF O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 31TT I still revor signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input	12	PW	0	Subcode P, Q, R, S, T, U, V and W output						
15 XOUT O Connect a crystal resonator 16 DVDD1 — 3.3V power supply of the oscillation circuit 17 EMPH O Monitor pin of the deemphasis 18 SBSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 3 JITT I Jitter quantity detecting signal input of EFM PLL 31 RF_PH I RF peak hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 — Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 40 AVDD — SV power supply of A/D and D/A for servo 41 AVSD — O A auxiliary output 43 TILTDO O Sied control signal output 44 TBAL O Tracking alongut 45 FDDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 VREF — Reference level of D/A for servo	13	VSS	_	GND pin						
16 DVDD1 - 3.3V power supply of the oscillation circuit 17 EMPH O Monitor pin of the deemphasis 18 BSSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Titter quantity detecting signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEMIN I EFM/EFM + input 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTOO O Titt control signal output 44 TBAL O Tracking balance control signal output 45 SLOO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TBD O O Tracking control signal output	14	XIN	I	Connect a crystal resonator (16.9344MHz)						
17 EMPH O Monitor pin of the deemphasis 18 SBSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Tilt error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 3 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxillary output 43 TILTDO O Tit control signal output 44 TBAL O Tracking centrol signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Focus control signal output	15	XOUT	0	Connect a crystal resonator						
18 SBSY O Sync. signal output of the subcode block 19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Titte ror signal input 30 RF_PH I RF bottom hold signal input 31 RF_BH I RF bottom hold signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 1 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFMERT+ input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 44 TBAL O Tracking alone control signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Fracking control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	16	DVDD1	_	3.3V power supply of the oscillation circuit						
19 DOUT O Audio EIAJ data output 20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JUTT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Title error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 1 for SLC 38 TEST1 I Test input 1 40 AVDD - SV power supply of AVD and D/A for servo 41 AVSS - GND of AVD and D/A for servo 42 AUXO O DA auxiliary output 44 TILTDO O Tit control signal output 45 SLDO O Seid control signal output 46 SPDO O Spindle control signal output 47 FDO O Tracking balance control signal output 48 VREF - Reference level of D/A for servo	17	EMPH	0	Monitor pin of the deemphasis						
20 EFLG O Error correction state monitor of the error correction C1 and C2 21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 33 V_PB O Monitor output of the rough servo/CLV control 44 DRF O In focus monitor 55 TEST3 I Test input 3 56 TES I Tracking error signal input 57 TEST2 I Test input 2 58 JITT I Jitter quantity detecting signal input of EFM PLL 59 TILTE I Title rorr signal input 50 TR_PH I RF peak hold signal input 51 RF_BH I RF bottom hold signal input 52 TE I Focus error signal input 53 FE I Focus error signal input 53 FE I Focus error signal input 54 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 55 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 56 SLCO1 O Control output 1 for SLC 57 SLCO2 O Control output 2 for SLC 58 SLCO3 O Control output 2 for SLC 59 EFMIN I EFM/EFM + input 50 AVDD - 5V power supply of A/D and D/A for servo 51 AVSS - GND of A/D and D/A for servo 52 SLCD O Seled control signal output 53 SLCO O Tracking balance control signal output 54 SLDO O Sled control signal output 55 SLDO O Tracking balance control signal output 56 SLDO O Tracking balance control signal output 57 FDO O Fracking control signal output 58 SLDO O Tracking balance control signal output 58 SLDO O Tracking balance control signal output 59 VREF - Reference level of D/A for servo	18	SBSY	0	Sync. signal output of the subcode block						
21 FSEQ O Detection monitor of the CD/DVD frame sync. signal 22 FAST O Playback speed monitor N ch-OD output 32 V_PB O Monitor output of the rough servo/CLV control 44 DRF O In focus monitor 55 TEST3 I Test input 3 56 TES I Tracking error signal input 57 TEST2 I Test input 2 58 JITT I Jilter quantity detecting signal input of EFM PLL 59 TILTE I Tilt error signal input 50 RF_PH I RF peak hold signal input 51 RF_BH I RF bottom hold signal input 52 TE I Tracking error signal input 53 FE I Focus error signal input 54 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 55 SLCIST2 - Current setting pin 1 of the constant current charge pump for SLC 56 SLCO1 O Control output 1 for SLC 57 SLCO2 O Control output 2 for SLC 58 TEST1 I Test input 1 59 FFMIN I FEM/EFM + input 50 AVDD - 5V power supply of A/D and D/A for servo 51 AVSS - GND of A/D and D/A for servo 52 AUXO O DA auxiliary output 53 SLDO O Sted control signal output 54 SLDO O Sted control signal output 55 SLDO O Fracking output 56 SLDO O Tracking balance control signal output 57 SLDO O Tracking balance control signal output 58 SLDO O Spindle control signal output 59 VREF - Reference level of D/A for servo	19	DOUT	0	Audio EIAJ data output						
22 FAST O Playback speed monitor N ch-OD output 23 V-PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Titter ror signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 RF_BH I RF bottom hold signal input 34 SLCISTI - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tit control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Tracking control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	20	EFLG	0	Error correction state monitor of the error correction C1 and C2						
23 V_PB O Monitor output of the rough servo/CLV control 24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Titl error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tit control signal output 44 TBAL O Tracking balance control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	21	FSEQ	0	Detection monitor of the CD/DVD frame sync. signal						
24 DRF O In focus monitor 25 TEST3 I Test input 3 26 TES I Tracking error signal input 27 TEST2 I Test input 2 28 JITT I Jitter quantity detecting signal input of EFM PLL 29 TILTE I Tilt error signal input 30 RF_PH I RF peak hold signal input 31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Spindle control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	22	FAST	0	Playback speed monitor N ch-OD output						
25 TEST3	23	V_PB	0	· · · ·						
Test Test Test input	24	DRF	0	· · · · · · · · · · · · · · · · · · ·						
27 TEST2	25	TEST3	I	Test input 3						
28 JITT	26	TES	I	Tracking error signal input						
29 TILTE	27	TEST2	I	Test input 2						
30 RF_PH	28	JITT	I	Jitter quantity detecting signal input of EFM PLL						
31 RF_BH I RF bottom hold signal input 32 TE I Tracking error signal input 33 FE I Focus error signal input 34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tit control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 48 TDO O Tracking balance control signal ou	29	TILTE	ı	Tilt error signal input						
32 TE	30	RF_PH	ı	RF peak hold signal input						
33 FE	31	RF_BH	I	RF bottom hold signal input						
34 SLCIST1 - Current setting pin 1 of the constant current charge pump for SLC 35 SLCIST2 - Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	32	TE	ı	Tracking error signal input						
35 SLCIST2 — Current setting pin 2 of the constant current charge pump for SLC 36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD — 5V power supply of A/D and D/A for servo 41 AVSS — GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF — Reference level of D/A for servo	33	FE	ı	Focus error signal input						
36 SLCO1 O Control output 1 for SLC 37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	34	SLCIST1	_	Current setting pin 1 of the constant current charge pump for SLC						
37 SLCO2 O Control output 2 for SLC 38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	35	SLCIST2	_	Current setting pin 2 of the constant current charge pump for SLC						
38 TEST1 I Test input 1 39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	36	SLCO1	0	Control output 1 for SLC						
39 EFMIN I EFM/EFM + input 40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	37	SLCO2	0	Control output 2 for SLC						
40 AVDD - 5V power supply of A/D and D/A for servo 41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	38	TEST1	I	Test input 1						
41 AVSS - GND of A/D and D/A for servo 42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	39	EFMIN	I	EFM/EFM + input						
42 AUXO O DA auxiliary output 43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	40	AVDD	_	5V power supply of A/D and D/A for servo						
43 TILTDO O Tilt control signal output 44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	41	AVSS	_	GND of A/D and D/A for servo						
44 TBAL O Tracking balance control signal output 45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	42	AUXO	0	DA auxiliary output						
45 SLDO O Sled control signal output 46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	43	TILTDO	0	Tilt control signal output						
46 SPDO O Spindle control signal output 47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	44	TBAL	0	Tracking balance control signal output						
47 FDO O Focus control signal output 48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	45	SLDO	0	Sled control signal output						
48 TDO O Tracking control signal output 49 VREF - Reference level of D/A for servo	46	SPDO	0	Spindle control signal output						
49 VREF - Reference level of D/A for servo	47	FDO	0	Focus control signal output						
	48	TDO	0	• ,						
50 TEST4 I Test input 4	49	VREF	-	Reference level of D/A for servo						
· · · · · · · · · · · · · · · · · · ·	50	TEST4	I	Test input 4						

51 HFLIO I/O Mirror detection signal input/output 52 LASER O Output pin for laser ON/OFF control 53 PPO/DVD_CDB I/O General-purpose port input/output / Disc discrimination signal output 54 PP1/CRCERRB I/O General-purpose port input/output / Subcode CRC result signal output 55 FG I FG counter input 56 PP2/EVENT I/O General-purpose port input/output / Event counter input 57 RESB I Reset input 58 CSB I Chip select input 59 RDB I Internal state reading signal input 60 WRB I Command / data writing signal input 61 DVDD2 - 5V power supply 62 VSS - GND 63 PO - 6RP3 67 P4 - 6BP3 69 P6 - 70 70 P7 - 71 71 VSS - GND 72 DVDD1 - 3.3V	
S2 LASER	
S3 PP0/DVD_CDB I/O General-purpose port input/output / Disc discrimination signal output	
54 PP1/CRCERRB I/O General-purpose port input/output / Subcode CRC result signal output 55 FG	
FG	
56 PP2/EVENT I/O General-purpose port input/output / Event counter input 57 RESB I Reset input 58 CSB I Chip select input 59 RDB I Internal state reading signal input 60 WRB I Command / data writing signal input 61 DVDD2 - 5V power supply 62 VSS - GND 63 PO - 64 64 P1 - 65 68 P2 - 66 69 P6 - 70 70 P7 - 71 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 80 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 79 VRPFR	
57 RESB I Reset input 58 CSB I Chip select input 59 RDB I Internal state reading signal input 60 WRB I Command / data writing signal input 61 DVDD2 - 5V power supply 62 VSS - GND 63 PO - 6AP 64 P1 - 6F 66 P2 - 6F 67 P4 - Command / data input/output 68 P5 - 6B 69 P6 - 70 P7 - 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 73 BUSYB O Busys signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q 76 RWC	
Section Company Comp	
SP RDB	
Command / data writing signal input	
61 DVDD2 - 5V power supply 62 VSS - GND 63 P0 - 64 64 P1 - 65 65 P2 - 66 66 P3 - I/O 67 P4 - 68 69 P6 - 70 70 P7 - 71 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 73 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS - PLL GND for internal system clock 79 VRPFR - VCO oscillation range setting of PLL for system clock 80 VCOC I 81 VPDO O 82 AVDD - PLL 5V power supply for system clock	
62 VSS - GND 63 P0 - GA P1 65 P2 - GBND - 66 P3 I/O Command / data input/output 68 P5 - GBND 70 P7 - GND 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 73 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS - PLL GND for internal system clock 79 VRPFR - VCO oscillation range setting of PLL for system clock 80 VCOC I Connect a PLL filter for system clock 82 AVDD - <td></td>	
63 PO 64 P1 65 P2 66 P3 67 P4 68 P5 69 P6 70 P7 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 73 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS - PLL GND for internal system clock 79 VRPFR - VCO oscillation range setting of PLL for system clock 80 VCOC I 81 VPDO O 82 AVDD - PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
64 P1 65 P2 66 P3 67 P4 68 P5 69 P6 70 P7 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 73 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS - PLL GND for internal system clock 79 VRPFR - VCO oscillation range setting of PLL for system clock 80 VCOC I 81 VPDO O 82 AVDD - PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
65 P2 66 P3 67 P4 68 P5 69 P6 70 P7 71 VSS	
66 P3 67 P4 68 P5 69 P6 70 P7 71 VSS - GND 72 DVDD1 - 3.3V power supply for internal 73 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS - PLL GND for internal system clock 79 VRPFR - VCO oscillation range setting of PLL for system clock 80 VCOC I 81 VPDO O 82 AVDD - PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
Command / data input/output	
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72 DVDD1 — 3.3V power supply for internal 73 BUSYB O Busy signal output of command process 74 SQOUT O Serial output of subcode Q 75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS — PLL GND for internal system clock 79 VRPFR — VCO oscillation range setting of PLL for system clock 80 VCOC I 81 VPDO O Connect a PLL filter for system clock 82 AVDD — PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
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75 CQCKB I Shift clock input for subcode Q data output 76 RWC I Update permission input of subcode Q 77 WRQ O Read out ready monitor of subcode Q 78 AVSS - PLL GND for internal system clock 79 VRPFR - VCO oscillation range setting of PLL for system clock 80 VCOC I 81 VPDO O Connect a PLL filter for system clock 82 AVDD - PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
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80 VCOC I 81 VPDO O 82 AVDD - 83 PDO1 I/O PLL 5V power supply for system clock PLL 5V power supply for system clock R3 PDO1	
81 VPDO O Connect a PLL filter for system clock 82 AVDD - PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
81 VPDO O S 82 AVDD — PLL 5V power supply for system clock 83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
83 PDO1 I/O PLL filter connection pin 1 for EFM playback	
84 PDO2 I/O PLL filter connection pin 2 for EFM playback	
85 PDO3 I/O PLL filter connection pin 3 for EFM playback	
86 AVSS – PLL GND for EFM playback	
87 PCKIST1 - Current setting 1 of PLL constant current charge pump for EFM playback	
88 PCKIST2 - Current setting 2 of PLL constant current charge pump for EFM playback	
89 AVDD – PLL 5V power supply for EFM playback	
90 DVDFR – VCO oscillation range setting of PLL for EFM playback 1	
91 CDFR – VCO oscillation range setting of PLL for EFM playback 2	
92 JV O Jitter output of PLL clock for EFM playback	
93 PCK O Bit clock output for EFM playback	
94 ADRAO I Address input	
95 DVDSYEQ I DVD synchronize pulse input	
96 DVDSYNC I DVD synchronous signal input	
97 LEFM2 O Output the state that cut and out a signal which was binary-stated value EFM with PCK 2	
98 DVDD1 – 3.3V power supply for I/O	
99 VSS - GND	
100 LEFM O Output the state that cut and out a signal which was binary-stated value EFM with PCK 1	

■ PD3410A (DVDM ASSY : IC601)

• System Control IC

No.	Mark	Pin Name	I/O	Function	
1	XCS3/XCASL	XCS3	0	PD4995A (MY CHIP) chip select signal output	
2	GND	GND	-	GND	
3	СК	HCPUCK	0		
4	VCC	V+3D	-	V+3D	
5	PICLK	_	I/O	N.C.	
6	PIDATA	_	I/O	N.C.	
7	GND	GND	l -	GND	
8	PORTH0	XCSSP0	0		
9	PORTH1	33MVH	0		
10	PORTH2	36MVH	0		
11	PORTH3	V_SEL2	0	Composite/S switching signal output of the skirt terminal	
12	VCC	V+3D	-	V+3D	
13	PORTH4	SCTAON	0		
14	PORTH5	27MVH	0		
15	PORTH6	XCSSPD	0		
16	PORTH7	XAUDRST/ VPOFF/ ECHO	0	YSS922 (Dolby AC-3/Pro logic, audio DSP built-in DTS decoder) Video system	
17	GND	GND	-	GND	
18	EXTAL	EXTAL	ı	Connect a coramic reconstor	
19	XTAL	XTAL	0	Connect a ceramic resonator	
20	VCC	V+3D	_	V+3D	
21	PORTG0	XCSDF0	0	DAC chip select signal output	
22	PORTG1	XCSDF1/ XCSDASP	0	YSS912 (Dolby AC-3/Pro logic, audio DSP built-in DTS decoder) AD1853 (3D audio processor) TC74VHC595FT (Serial/parallel) →SM5847AF (DAC for Mch) YSS922 (DASP)	
23	PORTG2	XCSDF2/ DFRST1/ XMIC_ON	0	YSS912 (Dolby AC-3/Pro logic, audio DSP built-in DTS decoder) SM5847AF (DAC for Mch)	
24	PORTG3	HIBSEL	0	PD0236AM	
25	PORTG4	LFEON/ DFRST0	0	Buffer → Audio amp SM5847AF (DAC for Mch)	
26	GND	GND	_	GND	
27	PORTG5	6CHMD/ XMAOFF	0	Buffer → Front DAC selector	
28	PORTG6	DTSMD/ XMRST/ XDASP	0	SW (Switch circuit)	
29	PORTG7	XAMUTE/ XMUTM	0	Last stage mute signal output of the audio	
30	PORTF0	44X48	0	DAC 44/48 FS switching signal output	
31	PORTF1	DI_ERR/ XDIGIO	ı	DIR1700 (Digital audio interface receiver)	
32	PORTF2	3DON/ XMMUTE/ 48X44	0		
33	VCC	V+3D	_	V+3D	
34	PORTF3	XCSADSP0/ SYNC1	I	CD deck synchronous input	
35	PORTF4	XCSADSP1/ XAVS_RT/ DISC	I	Disc detection input	
36	PORTF5	XCSADSP2/ DPOS/ODD	I	Disc position detection input	

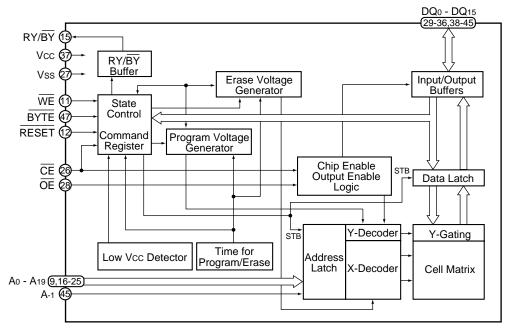
No.	Mark	Pin Name	I/O	Function		
37	PORTF6	XVQERST/ XANR	0	VQE4 reset output		
38	PORTF7	XCSVE/ XCSVQE	0	Serial communication enable signal output of the video encoder		
39	GND	GND	_	GND		
40	AVSS	GND	_	GND		
41	AVCC	V+3D	_	V+3D		
42	OUTA_P	LODRV	0	Loading drive output		
43	VREF	V+3D	_	V+3D		
44	OUTB_P	TEI	0	Tracking offset signal output		
45	AVSS	GND	_	GND		
46	AVSS	GND	_	GND		
47	PORTE0	V_SEL	0	Component/composite switching signal output		
48	PORTE1	CDGM	ı	PDC016A (Graphic IC)		
49	PORTE2	OEM???	ı			
50	PORTE3	FOFST1	I/O	Focus offset adjustment output 1		
51	PORTE4	FOFST2	I/O	Focus offset adjustment output 2		
52	PORTE5	XDFINH	I/O	Defect shunt signal output		
	PORTE6	DVD/XCD	0	DVD/CD switching signal output		
54	PORTE7	LD1_ON	0	650 nm laser diode ON signal output		
	PORTD0	LD2_ON	0	780 nm laser diode ON signal output		
56	VCC	V+3D	-	V+3D		
57	PORTD1	DPD/TE	0			
	PORTD2	AGOFF	0	1 beam/3 beams switching signal output		
	PORTD3	XCD2X		AGC ON/OFF switching signal output of RF IC		
59	PORTD4	OEICG	0	Signal output for switching the double speed playback		
60	_		0	OEIC gain switching signal output		
61	GND	GND	-	GND		
62	PORTD5	XMON	0	Control output ON/OFF switching output of the spindle motor		
63	PORTD6	XBCA	0			
64	PORTD7	OPEN_SW/ X???RST	ı	Mechanism connector		
	PORTJ0	XDRVMUT	0	Driver mute output		
	PORTJ1	DR/XLD	0	TC7W53F (Analog SW)		
67	PORTJ2	XDSPRST	0	LC78652W (Servo DSP)		
68	PORTJ3	MNJACK/ MC_MO	I/O	LA6531		
69	VCC	V+3D	_	V+3D		
70	PORTJ4	TM_ENT	I	Test mode input		
71	PORTJ5	XEXPE	0	TC74VHCT574F/FS (3-state buffer)		
72	PORTJ6	VSEL_SW	ı	Component/composite SW input		
73	PORTJ7	DQSY	ı	Timing input of CD TEXT DAT		
74	PB0/TIOCA2	XCBUSY	ı	Command busy input		
75	PB1/TIOCB2	XABUSY	ı	Auto-sequence busy input		
	PB2/TIOCA3	XINT2/ XAVIRQ2	ı	Interrupt input 2 (AV-1)		
77	VCC	V+3D	_	V+3D		
78	PB3/TIOCB3	LT1	0	Communication response signal output to the FL controller		
79	PB4/TIOCA4	SBSY	ı	Subcode block sync. input		
	XMTEST	_	ı	V+3D		
	XCPUMD	_	ı	V+3D		
	XRES	XRESET	—	Reset input		

No.	Mark	Pin Name	I/O	Function		
83	GND	GND	_	GND		
84	AN0	LODPOS	ı	Loading position input		
85	AN1	SLDPOS	ı	Slider position input		
86	AN2	DOORSW	ı	Mecha. connector		
87	AN3	NAP_SW	ı	NTSC/AUTO/PAL SW input		
88	AN4		ı			
89	AN5		ı			
90	AN6		1			
91	AN7	525IP_SW	ı			
92	Avref	V+3D	_	V+3D		
93	AVCC	V+3D	_	V+3D		
94	AVSS	GND	_	GND		
95	PB5/TIOCB4	DIBLK/HFL/ DCNT2	ı	Disc count input		
96	PB6/TIOCXA4/TCLKC	C2F	ı	C2 error input		
97	PB7/TIOCXB4/TCLKD	XRDY	ı	Communicatio request input from the FL controller		
98	PB8/RxD0	SSI	ı	Serial data input (FL controller)		
99	PB9/TxD0	SSO	0	Serial data output (FL controller, DAC)		
100	VCC	V+3D	_	V+3D		
101	PB10/RxD1	RXD	ı	Data input of the RS-232C		
102	PB11/TxD1	TXD	0	Data output of the RS-232C		
103	PB12/XIRQ4/SCK0	SSCK	I/O	Serial clock output (FL controller, DAC)		
104	PB13/XIRQ5/SCK1	XIRQL10	ı	Interrupt input 1 (MY CHIP)		
105	GND	GND	_	GND		
106	PB14/XIRQ6	XIRQL11	ı	Interrupt input 2 (MY CHIP)		
107	PB15/XIRQ7	XINT0/ XAVIRQ0	I	Interrupt input 0 (AV-1)		
108	PA0/XCS4/TIOCA0	XCS4	0	Servo DSP chip select signal output		
109	PA1/XCS5/XRAS	N.C.	0	Non connection		
110	PA2/XCS6/TIOCB0	XCS6	0	AV-1 chip select signal output		
111	XWAIT	XWAIT	ı	Wait signal input		
112	XWRL	XWRL	0	Write pulse output L		
113	GND	GND	_	GND		
114	XWRH	XWRH	0	Write pulse output H		
115	XRD	XRD	0	Read pulse output		
116	PA7/XBACK	XCURDET	I	Over-current detection signal input		
117	PA8/XBREQ	CTS	I	RS-232C transfer permit input		
118	PA9/XAH/XIRQOUT/ XADTRG	DTR	0	RS-232C transfer permit output		
119	PA10/DPL/TIOCA1	XAVIRQ1/ XINT1	I	Interrupt input 1 (AV-1)		
120	PA11/DPH/TIOCB1	THLD	ı	Tracking hold signal input		
121	VCC	V+3D	_	V+3D		
122	PA12/XIRQ0/DACK0/ TCLKA	DACK0	0	DMA response output (MY CHIP)		
123	PA13/XIRQ1/ XDREQ0/TCLKB	XDREQ0	I	DMA request input (MY CHIP)		
124	PA14/XIRQ2/XDACK1	XDACK1	0	DMA response output (AV-1)		
125	PA15/XIRQ3/XDREQ1	XDREQ1	I	DMA request input (AV-1)		
126	AD0	D0	I/O	Data bus 0		

No.	Mark	Pin Name	I/O	Function
127	GND	GND	-	GND
128	AD1	D1	I/O	Data bus 1
129	AD2	D2	I/O	Data bus 2
130	AD3	D3	I/O	Data bus 3
131	AD4	D4	I/O	Data bus 4
132	AD5	D5	I/O	Data bus 5
133	AD6	D6	I/O	Data bus 6
134	VCC	V+3D	-	V+3D
135	AD7	D7	I/O	Data bus 7
136	AD8	D8	I/O	Data bus 8
137	AD9	D9	I/O	Data bus 9
138	AD10	D10	I/O	Data bus 10
139	GND	GND	_	GND
140	AD11	D11	I/O	Data bus 11
141	AD12	D12	I/O	Data bus 12
142	AD13	D13	I/O	Data bus 13
143	AD14	D14	I/O	Data bus 14
144	VCC	V+3D	-	V+3D
	AD15	D15	I/O	Data bus 15
146	A0 (XHBS)	A0	0	Address bus 0
147		A1	0	Address bus 1
148	A2	A2	0	Address bus 2
	GND	GND	_	GND
150		A3	0	Address bus 3
151	A4	A4	0	Address bus 4
152	A5	A5	0	Address bus 5
153	A6	A6	0	Address bus 6
154		A7	0	Address bus 7
155		A8	0	Address bus 8
156		A9	0	Address bus 9
157	A10	A10	0	Address bus 10
158		A11	0	Address bus 11
159	A12	A12	0	Address bus 12
	A13	A13	0	Address bus 13
	A14	A14	0	Address bus 14
	A15	A15	0	Address bus 15
	A16	A16	0	Address bus 16
	A17	A17	0	Address bus 17
	VCC	V+3D		V+3D
	A18	A18	0	Address bus 18
	A19	A19	0	Address bus 19
	A20	A20	0	Address bus 20
169		A21	0	N.C.
	XNMI	XNMI	ı	V+3D
171		GND	-	GND
	XCS10	XCS10	0	VHCT574F/FS (3-state buffer)
	XCS20	XCS20	0	Chip select signal output of the flash ROM
	XCS22	XCS22	0	(GUI ROM)
	XCS23	XCS23	0	Chip select signal output of the SRAM
176	XCS2		0	N.C.

■ VYW1668 (DVDM ASSY : IC603)

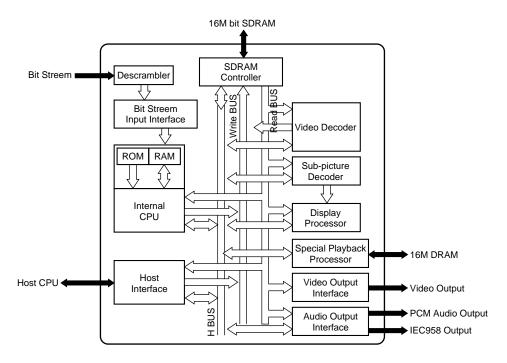
- 16M bit Flash Memory IC
- Block Diagram



No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	A15			25	A0	ı	Address input
2	A14			26	CE	I	Chip enable
3	A13			27	VSS	-	Ground
4	A12			28	ŌE	ı	Output enable
5	A11	1	Address inputs	29	DQ0		
6	A10			30	DQ8		
7	A9			31	DQ1		
8	A8			32	DQ9		
9	A19				DQ2	I/O	Data inputs/outputs
10	N.C.	-	Non connection	34	DQ10		
11	WE	I	Write enable	35	DQ3		
12	RESET	I	Hardware reset pin/Temporary sector unprotection	36	DQ11		
13	N.C.	_	Non connection	37	VCC	-	Power supply
14	N.C.	-	Non connection	38	DQ4		
15	RY/BY	0	Ready/Busy output	39	DQ12		
16	A18			40	DQ5		
17	A17			41	DQ13	I/O	Data inputs/outputs
18	A7			42	DQ6		
19	A6			43	DQ14		
20	A5	I	Address inputs	44	DQ7		
21	A4			45	DQ15/A-1	I/O	Data inputs/outputs / Address input
22	A3			46	VSS	_	Ground
23	A2			47	BYTE	ı	Selects 8-bit or 16-bit mode
24	A1			48	A16	ı	Address input

■ M65773AFP (DVDM ASSY: IC801)

- MPEG2 Decoder IC
- Block Diagram



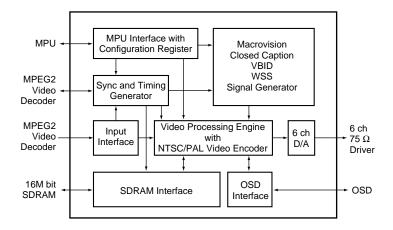
No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function
1	GND	ı	Ground	21	5VDD	ı	5V power supply
2	HD0			22	HD15	I/O	Data input and output port
3	HD1			23	cs	ı	Chip select signal input
4	HD2	1/0	Data input and output port	24	RE	ı	Read Enable signal input
5	HD3			25	WE	ı	Write Enable signal input
6	HD4			26	BHE	ı	Byte High Enable signal input
7	5VDD	I	5V power supply	27	RDY	0	Acknowledge signal which is indicated the finish of data reading or writing via the host bus
8	VDD	I	Power supply	28	INTR	0	Interrupt request signal against to the external CPU from M65773FP
9	HD5			29	GND	ı	Ground
10	HD6			30	HA0		
11	HD7	1/0	Data input and output port	31	HA1		
12	HD8			32	HA2	ı	Address input port
13	HD9			33	HA3		
14	GND	ı	Ground	34	HA4		
15	HD10			35	VDD	ı	Power supply
16	HD11			36	5VDD	ı	5V power supply
17	HD12	I/O	Data input and output port	37	HA5		
18	HD13			38	HA6		Address input port
19	HD14			39	HA7	'	Address input port
20	VDD	ı	Power supply	40	HA8		

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function	
41	HA9	I	Address input port	83	VDD	ı	Power supply	
42	GND	I	Ground	84	VSYNC	0	Vertiacl sync. signal output	
43	CDMCK	ı	Connect to ground	85	HSYNC	0	Horizontal sync. signal output	
44	CDLRCK	I	L/R clock clock input from CDDSP	86	PICSTRT			
45	CDBCK	I	PCM bit clock input from CDDSP	87	MBSTRT			
46	CDDATA	I	Digital audio interface input	88	MBDATA			
47	VDD	ı	Power supply	89	GND	ı	Ground	
48	CDDIN	ı	PCM audio data input from CDDSP	90	PWD	0	Phase comparator output for external sync. operation	
49	INT2	0	Interrupt request signal against to the	91	CSYNC	I	Composite SYNC signal input	
50	INT3	O	external CPU from M65773FP	92	OSDKEY	0	OSD key flag output	
51	DREQ	0	DMA request signal for OSD bitmap transfer	93	PXCLK	0	Pixel clock (27MHz free-running clock)	
52	DACK	I	DMA acknowledge signal for OSD bitmap transfer	94	VDD	I	Power supply	
53	GND	I	Ground	95	PD7			
54	CLKO	0	27MHz clock output	96	PD6	0	Digital pixel data	
55	CLKIN	I	System clock input	97	PD5		Digital pixel data	
56	AVDD1	I	Analog power supply	98	PD4			
57	AGND1	ı	Analog ground	99	GND	ı	Ground	
58	AGND3	'	Arialog ground	100	PD3			
59	AVDD3	I	Analog power supply	101	PD2	0	Digital pixel data	
60	CCAP	I	Connect to ground	102	PD1		Digital pixel data	
61	AGND2	ı	Analog ground	103	PD0			
62	AVDD2	ı	Analog power supply	104	VDD	ı	Power supply	
63	ACLKO	_	Open	105	GND	ı	Ground	
64	ACLKI	I	Audio clock input	106	RESET	ı	Hardware reset input	
65	HMODE1	I	Setting pin of host interface operating mode	107	TEST0			
66	GND	I	Ground	108	TEST1	ı	Connect to ground normally	
67	VDD	I	Power supply	109	TEST2			
68	AOD			110	VDD	ı	Power supply	
69	AO2	^	DOM suspent of quality data	111	NMD0			
70	AO1	0	PCM output of audio data	112	NMD15	.,	Data transfer line with DDAM	
71	AO0				NMD1	I/O	Data transfer line with DRAM	
72	GND	ı	Ground	114	NMD14			
73	DOUT1		Digital audio into face autout	115	GND	ı	Ground	
74	DOUT0	0	Digital audio interface output	116	NMD2			
75	SDA	_	Open	117	NMD13		Data transfer line with SDAM	
76	SCL	_	Open	118	NMD3	1/0	Data transfer line with DRAM	
77	VDD	I	Power supply	119	NMD12			
78	GND	ı	Ground	120	VDD	ı	Power supply	
79	DACCLK	0	Over-sampling operating clock output	121	NMD4			
80	DOCLK	0	PCM bit clock output	122	NMD11			
81	LRCLK	0	Clock output for discriminating the channel (L/R) of PCM audio data	123	NMD5	I/O	Data transfer line with DRAM	
82	HMODE0	ı	Setting pin of host interface operating mode	124	NMD10			

No.	Pin Name	I/O	Pin Function	No.	Pin Name	I/O	Pin Function		
125	GND	ı	Ground	167	MA5	0	Address line with SDRAM		
126	NMD6			168	GND	1	Ground		
127	NMD9			169	MA1				
128	NMD7	I/O	Data transfer line with DRAM	170	MA6	-			
129	NMD8			171	MA0	0	Address line with SDRAM		
130	VDD	ı	Power supply	172					
	NCAS0	0	CAS (Column Address Strobe) control line of	173		ı	Power supply		
	NWE	0	DRAM WE control line of DRAM		MA10	•	. спо. сарр.у		
			CAS (Column Address Strobe) control line of						
133	NCAS1	0	DRAM	175	MA8	0	Address line with SDRAM		
134	NRAS	0	RAS (Row Address Strobe) control line of DRAM	176	MA11		Address line with SDRAW		
135	GND	ı	Ground	177	MA9				
136	NMA9			178	GND	ı	Ground		
137	NMA8	0	Address line with DRAM	179	DCS	0	Chip select of SDRAM		
138	VDD	ı	Power supply	180	RAS	0	RAS (Row Address Strobe) control line of SDRAM		
139	NMA0			181	CAS	0	CAS (Column Address Strobe) control line of SDRAM		
140	NMA7	0	Address line with DRAM	182	VDD	I	Power supply		
141	NMA1			183	MCLK	0	Operation clock of SDRAM		
142	NMA6			184	GND	ı	Ground		
143	GND	ı	Ground	185	DWE	0	WE control line of SDRAM		
144	NMA2			186	DQMU	0	DQM control line of SDRAM Use for mask of upper byte output.		
145	NMA5	0	Address line with DRAM	187	DQML	0	DQM control line of SDRAM Use for mask of lower byte output.		
146	NMA3			188	VDD	I	Power supply		
147	NMA4			189	MD7				
148	VDD	ı	Power supply	190	MD8				
149	BD7				MD6	1/0	Data transfer line with SDRAM		
150	BD6		Bit stream input port	192	MD9	-			
151	GND	ı	Ground	193	GND	ı	Ground		
152	BD5			194	MD5				
153	BD4			195	MD10				
	BD3	ı	Bit stream input port	196	MD4	1/0	Data transfer line with SDRAM		
	BD2				MD11				
156	VDD	1	Power supply		VDD	1	Power supply		
	GND	· 	Ground		MD3				
	BD1	Ė			MD12				
	BD0	1	Bit stream input port		MD2	I/O	Data transfer line with SDRAM		
	BCLK	1	Strobe signal (clock) of BD port		MD13				
	BDEN	ı	Indicates the effective or invalid data which is sampled from BD port		GND	ı	Ground		
162	BDREQ	0	Output permission signal against to the device (channel decoder) which connecting to BD port	204	MD1				
163	VDD	ı	Power supply	205	MD14	I/O	Data transfer line with SDRAM		
164	MA3			206	MD0				
	MA4	0	Address line with SDRAM		MD15				
	MA2			208	VDD	1	Power supply		

■ PM0023AF (VQEB ASSY : IC101)

- VQE4 IC
- Block Diagram



No.	Pin Name	I/O	Pin Function
1	GND_00	_	Ground Connect to reference voltage (0V).
2	CLAMP	0	Clamp pulse output
3	RMA0		
4	RMA1		
5	RMA2		Register monitor address input
6	RMA3	'	Register monitor address input
7	RMA4		
8	RMA5		
9	DOC0		Output data control input
10	DOC1	•	Output data control input
11	VDD_00	_	Power supply Connect to 3.3V.
12	GND_01	ı	Ground Connect to reference voltage (0V).
13	CSB	ı	Chip select input for microcomputer interface L: select Schmitt input
14	SDATA	I	Serial data input for microcomputer interface Schmitt input
15	SCLK	I	Serial clock input for microcomputer interface Lead in SDATA at rising edge. Schmitt input
16	SRN	I	System reset input L: reset Schmitt input
17	TEST	I	Test mode cntrol input Connect to GND.
18	VCC_S0	_	Power supply Connect to 3.3V.
19	GND_S0	_	Ground Connect to reference voltage (0V).
20	XI	-	Connect a crystal resonator (27MHz) Connect to VCC (+3.3V) when using CLK (pin 23).
21	XO	0	Connect a crystal resonator (27MHz) Set to open when using CLK (pin 23).
22	GND_02	_	Ground Connect to reference voltage (0V).
23	CLKI	I	External clock (27MHz) input
24	VDD_01	_	Power supply Connect to 3.3V.

No.	Pin Name	I/O	Pin Function
25	VI0		4.00
26	VI1		(LSB)
27	VI2		
28	VI3		Video data input
29	VI4	ı	Input 8-bit parallel signal of CCIR-601 or CCIR-656 systems.
30	VI5		
31	VI6		(MCD)
32	VI7		(MSB)
33	GND_03	_	Ground Connect to reference voltage (0V).
34	NHS	I/O	Horizontal sync. signal input Outputs at Master mode and inputs at Slave mode (set with the register). Negative polarity
35	NVS	I/O	Vertical sync. signal input Outputs at Master mode and inputs at Slave mode (set with the register). Negative polarity
36	VDD_02	_	Power supply Connect to 3.3V.
37	DOC2	ı	Output data control input
	GND_04	_	Ground Connect to reference voltage (0V).
	MD00		(LSB)
	MD01	I/O	Data input and output for external memory with pull-up
	MD02		,
	MD03		
	VDD_03	_	Power supply Connect to 3.3V.
	GND_05	_	Ground Connect to reference voltage (0V).
	MD04		
	MD05	I/O	Data input and output for external memory with pull-up
	MD06		
	MD07		
	VDD_04	-	Power supply Connect to 3.3V.
	MD15	I/O	Data input and output for external memory with pull-up (MSB)
	MD14 MD13	1/0	Data input and output for output for output and manager with pull up
	MD12	I/O	Data input and output for external memory with pull-up
	VCC_S1	_	Power supply Connect to 3.3V.
	GND_S1	_	Ground Connect to reference voltage (0V).
	MD11		Ground Gormeet to reference voltage (6 v).
	MD10		
	MD09	I/O	Data input and output for external memory with pull-up
	MD08		
	GND_06	_	Ground Connect to reference voltage (0V).
	MCLK	0	Clock output for external memory
	MA09		·
	MA08	_	
	MA07	0	Address output for external memory
	MA06		
66	VDD_05	_	Power supply Connect to 3.3V.
67	GND_07	_	Ground Connect to reference voltage (0V).
68	MA05		Address output for external memory
69	MA04	0	Address output for external memory
70	MWEB	0	Writing control output for external memory
	MCASB	0	CAS output for external memory
72	MRASB	0	RAS output for external memory
73	MA11	0	Address output for external memory (MSB)
	MA10	0	Address output for external memory
75	MA00	0	Address output for external memory (LSB)

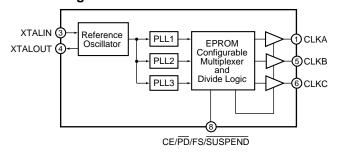
No.	Pin Name	I/O	Pin Function					
76	VDD_06	_	Power supply Connect to 3.3V.					
77	GND_08	_	Ground Connect to reference voltage (0V).					
78	MA01							
79	MA02	0	dress output for external memory					
80	MA03							
81	OSDHSY	0	orizontal sync. signal output for external OSD Negative polarity					
82	OSDVSY	0	Vertical sync. signal output for external OSD Negative polarity					
83	VDD_07	_	Power supply Connect to 3.3V.					
84	GND_09		Ground Connect to reference voltage (0V).					
85	OSDCLK	0	Clock output for external OSD					
86	GND_10		Ground Connect to reference voltage (0V).					
87	CTA2							
88	CTA1	1	OSD data input					
89	CTA0							
90	VCC_S2	_	Power supply Connect to 3.3V.					
91	GND_S2	_	Ground Connect to reference voltage (0V).					
92	BLD1		• • •					
93	BLD0	I	OSD blend control input					
	SCAN_T							
	SCAN_W	I	Control input for scan test Connect to 3.3V.					
96	DO0							
97	DO1							
98	DO2							
99	DO3							
100	DO4	_						
101	DO5	0	Data output					
102	DO6							
103	DO7							
104	DO8							
105	DO9							
106	RMO0	_						
107	RMO1	0	Register monitor output					
108	VDD_08	_	Power supply Connect to 3.3V.					
109	RMO2		D. circles and the section					
110	RMO3	0	Register monitor output					
111	VDD_09	_	Power supply Connect to 3.3V.					
112	GND_11	_	Ground Connect to reference voltage (0V).					
113	GND_AGB0	Α	Ground for Guard band Connect to reference voltage (0V).					
114	VDDDA_A0	Α	Power supply for A0 channel DAC Connect to 3.3V.					
115	DAO_A0	Α	DAC output of A0 channel Current output Connect a 330Ω resistor to GND.					
116	GNDDA_A0	Α	DAC ground of A0 channel Connect to reference voltage (0V).					
	DAO_A1	Α	DAC output of A1 channel Current output Connect a 330Ω resistor to GND.					
118	GNDDA_A1	Α	DAC ground of A1 channel Connect to reference voltage (0V).					
119	VDDDA_A1	Α	Power supply for A1 channel DAC Connect to 3.3V.					
120	CBU_A	Α	Connect a phase compensation capacitor for Group_A_DAC Connect a 0.1µF capacitor to GND.					
120	555_A	/1	Seminor a phase compensation supposition of Group_r_Dno					

No.	Pin Name	I/O	Pin Function
121	REXT_A	Α	Connect a reference resistor for Group_A_DAC Connect a 3.1 (3.0) kΩ resistor to GND>
122	CBL_A	Α	Connect a by-pass capacitor for Group_A_DAC Connect a 0.1µF capacitor to GND.
123	VDDDA_A2	Α	Power supply for A2 channel DAC Connect to 3.3V.
124	GNDDA_A2	Α	DAC ground of A2 channel Connect to reference voltage (0V).
125	DAO_A2	Α	DAC output of A2 channel Current output Connect a 330Ω resistor to GND.
126	VDDDA_B0	Α	Power supply for B0 channel DAC Connect to 3.3V.
127	GNDDA_B0	Α	DAC ground of B0 channel Connect to reference voltage (0V).
128	DAO_B0	Α	DAC output of B0 channel Current output Connect a 330Ω resistor to GND.
129	GNDDA_B1	Α	DAC ground of B1 channel Connect to reference voltage (0V).
130	DAO_B1	Α	DAC output of B1 channel Current output Connect a 330Ω resistor to GND.
131	VDDDA_B1	Α	Power supply for B1 channel DAC Connect to 3.3V.
132	CMU_B	Α	Connect a phase compensation capacitor for Group_B_DAC Connect a 0.1µF capacitor to GND.
133	REXT_B	Α	Connect a reference resistor for Group_B_DAC Connect a 3.1 (3.0) kΩ resistor to GND>
134	CBL_B	Α	Connect a by-pass capacitor for Group_B_DAC Connect a 0.1µF capacitor to GND.
135	GNDDA_B2	Α	DAC ground of B2 channel Connect to reference voltage (0V).
136	DAO_B2	Α	DAC output of B2 channel Current output Connect a 330Ω resistor to GND.
137	VDDDA_B2	Α	Power supply for B2 channel DAC Connect to 3.3V.
138	GND_AGB1	Α	Ground for Guard band Connect to reference voltage (0V).
139	RMO4		
140	RMO5	0	Register monitor output
141	RMO6	U	Integrater monitor output
142	RMO7		
143	VDD_10	_	Power supply Connect to 3.3V.
144	CLKO	0	Clock (27MHz) output

■ CY2081SL-655 (DVDM ASSY : IC21)

• Clock Generate IC

• Block Diagram



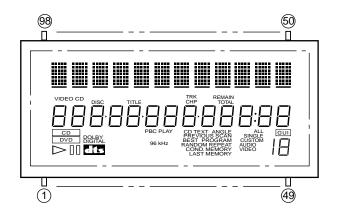
No.	Pin Name	Pin Function
1	CLKA	Configurable clock output
2	GND	Ground
3	XTALIN	Reference crystal input of external reference clock input
4	XTALOUT	Reference crystal feedback
5	CLKB	Configurable clock output
6	CLKC	Configurable clock output
7	VDD	Voltage supply
8	OE/PD/FS/ SUSPEND	Output control pin; either active-HIGH output enable, active-LOW power down, CLKA frequency select, or active-LOW suspend input

7.2.2 DISPLAY

■ VAW1052 (FLKY ASSY: V701)

• FL Display

Pin Assignment



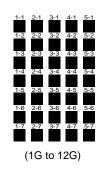
Pin Connection

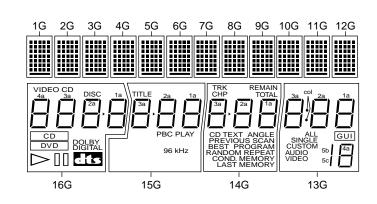
Pin No.	98	97	96	95	94	93	92	91	90	89	88	87	86
Connection	F1	F1	F1	F1	NP	NP	13G	12G	11G	10G	9G	8G	7G
Pin No.	85	84	83	82	81	80	79	78	77	76	75	74	73
Connection	6G	5G	4G	3G	2G	1G	NX	NX	NX	NX	NX	NX	NX
Pin No.	72	71	70	69	68	67	66	65	64	63	62	61	60
Connection	NX	NX	NX	P37	P36	P35	P34	P33	P32	P31	P30	P29	P28
Pin No.	59	58	57	56	55	54	53	52	51	50	49	48	47
Connection	P27	P26	P25	P24	NP	NP	F2	F2	F2	F2	F2	F2	F2
Pin No.	46	45	44	43	42	41	40	39	38	37	36	35	34
Connection	F2	-											-
	ΓZ	NP	NP	P23	P22	P21	P20	P19	P18	P17	P16	P15	P14
Pin No.	33	32	NP 31	30	P22 29	P21 28	P20 27	P19 26	P18 25	P17 24	P16 23	P15	P14 21
Pin No. Connection													
-	33	32	31	30	29	28	27	26	25	24	23	22	21
Connection	33 P13	32 P12	31 P11	30 NX	29 NX	28 NX	27 NX	26 NX	25 NX	24 NX	23 NX	22 NX	21 NX
Connection Pin No.	33 P13 20	32 P12 19	31 P11 18	30 NX 17	29 NX 16	28 NX 15	27 NX 14	26 NX 13	25 NX 12	24 NX 11	23 NX 10	22 NX 9	21 NX 8

Note (1) F1, F2 : Filament (4) DL : Datum Line (2) NP : No pin (5) 1G to 16G : Grid

(3) NX : No extend pin (6) IC : Internal connection

Grid Assignment





Anode Connection

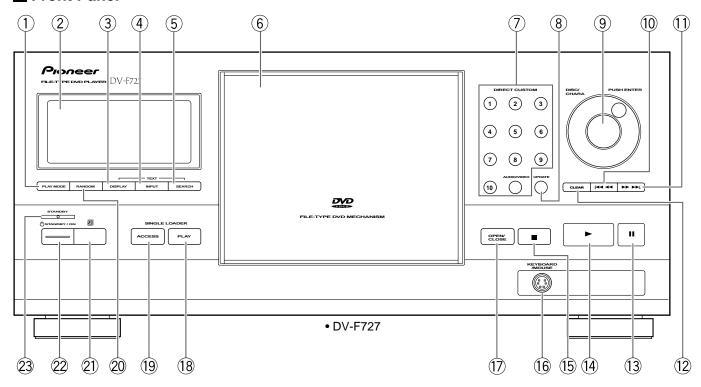
	1G to 12G	13G	14G	15G	16G
P1	1-1	VIDEO	LAST MEMORY	DOWN MIX	dts
P2	2-1	AUDIO	COND. MEMORY	LFE	00
P3	3-1	CUSTOM	REPEAT	0	\triangleright
P4	4-1	SINGLE	RANDOM	V-PART	DVD
P5	5-1	5d	PROGRAM	RS	CD
P6	1-2	3d	BEST	S	4d
P7	2-2	3e	SCAN	LS	4e
P8	3-2	3c	PREVIOUS	R	4c
P9	4-2	3g	ANGLE	С	4g
P10	5-2	3f	TEXT	٦	4f
P11	1-3	3b	CD	96kHz	4b
P12	2-3	3a	REMAIN	192kHz	4a
P13	3-3	col	TOTAL	PBC PLAY	DOLBY DIGITAL
P14	4-3	2d	3d	3d	3d
P15	5-3	2e	3e	3e	3e
P16	1-4	2c	3c	3c	3c
P17	2-4	2g	3g	3g	3g
P18	3-4	2f	3f	3f	3f

	40 1: 400	400	440	450	400
	1G to 12G	13G	14G	15G	16G
P19	4-4	2b	3b	3b	3b
P20	5-4	2a	3a	3a	3a
P21	1-5	5b, 5c	•	•	VIDEO CD
P22	2-5	1d	2d	2d	2d
P23	3-5	1e	2e	2e	2e
P24	4-5	1c	2c	2c	2c
P25	5-5	1g	2g	2g	2g
P26	1-6	1f	2f	2f	2f
P27	2-6	1b	2b	2b	2b
P28	3-6	1a	2a	2a	2a
P29	4-6	GUI	CHP	TITLE	>
P30	5-6	4d	1d	1d	1d
P31	1-7	4e	1e	1e	1e
P32	2-7	4c	1c	1c	1c
P33	3-7	4g	1g	1g	1g
P34	4-7	4f	1f	1f	1f
P35	5-7	4b	1b	1b	1b
P36		4a	1a	1a	1a
P37	_	ALL	TRK	GRP	DISC

8. PANEL FACILITIES AND SPECIFICATIONS

8.1 PANEL FACILITIES

Front Panel



1 PLAY MODE button

Press repeatedly to select one of the player's play modes. You can select either single play, ALL play, or custom play.

2 Display window

Displays system information.

③ TEXT DISPLAY button

Press repeatedly to display the disc title or artist name in the display window. When text information is included on the disc, this information will also be displayed.

(4) TEXT INPUT button

Press to start text input.

(5) TEXT SEARCH button

Press to search for a disc loaded in the rack by format, disc title, or artist .

6 Hood

The hood can be opened and closed by pressing **OPEN/ CLOSE**.

7 DIRECT CUSTOM buttons

Number buttons

Press the number button of the custom file you want to play. Playback of the discs in the selected custom file begins automatically.

AUDIO/VIDEO button

Press repeatedly to select the audio or video custom file bank.

8 UPDATE button

Use to update information on the discs loaded in the rack.

9 Selection dial

Rotate to select a disc number. When inputting text, rotate to select a character.

Enter button

Press to select the disc or enter text that has been selected using the selection dial.

10 Ida de (reverse) button

Press to go back to previous chapters/tracks. Press and hold to perform reverse playback scanning. When using the front panel to edit input text, use to move the position of the cursor.

11 ►► ►► (forward) button

Press to advance to chapters/tracks. Press and hold to perform fast-forward scanning. When using the front panel to edit input text, use to move the position of the cursor.

(12) CLEAR button

Use to cancel repeat and random playback, edit programs, and clear text entries.

(13) II (pause) button

Press during playback to pause. Press again to continue playback.

14 ► (play) button

Press to start disc playback.

(15) ■ (stop) button

Press to stop playback.

16 KEYBOARD/MOUSE connection jack

A PS/2 compatible keyboard or mouse can be connected to this jack for easy entry and editing of text information.

(17) OPEN/CLOSE button

Press to open and close the hood.

18 SINGLE LOADER PLAY button

Press to play the disc loaded in the single loader slot. This button can be used at any time, even if another disc is being played.

19 SINGLE LOADER ACCESS button

Press to have the hood open and the single loader slot brought to the front position.

20 RANDOM button

Press to start random playback.

21) Remote sensor

Point the remote control toward the remote sensor to operate the player.

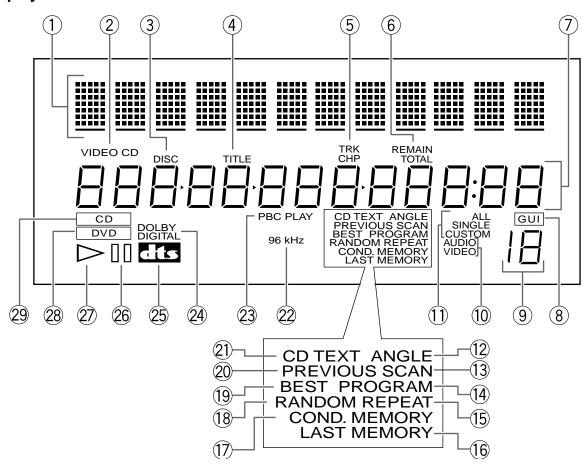
22 & STANDBY/ON button

Press to switch the player on or to put in standby.

23 STANDBY indicator

Indicates that the player is in standby, using a minimum amount of power to maintain system settings.

Display Window



1 Character display

Displays text information already recorded on the disc such as CD TEXT or DVD text, and also text manually input into the player.

(2) VIDEO CD indicator

Indicates a Video CD is currently selected in the player.

(3) DISC indicator

Indicates the disc number.

(4) TITLE indicator

Indicates a title number is being displayed.

5 TRK and CHP indicators

Indicates a chapter or track number is being displayed.

6 REMAIN and TOTAL indicators

REMAIN indicates the remaining playback time of a title or chapter/track is being displayed. TOTAL indicates the disc in the player is stopped and **DISPLAY** has been pressed.

7 Counter display

Displays title and chapter/track numbers, playback time, etc.

(8) GUI indicator

Indicates an on-screen menu operation is being performed.

(9) Custom file indicator

Indicates the number of the currently selected custom audio or video file.

10 AUDIO and VIDEO indicators

During custom play, indicate whether the current custom file is an audio (CD) or video (DVD or Video CD) custom file.

(1) ALL, SINGLE, and CUSTOM indicators

Indicates the current play mode.

(12) ANGLE indicator

Indicates Multi-Angle playback is in progress.

(13) SCAN indicator

Indicates a Hi-Lite scan is being performed.

(14) PROGRAM indicator

Indicates program playback is being performed.

15 REPEAT indicator

Indicates repeat playback is being performed.

16 LAST MEMORY indicator

Indicates the Last Memory location is registered in memory for the DVD currently playing.

(17) COND. MEMORY indicator

Indicates Condition Memory settings are memorized for the DVD currently playing.

18 RANDOM indicator

Indicates random playback is being performed.

19 BEST indicator

Indicates Best play is being performed.

20 PREVIOUS SCAN indicator

Indicates Previous scan is being performed.

21 CD TEXT indicator

Indicates the CD is recorded with CD TEXT information. When a DVD recorded with text is encountered, only the TEXT indicator lights.

22 96 kHz indicator

Indicates the DVD currently playing contains an audio signal with a sampling frequency of 96 kHz.

23 PBC PLAY indicator

Indicates PBC (playback control) playback of a Video CD.

24 DOLBY DIGITAL indicator

Indicates Dolby Digital audio playback.

25 DTS indicator

Indicates DTS audio playback.

26 (pause) indicator

Indicates playback is paused.

② ⊳ (play) indicator

Indicates a disc is playing.

28 DVD indicator

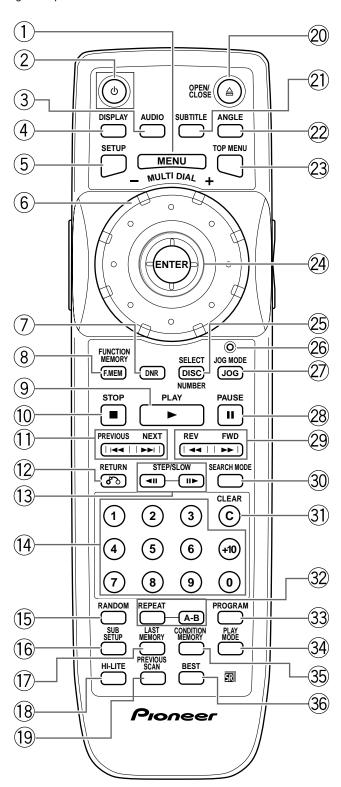
Indicates a DVD is currently selected in the player.

29 CD indicator

Indicates a CD is currently selected in the player.

■ Remote Control Unit

All of the command buttons on the remote control glow in the dark for easy control of the player even in the dark. Hold the unit under a light for optimal results.



(Buttons indicated with * are used for menu operation.)

1 MENU button*

Use to display or close the DVD menu screen .

② 🖒 (standby/on) button

Press to switch the player on or to put in standby.

3 AUDIO button

Press repeatedly to select one of the audio languages and/or audio formats programmed on a DVD.

For Video CD and CD, each press changes the audio output as follows.

(4) DISPLAY button

Press during playback to display statistical disc information. Press repeatedly to display different information.

(5) SETUP button*

Press to open and close the Setup screen.

6 MULTI DIAL

In Select Disc Number Mode, use to select one of the discs in the rack. In Jog Mode, use to control the rate and direction of playback.

7 DNR button

Press **DNR** to select a preprogrammed picture quality setting or to adjust various attributes of the video picture.

8 FUNCTION MEMORY button*

Press to incorporate a menu item into a shortcut list that is stored in memory and can be called up at any time.

9 PLAY ► button

Press to start disc playback.

10 STOP ■ button

Press to stop playback.

① PREVIOUS I◄◄/NEXT ►►I buttons*

During playback, press **PREVIOUS** | ◄◄ to go back to a previous chapter/track and **NEXT** ►► I to advance to the next chapter/track. Also use to display different sets of information in on-screen displays.

12 RETURN & button*

Use to go one menu back (current settings are maintained). Use **RETURN** • when you do not want to change the option setting in a menu.

13 STEP/SLOW **◄II/II** buttons

Press **STEP/SLOW II►** during playback to view slow playback. In pause mode, press **STEP/SLOW II►** to advance DVDs and Video CDs frame by frame and **STEP/SLOW ⊲II** to back up a DVD a few frames at a time.

14 Number buttons (1-9, 0, +10)*

Use to select a disc, perform direct title and chapter/track searches, and to input numerical values.

15 RANDOM button

Press to start random playback.

16 SUB SETUP button*

Press to open and close the player's Sub Setup screen.

17 LAST MEMORY button

Press **LAST MEMORY** during playback to set a Last Memory point .

18 HI-LITE button

Press to perform a highlight scan of all of the discs included in the current play mode.

19 PREVIOUS SCAN button

Press to a play highlight from up to 20 previously played discs in order from the most recently played disc.

20 OPEN/CLOSE ≜ button

Press to open or close the hood.

21) SUBTITLE button

Press repeatedly to select one of the subtitle languages programmed on a DVD or to turn the subtitles off.

22 ANGLE button

Press repeatedly to display different camera angles as recorded on some DVDs.

23 TOP MENU button*

Press to call up the top menu programmed on the DVD. Depending on the DVD, the top menu may be identical to the DVD menu.

24 Cursor control joystick*

Use to move the cursor through the options on menu screens and to change settings.

ENTER button*

Press to implement settings selected with the cursor control joystick or to set items highlighted in a menu.

25 SELECT DISC NUMBER button

Press to turn on the Select Disc Number Mode. The dial mode indicator lights green and turning **MULTI DIAL** selects the number of a disc loaded in the rack.

26 Dial mode indicator

Lights red when the player is in the Jog Mode and green when the player is in the Select Disc Number mode.

27 JOG MODE button

Press to put the player in the Jog Mode. The dial mode indicator lights red, and turning **MULTI DIAL** controls the playback of DVDs and Video CDs in both forward and reverse directions.

28 PAUSE II button

Press to pause playback of a disc. Press again to continue playback.

29 REV ◄◄/FWD ▶▶ (fast reverse/ fast forward) buttons

During playback, press **FWD** ►► to perform fast forward scanning and **REV** ◀ to perform fast reverse scanning.

30 SEARCH MODE button*

Press to perform a title, chapter/track or elapsed time search.

(31) CLEAR button

Press to cancel repeat and random playback, edit programs, and clear text entries.

32 REPEAT button

Press to repeat playback.

A-B button

Press at the beginning and end of the section you want to repeat or to mark a location you want to return to.

33 PROGRAM button

You can program discs, titles, chapters, or tracks to play back in a desired order.

34 PLAY MODE button

Press repeatedly to select one of the player's playback modes. You can select either single play, ALL play, or custom play mode.

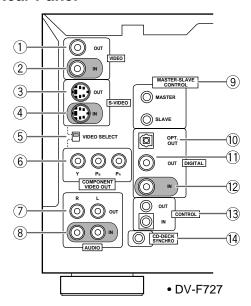
35 CONDITION MEMORY button

Press **CONDITION MEMORY** during DVD playback to memorize playback settings.

36 BEST button

Press during playback to add tracks or chapters to the Best play program. Press when the player is stopped to begin Best play.

Rear Panel



(1) VIDEO OUT jack

Connect the VIDEO OUT jack to the video input on a TV or monitor or to an AV component with video input capability. When using this jack, be sure to set VIDEO SELECT to the top position.

2 VIDEO IN jack

Connect the VIDEO IN jack to the video output of another DV-F727 player or another component with video output capability.

3 S-VIDEO OUT jack

If your TV or monitor has an S-video input, clear picture reproduction is possible by connecting the S-VIDEO OUT jack to your TV or monitor using a commercially available S-video cable. When using this jack, be sure to set VIDEO SELECT to the top position.

4 S-VIDEO IN jack

Connect the S-VIDEO IN jack to another DV-F727 player or component with S-video output capability.

(5) VIDEO SELECT switch

Use to set which output is used to output the video signals. Set to the top position for composite video and S-video output and to the bottom position for component video output.

6 COMPONENT VIDEO OUT jacks

If your TV, projection monitor, projector, or similar component has component video inputs, you can produce a high quality picture by connecting to the component video outputs on this unit. When using these jacks, be sure to set VIDEO SELECT to the bottom position.

7 AUDIO OUT jacks

Connect to the stereo audio inputs of a TV or stereo AV component. If you are connecting to an AV component that has both digital and analog input jacks for DVD player connection, it may be beneficial to make both connections.

8 AUDIO IN jacks

Connect to the analog audio outputs of another DV-F727 or component with audio output capability.

9 MASTER-SLAVE CONTROL jacks

Connect two DV-F727 players for singular control of both players and a total of 601 discs. Connect the MASTER jack on the player to be used as the "Master" to the SLAVE jack of a second "Slave" player using the supplied Master-Slave control cord. Do not attempt to make connections to other components using this jack.

10 DIGITAL OPT. OUT (optical) jack

Connect the DIGITAL OPT. OUT (optical) to the digital optical input of an AV component with a built-in decoder, etc. to output the digital audio signal recorded on discs.

11 DIGITAL OUT (coaxial) jack

Connect the DIGITAL OUT (coaxial) to the digital coaxial input of an AV component to output the digital audio signal recorded on discs

12 DIGITAL IN (coaxial) jack

Connect the DIGITAL IN (coaxial) jack of another DV-F727 player or another component with digital output (coaxial) capability.

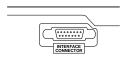
13 CONTROL jacks

Use to connect this player to another component bearing the Pioneer mark. This lets you control this unit as though it were a component in a system. Player operations are then performed by pointing the remote control at the component that the player is connected to.

(14) CD-DECK SYNCHRO jack

If you have a Pioneer cassette deck that has the CD-Deck synchro function, connect the CD-DECK SYNCHRO jacks on this unit to the identical jack on the cassette deck using a commercially available cord with a mini plug (2.5 mm dia. with no resistance).

• DV-F07 ONLY



INTERFACE CONNECTOR jack

This jack is intended for use by a system commander in the integration of some systems. Normally, this jack is not used.

8.2 SPECIFICATIONS

General
System File-type DVD system and Compact Disc digital audio system
Power requirements AC 120 V, 60 Hz
Power consumption21 W
Power consumption in standby mode less than 0.9 W
Weight (DV-F727) 7.7 kg (17 lbs.)
Weight (DV-F07)
Dimensions (DV-F727) 420 (W) x 434 (D) x 193 (H) mm
$(16^{9}/_{16})$ (W) x 17 $^{1}/_{16}$ (D) x 7 $^{5}/_{8}$ (H) in.)
(Not including protruding cables, etc.)
Dimensions (DV-F07) 460 (W) x 434 (D) x 194 (H) mm
$(18 \frac{1}{8} \text{ (W) x } 17 \frac{1}{16} \text{ (D) x } 7 \frac{5}{8} \text{ (H) in.)}$
(Not including protruding cables, etc.)
Operating temperature+5°C to +35°C (+36°F to +96°F)
Operating humidity 5% to 85% (no condensation)
S-Video input/output
Y (luminance) - Output level
C (color) - Output level
Jacks
Video input/output
Output level
Jacks RCA jack
Commonweat video cutavat
Component video output
(Y, P _B , P _R) Output levelΥ: 1.0 Vp-p (75 Ω)
P _B , P _R : 0.7 Vp-p (75 Ω)
Jacks RCA jack
· · · · · · · · · · · · · · · · · · ·
Audio input/output
Output level
During audio output
Number of channels
Jacks
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
Digital audio characteristics
Frequency response 4 Hz to 44 kHz (DVD fs: 96 kHz)
S/N ratio more than 115 dB
Dynamic range more than 102 dB
Total harmonic distortion
Wow and flutter Limit of measurement
(±0.001% W. PEAK) or lower
Digital output
Optical digital output Optical digital jack
Coaxial digital input/output RCA jack
Control jacks
Control input/output Monaural minijack (3.5 ø)
MASTER-SLAVE Stereo minijack (3.5 ø)
- , ,

Other jacks

CD-DECK SYNCHRO	jack	Minijack (2.5	ø)
KEYBOARD/MOUSE i	jack	6-pin DIN connection ja	ack

Accessories

Remote control unit	. 1
AA (R6P) dry cell batteries	. 2
Audio cord	. 1
Video cord	. 1
Master-Slave control cord	. 1
Operating Instructions	. 1
Warranty Card (U.S. and Canadian models on	ly)

Note

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